

# Silicides in VLSI

- Almost all metals in periodic table react with Si to silicides with general formula ' $M_xSi_y$ '.
- Silicides have 'metal-like' properties.



# Silicides For Vlsi Apps

**Badih El-Kareh, Lou N. Hutter**

A red circular graphic with a gradient, appearing as a stylized arrow or a partial circle, located to the right of the authors' names.

## **Silicides For Vlsi Apps:**

**Silicides for VLSI Applications** S. P. Murarka, 1983 Most of the subject matter of this book has previously been available only in the form of research papers and review articles I have not attempted to refer to all the published papers The reader may find it advantageous to refer to the references listed Semiconducting Silicides Victor E. Borisenko, 2013-03-07 Semiconductors are well known as the main materials of modern solid state electronics They have held the attention of researchers and engineers since the brilliant invention of the semiconductor transistor by Bardeen Brattain and V V Shockley in the middle of the 20th century Silicon germanium IIIIB and IIIB compounds have been widely used in discrete semiconductor devices and microelectronic and nanoelectronic integrated systems Each of these materials has separately met specific physical and technological requirements to provide formation of solid state structures with the best electronic or optical performance However attempts to combine them within integrated circuit appear to be ineffective or even technologically impossible Thus material and related technological compatibilities are important for further progress particularly in microelectronics optoelectronics and nanoelectronics This stimulates an increasing interest in silicides and silicon germanium alloys which provide new prospects for silicon based integration Elements from the Periodic Table form more than 180 silicides which are chemical compounds of silicon with different metals Most of them except the silicides of lanthanides and actinides are shown in Table 1 Along with appropriate compatibility with silicon and easy formation by silicidation in a metal silicon couple silicides are characterized by high thermal stability and resistance to oxidation The majority of them are metallic and have low resistivity Exactly metallic silicides were first employed for interconnections gates in MOS structures ohmic contacts and Schottky barriers in silicon integrated circuits For a comprehensive overview of their properties and general features of the formation technology the reader may address the books and reviews 1 10

**Handbook of Semiconductor Manufacturing Technology** Yoshio Nishi, Robert Doering, 2000-08-09 The Handbook of Semiconductor Manufacturing Technology describes the individual processes and manufacturing control support and infrastructure technologies of silicon based integrated circuit manufacturing many of which are also applicable for building devices on other semiconductor substrates Discussing ion implantation rapid thermal processing photomask fabrication chip testing and plasma etching the editors explore current and anticipated equipment devices materials and practices of silicon based manufacturing The book includes a foreword by Jack S Kilby co-winner of the Nobel Prize in Physics 2000 for his part in the invention of the integrated circuit Advanced Nanoscale ULSI Interconnects: Fundamentals and Applications Yosi Shacham-Diamand, Tetsuya Osaka, Madhav Datta, Takayuki Ohba, 2009-09-19 In Advanced ULSI interconnects fundamentals and applications we bring a comprehensive description of copper based interconnect technology for ultra large scale integration ULSI technology for integrated circuit IC application In integrated circuit technology is the base for all modern electronics systems You can find electronics systems today everywhere from toys and home appliances to airplanes and space

shuttles Electronics systems form the hardware that together with software are the bases of the modern information society The rapid growth and vast exploitation of modern electronics system create a strong demand for new and improved electronic circuits as demonstrated by the amazing progress in the field of ULSI technology This progress is well described by the famous Moore's law which states in its most general form that all the metrics that describe integrated circuit performance e.g speed number of devices chip area improve exponentially as a function of time For example the number of components per chip doubles every 18 months and the critical dimension on a chip has shrunk by 50% every 2 years on average in the last 30 years This rapid growth in integrated circuit technology results in highly complex integrated circuits with an increasing number of interconnects on chips and between the chip and its package The complexity of the interconnect network on chips involves an increasing number of metal lines per interconnect level more interconnect levels and at the same time a reduction in the interconnect line critical dimensions

Advanced Silicon Materials for Photovoltaic Applications Sergio Pizzini, 2012-06-07 Today the silicon feedstock for photovoltaic cells comes from processes which were originally developed for the microelectronic industry It covers almost 90% of the photovoltaic market with mass production volume at least one order of magnitude larger than those devoted to microelectronics However it is hard to imagine that this kind of feedstock extremely pure but heavily penalized by its high energy cost could remain the only source of silicon for a photovoltaic market which is in continuous expansion and which has a cumulative growth rate in excess of 30% in the last few years Even though reports suggest that the silicon share will slowly decrease in the next twenty years finding a way to manufacture a specific solar grade feedstock in large quantities at a low cost while maintaining the quality needed still remains a crucial issue Thin film and quantum confinement based silicon cells might be a complementary solution Advanced Silicon Materials for Photovoltaic Applications has been designed to describe the full potentialities of silicon as a multipurpose material and covers Physical chemical and structural properties of silicon Production routes including the promise of low cost feedstock for PV applications Defect engineering and the role of impurities and defects Characterization techniques and advanced analytical techniques for metallic and non metallic impurities Thin film silicon and thin film solar cells Innovative quantum effects and 3rd generation solar cells With contributions from internationally recognized authorities this book gives a comprehensive analysis of the state of the art of process technologies and material properties essential for anyone interested in the application and development of photovoltaics

Electronics (fundamentals And Applications) D. Chattopadhyay, 2006 The Book Is Meant For The Students Pursuing A Beginners Course In Electronics Current Syllabi Of Basic Electronics Included In Physics Honours Curriculum Of Different Universities And Those Offered In Various Engineering And Technical Institutions Have Been Consulted In Preparing The Material Contained Herein In 22 Chapters The Book Deals With Formation Of Energy Bands In Solids Electron Emission From Solid Surfaces Vacuum Tubes Properties Of Semiconductors Pn Junction Diodes Rectifiers Voltage Multipliers Clipping And Clamping Circuits Bipolar Junction

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### **Fundamentals of Semiconductor Processing Technology**

Badih El-Kareh, Lou N. Hutter, 2012-12-06 The drive toward new semiconductor technologies is intricately related to market demands for cheaper smaller faster and more reliable circuits with lower power consumption The development of new processing tools and technologies is aimed at optimizing one or more of these requirements This goal can however only be achieved by a concerted effort between scientists engineers technicians and operators in research development and manufacturing It is therefore important that experts in specific disciplines such as device and circuit design understand the principle capabilities and limitations of tools and processing technologies It is also important that those working on specific unit processes such as lithography or hot processes be familiar with other unit processes used to manufacture the product Several excellent books have been published on the subject of process technologies These texts however cover subjects in too much detail or do not cover topics important to modern technologies This book is written with the need for a bridge between different disciplines in mind It is intended to present to engineers and scientists those parts of modern processing technologies that are of greatest importance to the design and manufacture of semiconductor circuits The material is presented with sufficient detail to understand and analyze interactions between processing and other semiconductor disciplines such as design of devices and circuits their electrical parameters reliability and yield

*Silicide Technology for Integrated Circuits* Institution of Electrical Engineers, 2004-12-21 This is the first book to provide guidance on the development and application of metal silicide technology as it emerges from the scientific to the prototype and manufacturing stages Other key topics covered are fundamentals present and future silicide technology for Si based devices and characterisation methods Suitable for engineers and students in microelectronics

**Semiconductor Silicon**, 1986

Rapid Thermal Processing and beyond: Applications in Semiconductor Processing Wielfried Lerch, Jürgen Niess, 2008-03-24 Special topic volume with invited papers only

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## **Table of Contents Silicides For Vlsi Apps**

1. Understanding the eBook Silicides For Vlsi Apps
  - The Rise of Digital Reading Silicides For Vlsi Apps
  - Advantages of eBooks Over Traditional Books
2. Identifying Silicides For Vlsi Apps
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Silicides For Vlsi Apps
  - User-Friendly Interface
4. Exploring eBook Recommendations from Silicides For Vlsi Apps
  - Personalized Recommendations
  - Silicides For Vlsi Apps User Reviews and Ratings
  - Silicides For Vlsi Apps and Bestseller Lists
5. Accessing Silicides For Vlsi Apps Free and Paid eBooks
  - Silicides For Vlsi Apps Public Domain eBooks
  - Silicides For Vlsi Apps eBook Subscription Services
  - Silicides For Vlsi Apps Budget-Friendly Options

6. Navigating Silicides For Vlsi Apps eBook Formats
  - ePub, PDF, MOBI, and More
  - Silicides For Vlsi Apps Compatibility with Devices
  - Silicides For Vlsi Apps Enhanced eBook Features
7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Silicides For Vlsi Apps
  - Highlighting and Note-Taking Silicides For Vlsi Apps
  - Interactive Elements Silicides For Vlsi Apps
8. Staying Engaged with Silicides For Vlsi Apps
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Silicides For Vlsi Apps
9. Balancing eBooks and Physical Books Silicides For Vlsi Apps
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Silicides For Vlsi Apps
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
11. Cultivating a Reading Routine Silicides For Vlsi Apps
  - Setting Reading Goals Silicides For Vlsi Apps
  - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Silicides For Vlsi Apps
  - Fact-Checking eBook Content of Silicides For Vlsi Apps
  - Distinguishing Credible Sources
13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
  - Integration of Multimedia Elements

- Interactive and Gamified eBooks

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