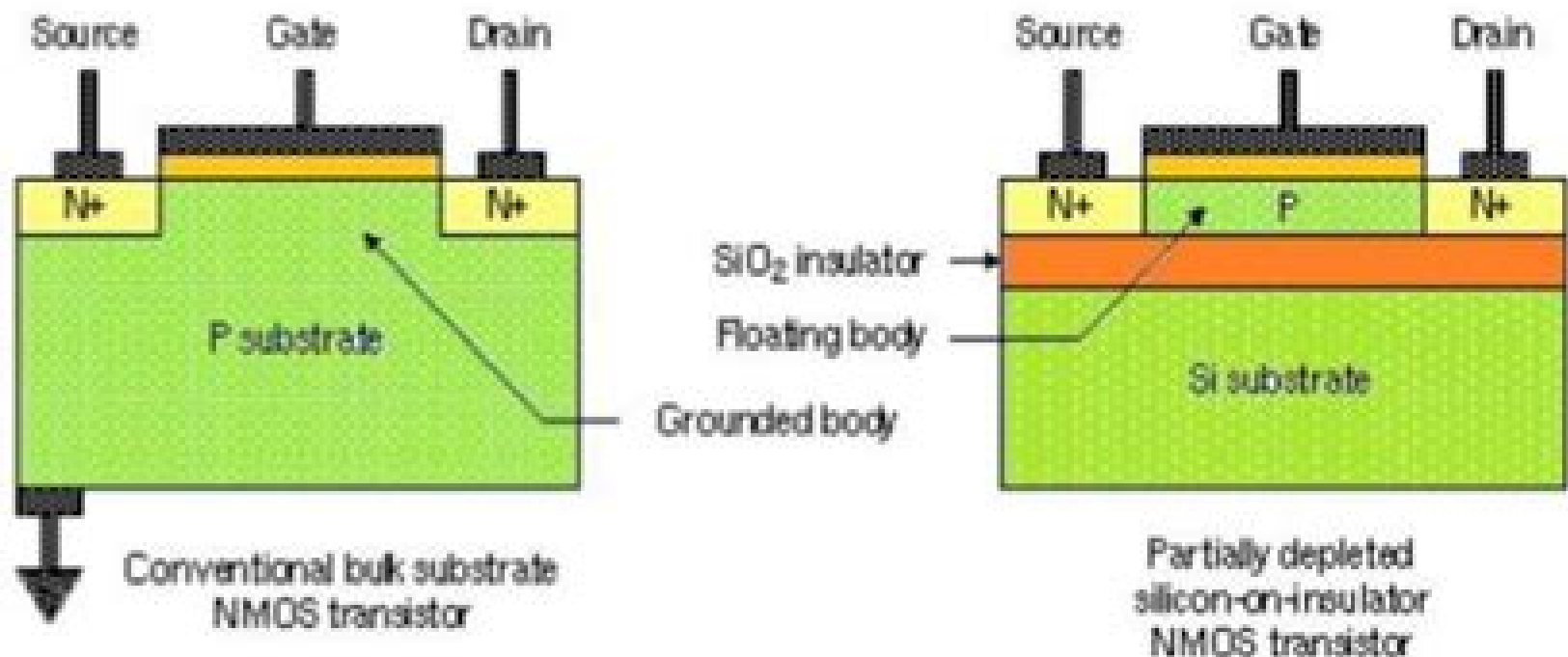


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Silicon On Insulator

J. P. Colinge



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Silicon-on-insulator Technology and Devices XI
Electrochemical Society. Meeting, 2003 ***Fabrication of Silicon on Insulator(SOI) Wafers Using Ion Implantation***
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in SOI technology **Silicon-on-insulator Technology and Devices** Peter L. F. Hemment, Sorin Cristoloveanu, 1999

Silicon-on-Insulator S. Furukawa, 1985-05-31 This volume contains papers presented during the US Japan seminar on Solid Phase Epitaxy and Interface Kinetics held in Oiso Japan June 20-24 1983 This seminar was co sponsored by the National Science Foundation and Japan Society for the Promotion of Science and co chaired by Professor S Furukawa Tokyo Institute of Technology and Professor J W Mayer Cornell University Extensive topics such as solid phase epitaxy growth mechanisms and interface kinetics silicon on insulator structures silicide on Si structures novel nanometer and layered devices and so on were discussed and more than 50 papers were presented Most papers were original ones with brief reviews added for the convenience of the readers at the editor's request The editor classified these papers into two groups and compiled two volumes Silicon on Insulator SOI Its Technology and Applications and Layered Structures and Interface Kinetics Their Technology and Applications This volume mainly contains the papers related to epitaxial growth of metal insulator and semiconductor films growth mechanisms interface kinetics properties and applications of silicide films and novel nanometer and layered devices These papers offer basic properties of the layered structures and possibility of various applications of the structures to present and future semiconductor devices The editor is indebted to our fellow contributors who agreed to participate in publishing the proceedings of the seminar to Japanese principal participants of the seminar for encouraging him to have the seminar and to compile these volumes to Professor H Ishiwara for his secretarial work throughout the seminar and the publication Development and Testing of a Silicon-On-Insulator (SOI) Technology Process J. P. Colinge, 1993

Electrical Characterization of Silicon-on-Insulator Materials and Devices Sorin Cristoloveanu, Sheng Li, 1995-06-30 Silicon on Insulator is more than a technology more than a job and more than a venture in microelectronics it is something different and refreshing in device physics This book recalls the activity and enthusiasm of our SOI groups Many contributing students have since then disappeared from the SOI horizon Some of them believed that SOI was the great love of their scientific lives others just considered SOI as a fantastic LEGO game for adults We thank them all for kindly letting us imagine that we were guiding them This book was very necessary to many people SOI engineers will certainly be happy indeed if the performance of their SOI components is not always outstanding they can now safely incriminate the relations given in the book rather than their process Martine Gunter and Y S Chang can contemplate at last the amount of work they did with the figures Our SOI accomplices already know how much we borrowed from their expertise and would find it indecent to have their detailed contributions listed Jean Pierre and Dimitris incited the book while sharing their experience in the reliability of floating bodies Our families and friends now realize the SOI capability of dielectrically isolating us for about two years in a BOX Our kids encouraged us to start writing Our wives definitely gave us the courage to stop writing They had a hard time fighting the symptoms of a rapidly developing SOI allergy

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