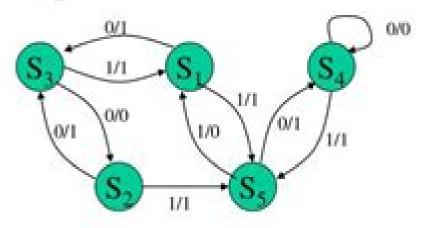
# Synthesis of sequential circuits

- · Steps in synthesis of a sequential circuit
  - Specify the FSM (state table or state diagram)
  - Minimize the states
  - State assignment/encoding
- Specification:



PS	x	NS	Z
$S_1$	0	S3	1
$S_1$	1	S <sub>5</sub>	1
S <sub>2</sub>	0	S <sub>3</sub>	1
$S_2$	1	S <sub>5</sub>	1
S <sub>3</sub>	0	S <sub>2</sub>	0
$S_3$	1	S	1
S <sub>4</sub>	0	S <sub>4</sub>	0
$S_4$	1	Ss	1
S <sub>5</sub>	0	S <sub>4</sub>	1
S <sub>5</sub>	1	S	0

## **Sequential Circuit Synthesis State Assignment Aspects**

**JE Gale** 

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