

# SEQUENTIAL LOGIC TESTING AND VERIFICATION

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**Kluwer Academic Publishers**

# Sequential Logic Testing And Verification

**Richard Zurawski**



## **Sequential Logic Testing And Verification:**

**Sequential Logic Testing and Verification** Abhijit Ghosh, Srinivas Devadas, A. Richard Newton, 1991-11-30

**Sequential Logic Testing and Verification** Abhijit Ghosh, Srinivas Devadas, A. Richard Newton, 2012-12-06 In order to design and build computers that achieve and sustain high performance it is essential that reliability issues be considered carefully. The problem has several aspects. Certainly considering reliability implies that an engineer must be able to analyze how design decisions affect the incidence of failure. For instance in order design reliable integrated circuits it is necessary to analyze how decisions regarding design rules affect the yield i.e. the percentage of functional chips obtained by the manufacturing process. Of equal importance in producing reliable computers is the detection of failures in its Very Large Scale Integrated VLSI circuit components caused by errors in the design specification implementation or manufacturing processes. Design verification involves the checking of the specification of a design for correctness prior to carrying out an implementation. Implementation verification ensures that the manual design or automatic synthesis process is correct i.e. the mask level description correctly implements the specification. Manufacture test involves the checking of the complex fabrication process for correctness i.e. ensuring that there are no manufacturing defects in the integrated circuit. It should be noted that all the above verification mechanisms deal not only with verifying the functionality of the integrated circuit but also its performance.

Testing and Diagnosis of VLSI and ULSI F. Lombardi, M.G. Sami, 2012-12-06 This volume contains a collection of papers presented at the NATO Advanced Study Institute on Testing and Diagnosis of VLSI and ULSI held at Villa Olmo Como Italy June 22 July 3 1987. High Density technologies such as Very Large Scale Integration VLSI Wafer Scale Integration WSI and the not so far promises of Ultra Large Scale Integration ULSI have exasperated the problems associated with the testing and diagnosis of these devices and systems. Traditional techniques are fast becoming obsolete due to unique requirements such as limited controllability and observability increasing execution complexity for test vector generation and high cost of fault simulation to mention just a few. New approaches are imperative to achieve the highly sought goal of the three months turn around cycle time for a state of the art computer chip. The importance of testing and diagnostic processes is of primary importance if costs must be kept at acceptable levels. The objective of this NATO ASI was to present analyze and discuss the various facets of testing and diagnosis with respect to both theory and practice. The contents of this volume reflect the diversity of approaches currently available to reduce test and diagnosis time. These approaches are described in a concise yet clear way by renowned experts of the field. Their contributions are aimed at a wide readership the uninitiated researcher will find the tutorial chapters very rewarding. The expert will be introduced to advanced techniques in a very comprehensive manner.

**Computer Logic, Testing, and Verification** John Paul Roth, 1980 *Principles of Testing Electronic Systems* Samiha Mourad, Yervant Zorian, 2000-07-25 A pragmatic approach to testing electronic systems. As we move ahead in the electronic age rapid changes in technology pose an ever increasing number of challenges in testing.

electronic products Many practicing engineers are involved in this arena but few have a chance to study the field in a systematic way learning takes place on the job By covering the fundamental disciplines in detail Principles of Testing Electronic Systems provides design engineers with the much needed knowledge base Divided into five major parts this highly useful reference relates design and tests to the development of reliable electronic products shows the main vehicles for design verification examines designs that facilitate testing and investigates how testing is applied to random logic memories FPGAs and microprocessors Finally the last part offers coverage of advanced test solutions for today s very deep submicron designs The authors take a phenomenological approach to the subject matter while providing readers with plenty of opportunities to explore the foundation in detail Special features include An explanation of where a test belongs in the design flow Detailed discussion of scan path and ordering of scan chains BIST solutions for embedded logic and memory blocks Test methodologies for FPGAs A chapter on testing system on a chip Numerous references

**Efficient Branch and Bound Search with Application to Computer-Aided Design** Xinghao Chen, Michael L. Bushnell, 2012-12-06 Branch and bound search has been known for a long time and has been widely used in solving a variety of problems in computer aided design CAD and many important optimization problems In many applications the classic branch and bound search methods perform duplications of computations or rely on the search decision trees which keep track of the branch and bound search processes In CAD and many other technical fields the computational cost of constructing branch and bound search decision trees in solving large scale problems is prohibitive and duplications of computations are intolerable Efficient branch and bound methods are needed to deal with today s computational challenges Efficient branch and bound methods must not duplicate computations Efficient Branch and Bound Search with Application to Computer Aided Design describes an efficient branch and bound method for logic justification which is fundamental to automatic test pattern generation ATPG redundancy identification logic synthesis minimization verification and other problems in CAD The method is called justification equivalence based on the observation that justification processes may share identical subsequent search decision sequences With justification equivalence duplication of computations is avoided in the dynamic branch and bound search process without using search decision trees Efficient Branch and Bound Search with Application to Computer Aided Design consists of two parts The first part containing the first three chapters provides the theoretical work The second part deals with applications particularly ATPG for sequential circuits This book is particularly useful to readers who are interested in the design and test of digital circuits

**Digital Logic Testing and Simulation** Alexander Miczo, 2003-10-10 Your road map for meeting today s digital testing challenges Today digital logic devices are common in products that impact public safety including applications in transportation and human implants Accurate testing has become more critical to reliability safety and the bottom line Yet as digital systems become more ubiquitous and complex the challenge of testing them has become more difficult As one development group designing a RISC stated the work required to test a chip of this size approached the

amount of effort required to design it. A valued reference for nearly two decades, *Digital Logic Testing and Simulation* has been significantly revised and updated for designers and test engineers who must meet this challenge. There is no single solution to the testing problem. Organized in an easy-to-follow sequential format, this Second Edition familiarizes the reader with the many different strategies for testing and their applications and assesses the strengths and weaknesses of the various approaches. The book reviews the building blocks of a successful testing strategy and guides the reader on choosing the best solution for a particular application. *Digital Logic Testing and Simulation* Second Edition covers such key topics as Binary Decision Diagrams (BDDs) and cycle-based simulation, Tester architectures, Standard Test Interface Language (STIL), Practical algorithms written in a Hardware Design Language (HDL), Fault tolerance, Behavioral Automatic Test Pattern Generation (ATPG), The development of the Test Design Expert (TDX), the many obstacles encountered and lessons learned in creating this novel testing approach. Up-to-date and comprehensive, *Digital Logic Testing and Simulation* is an important resource for anyone charged with pinpointing faulty products and assuring quality, safety, and profitability.

**Design of Embedded Control Systems** Marian Andrzej Adamski, Andrei Karatkevich, Marek Wegrzyn, 2006-11-22. A set of original results in the field of high-level design of logical control devices and systems is presented in this book. These concern different aspects of such important and long-term design problems including the following, which seem to be the main ones: First, the behavior of a device under design must be described properly and some adequate formal language should be chosen for that. Second, effective algorithms should be used for checking the prepared description for correctness for its syntactic and semantic verification at the initial behavior level. Third, the problem of logic circuit implementation must be solved using some concrete technological base of efficient methods of logic synthesis; test and verification should be developed for that. Fourth, the task of the communication between the control device and controlled objects and maybe between different control devices waits for its solution. All these problems are hard enough and cannot be successfully solved without efficient methods and algorithms oriented toward computer implementation. Some of these are described in this book. The languages used for behavior description have been descended usually from two well-known abstract models which became classic Petri nets and finite state machines (FSMs). Anyhow, more detailed versions are developed and described in the book which enable to give more complete information concerning specific qualities of the regarded systems. For example, the model of parallel automaton is presented which, unlike the conventional finite automaton, can be placed simultaneously into several places called partial. As a base for circuit implementation of control algorithms, FPGA is accepted in majority of cases.

**Design Automation for Timing-Driven Layout Synthesis** S. Sapatnekar, Sung-Mo (Steve) Kang, 2012-12-06. Moore's law, Noy 77, which predicted that the number of devices integrated on a chip would be doubled every two years, was accurate for a number of years. Only recently has the level of integration begun to slow down somewhat due to the physical limits of integration technology. Advances in silicon technology have allowed designers to integrate more than a few million transistors on a chip, even a whole system of

moderate complexity can now be implemented on a single chip To keep pace with the increasing complexity in very large scale integrated VLSI circuits the productivity of chip designers would have to increase at the same rate as the level of integration Without such an increase in productivity the design of complex systems might not be achievable within a reasonable time frame The rapidly increasing complexity of VLSI circuits has made design automation an absolute necessity since the required increase in productivity can only be accomplished with the use of sophisticated design tools Such tools also enable designers to perform trade off analyses of different logic implementations and to make well informed design decisions

*Logic Verification and Test Generation for VLSI Circuits* Ruey-sing Wei, 1986

**Real-World Applications of Evolutionary Computing** Stefano Cagnoni, Riccardo Poli, George D. Smith, David Corne, Martin Oates, Emma Hart, Pier L. Lanzi, Egbert J. Willem, Yun Li, Ben Paechter, Terence C. Fogarty, 2003-07-31 The increasingly active field of Evolutionary Computation EC provides valuable tools inspired by the theory of natural selection and genetic inheritance to problem solving machine learning and optimization in many real world applications Despite some early intuitions about EC that can be dated back to the invention of computers and a better formal definition of EC made in the 1960s the quest for real world applications of EC only began in the late 1980s The dramatic increase in computer performances in the last decade of the 20th century gave rise to a positive feedback process EC techniques became more and more applicable stimulating the growth of interest in their study and allowing in turn new powerful EC paradigms to be devised In parallel with new theoretical results the number of fields to which EC is being applied is increasing day by day along with the complexity of applications and application domains In particular industrially relevant fields such as signal and image processing computer vision pattern recognition industrial control telecommunication scheduling and timetabling and aerospace engineering are employing EC techniques to solve complex real world problems

**Embedded Systems**

**Handbook 2-Volume Set** Richard Zurawski, 2018-10-08 During the past few years there has been an dramatic upsurge in research and development implementations of new technologies and deployments of actual solutions and technologies in the diverse application areas of embedded systems These areas include automotive electronics industrial automated systems and building automation and control Comprising 48 chapters and the contributions of 74 leading experts from industry and academia the Embedded Systems Handbook Second Edition presents a comprehensive view of embedded systems their design verification networking and applications The contributors directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews exploring new developments deployments and trends To accommodate the tremendous growth in the field the handbook is now divided into two volumes New in This Edition Processors for embedded systems Processor centric architecture description languages Networked embedded systems in the automotive and industrial automation fields Wireless embedded systems Embedded Systems Design and Verification Volume I of the handbook is divided into three sections It begins with a brief introduction to embedded

systems design and verification The book then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Networked Embedded Systems Volume II focuses on selected application areas of networked embedded systems It covers automotive field industrial automation building automation and wireless sensor networks This volume highlights implementations in fast evolving areas which have not received proper coverage in other publications Reflecting the unique functional requirements of different application areas the contributors discuss inter node communication aspects in the context of specific applications of networked embedded systems

**Boolean Models and Methods in Mathematics, Computer Science, and Engineering** Yves Crama, Peter L. Hammer, 2010-06-28 A collection of papers written by prominent experts that examine a variety of advanced topics related to Boolean functions and expressions

Electronic CAD Frameworks Timothy J. Barnes, David Harrison, A. Richard Newton, Rick L. Spickelmier, 2012-12-06 When it comes to frameworks the familiar story of the elephant and the six blind philosophers seems to apply As each philosopher encountered a separate part of the elephant each pronounced his considered but flawed judgement One blind philosopher felt a leg and thought it a tree Another felt the tail and thought he held a rope Another felt the elephant's flank and thought he stood before a wall We're supposed to learn about snap judgements from this allegory but its author might well have been describing design automation frameworks For in the reality of today's product development requirements a framework must be many things to many people xiv CAD Frameworks Integration Technology for CAD As the authors of this book note framework design is an optimization problem Somehow it has to be both a superior rope for one and a tremendous tree for another Somehow it needs to provide a standard environment for exploiting the full potential of computer aided engineering tools And somehow it has to make real such abstractions as interoperability and interchangeability For years we've talked about a framework as something that provides application oriented services just as an operating system provides system level support And for years that simple statement has hid the tremendous complexity of actually providing those services

**Wavelet Theory and Its Applications** Randy K. Young, 2012-12-06 The continuous wavelet transform has deep mathematical roots in the work of Alberto P Calderon His seminal paper on complex method of interpolation and intermediate spaces provided the main tool for describing function spaces and their approximation properties The Calderon identities allow one to give integral representations of many natural operators by using simple pieces of such operators which are more suited for analysis These pieces which are essentially spectral projections can be chosen in clever ways and have proved to be of tremendous utility in various problems of numerical analysis multidimensional signal processing video data compression and reconstruction of high resolution images and high quality speech A proliferation of research papers and a couple of books written in English there is an earlier book written in French have emerged on the subject These books so far are written by specialists for

specialists with a heavy mathematical flavor which is characteristic of the Calderon Zygmund theory and related research of Duffin Schaeffer Daubechies Grossman Meyer Morlet Chui and others Randy Young s monograph is geared more towards practitioners and even non specialists who want and probably should be cognizant of the exciting proven as well as potential benefits which have either already emerged or are likely to emerge from wavelet theory *Clinical Engineering Handbook* Joseph Dyro,2004-09-15 As the biomedical engineering field expands throughout the world clinical engineers play an evermore important role as translators between the medical engineering and business professions They influence procedure and policy at research facilities universities as well as private and government agencies including the Food and Drug Administration and the World Health Organization The profession of clinical engineering continues to seek its place amidst the myriad of professionals that comprise the health care field The Clinical Engineering Handbook meets a long felt need for a comprehensive book on all aspects of clinical engineering that is a suitable reference in hospitals classrooms workshops and governmental and non governmental organization The Handbook s thirteen sections address the following areas Clinical Engineering Models of Clinical Engineering Practice Technology Management Safety Education and Training Design Manufacture and Evaluation and Control of Medical Devices Utilization and Service of Medical Devices Information Technology and Professionalism and Ethics The Clinical Engineering Handbook provides the reader with prospects for the future of clinical engineering as well as guidelines and standards for best practice around the world From telemedicine and IT issues to sanitation and disaster planning it brings together all the important aspects of clinical engineering Clinical Engineers are the safety and quality facilitators in all medical facilities The most definitive comprehensive and up to date book available on the subject of clinical engineering Over 170 contributions by leaders in the field of clinical engineering

**Technical Report - Jet Propulsion Laboratory, California Institute of Technology** Jet Propulsion Laboratory (U.S.),1966 **Digital BiCMOS Integrated Circuit Design** Sherif H.K. Embabi,Abdellatif Bellaouar,Mohamed I. Elmasry,2012-12-06 Digital BiCMOS Integrated Circuit Design is the first book devoted entirely to the analysis and design of digital BiCMOS integrated circuits BiCMOS Integrated Circuit Design also reviews CMOS and CML integrated circuit design The application of BiCMOS in the design of digital subsystems e g adders multipliers RAMs and PLAs is addressed The book also introduces the reader to IC process technology CMOS bipolar and BiCMOS The modeling of both the bipolar and MOS devices are covered Many process device circuit design issues are discussed Digital BiCMOS Integrated Circuit Design can be used by engineers researchers graduate and senior undergraduate students working in the area of digital integrated circuits digital circuits and system design BiCMOS process and device modeling **EDA for IC System Design, Verification, and Testing** Louis Scheffer,Luciano Lavagno,Grant Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design



Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set      **Digital Circuit Testing** Francis C.

Wang,1991-07-28 Recent technological advances have created a testing crisis in the electronics industry smaller more highly integrated electronic circuits and new packaging techniques make it increasingly difficult to physically access test nodes New testing methods are needed for the next generation of electronic equipment and a great deal of emphasis is being placed on the development of these methods Some of the techniques now becoming popular include design for testability DFT built in self test BIST and automatic test vector generation ATVG This book will provide a practical introduction to these and other testing techniques For each technique introduced the author provides real world examples so the reader can achieve a working knowledge of how to choose and apply these increasingly important testing methods

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