Using Multiple-Valued Logic Decision Diagrams to Model System Threat Probabilities

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Abstract—System security continues to be of increasing importance. To effectively address both natural and intentional threats to large systems, the threats must be cataloged and analyzed. Extremely large and complex systems can have an accordingly large number of threat scenarios. Simply listing the threats and devising countermeasures for each is ineffective and not efficient. We describe a threat cataloging methodology whereby a large number of threats can be efficiently cataloged and analyzed for common features. This allows countermeasures to be formulated that address a large number of threats that share common features. The methodology utilizes Multiple-Valued Logic for describing the state of a large system and a multiple-valued decision diagram (MDD) for the threat catalog and analysis.

Large System Security, Threat cataloging, MDD

I. INTRODUCTION

In recent years, more emphasis has been placed on design for security, security assessment, disaster recovery, disaster tolerance, in addition to the ongoing efforts in the established areas in fault tolerance. All of these areas require the identification and analysis of faults or threats. Most fault models and analysis methods assume that a fault causes a system to either function or fail and are thus modeled with binary conditions.

Recent events have demonstrated our vulnerability to disasters, both natural and man-made. This motivates the need to incorporate disaster tolerance into large system designs. Disaster tolerance is a superset of the more established approaches commonly referred to as fault tolerance. Models for disaster tolerance differ from those for fault tolerance since they assume that failures can occur due to single or massive numbers of individual faults occurring simultaneously or in a rapidly cascading manner. Traditional fault tolerance models single points of failure. Therefore, a disaster-tolerant system can withstand a catastrophic failure and still function with some degree of normality [8, 15].

Threats on a system can include both accidental and intentional effects. Examples of accidental events include random component failures, natural disasters, and fires. Examples of intentional effects include sabotage and cyber threats. A variety of tree structures have been developed to represent possible system threats. These tree structures are

commonly called fault trees or attack trees. Fault trees were originally developed to identify the effects of component failures on a system [16] while attack trees traditionally focused on the effects of cyber security breaches [14]. There have been recent variants of these trees, and sometimes these trees have been used interchangeably [2, 5, 6, 7, 11]. However, these trees all have one thing in common: they are binaryvalued trees, connected with AND and OR operators. This limits the effectiveness of how they can represent possible system-wide attacks. Modeling different operational modes other than just the binary case of failure or normal operation are critical in analyzing large systems in the presence of threats. As an example, in the 2003 blackout of the US power grid, many complex interactions caused a blackout to occur in a large portion of the northeastern US; however, it would be incorrect to state that the entire US power grid failed. In order to effectively catalog system threats, it is necessary to determine the probabilities of these threats based on various system stimuli, including input conditions and their probabilities.

As part of our preliminary research on large-scale system threat assessment, we developed threat tree models [12]. Threat trees are a superset of fault and attack trees since they are based on multiple-valued (MIV) or radix-p valued algebras over a finite and discrete set of values [10]. When the radix p=2, the threat tree reduces to a fault or attack tree depending on the nature of the disruptive events. Generally, threat trees have p>2: these additional logic states allow for more complicated interactions to be modeled. In particular, these additional states can represent partial failures or degraded performance in a system, which are critical in analyzing large systems in the presence of threats.

The structure of this paper is the following: First, background information on decision diagram models is provided. Next, an approach is presented using decision diagram models to determine system threat probabilities. Finally, a system example is used to illustrate these concepts.

III. DECESION DIAGRAMS

Fig. I shows an example of a simple binary fault tree where the circular nodes represent the event of a single component failure and the logic operators (AND/OR gates) show how the events combine to result in a subsystem failure. Referring to



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Multiple-valued Logic ,1989 Multiple-Valued Logic, 2006. ISMVL 2006. 36th International Symposium on IEEE Computer Society, 2006 ISMVL 2004 International Symposium on Multiple Valued Logic (34, 2004, Toronto), IEEE Computer Society, 2004 Modern Uses of Multiple-Valued Logic M. Dunn, G. Epstein, 2012-12-06 This is a collection of invited papers from the 1975 International Sym posium on Multiple valued Logic Also included is an extensive bib liography of works in the field of multiple valued logic prior to 1975 this supplements and extends an earlier bibliography of works prior to 1965 by Nicholas Rescher in his book Many Valued Logic McGraw Hill 1969 There are a number of possible reasons for interest in the present volume First the range of various uses covered in this collection of papers may be taken as indicative of a breadth which occurs in the field of multiple valued logic as a whole the papers here can do no more than cover a small sample question answering systems analysis of computer hazards algebraic structures relating to multiple valued logic algebra of computer programs fuzzy sets Second a large part of the interest in such uses and applications has occurred in the last twenty even ten years It would be too much to expect this to be reflected in Rescher's 1969 book Third in the 1970 s a series of annual symposia have been held on multiple valued logic which have brought much of this into a sharp focus The 1971 and 1972 symposia were held at the SUNY at Buffalo the 1973 symposium at the Uni versity of Toronto and the 1974 symposium at West Virginia Uni versity Papers from these symposia are included in the bibliography which may be found in an appendix of this book Multiple Valued Logic IEEE Staff,1988-05 Proceedings of the International Symposium on Multiple-Valued Logic (8th). Anthony S. Wojcik, Robert Swartwout, ILLINOIS INST OF TECH CHICAGO DEPT OF COMPUTER SCIENCE.,1978 1999 29th IEEE International Symposium on Multiple-Valued Logic ,1999 The invited addresses discuss developing quantum functional devices for multiple valued logic circuits and structural and behavioral modeling with monadic logics Others of the 43 papers consider algebra circuits decomposition clones logic design decision diagrams applications logic testing and fuzzy logic A panel session reports on challenges and perspectives for the next millennium Specific topics include multivalued binary relations and post algebras supplementary symmetrical logic circuit structure gigantic pairs of minimal clones quaternary coded genetic algorithms highly testable Boolean ring logic circuits and some classes of fuzzy information granularity and their representations No subject index Annotation copyrighted by Book News Inc Portland OR Twelfth International Symposium on Multiple-valued Logic, 1982 The Eiahth International Symposium on Multiple-Valued Logic ,1978 Multiple Valued Logic IEEE Computer Society Press, 1987-10

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