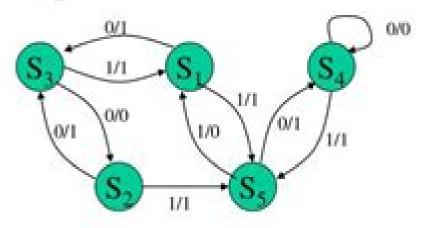
Synthesis of sequential circuits

- · Steps in synthesis of a sequential circuit
 - Specify the FSM (state table or state diagram)
 - Minimize the states
 - State assignment/encoding
- Specification:



PS	x	NS	Z
S_1	0	S3	1
S_1	1	S ₅	1
S ₂	0	S ₃	1
S_2	1	S ₅	1
S ₃	0	S ₂	0
S_3	1	S	1
S ₄	0	S ₄	0
S_4	1	Ss	1
S ₅	0	S ₄	1
S ₅	1	Sı	0

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Melvil Dewey, Richard Rogers
Bowker, L. Pylodet, Charles Ammi
Cutter, Bertine Emma Weston, Karl
Brown, Helen E. Wessells

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of electronic systems Software programmable components such as microprocessors and digital signal processors have been

used extensively in such systems since they allow rapid design revisions However the inherent performance limitations of software programmable systems mean that they are inadequate for high performance designs Designers thus turned to gate arrays as a solution User programmable gate arrays field programmable gate arrays FPGAs have recently emerged and are changing the way electronic systems are designed and implemented The growing complexity of the logic circuits that can be packed onto an FPGA chip means that it has become important to have automatic synthesis tools that implement logic functions on these architectures Logic Synthesis for Field Programmable Gate Arrays describes logic synthesis for both look up table LUT and multiplexor based architectures with a balanced presentation of existing techniques together with algorithms and the system developed by the authors Audience A useful reference for VLSI designers developers of computer aided design tools and anyone involved in or with FPGAs **VLSI Design Environments** George Zobrist, 2000-04-17 VLSI Design Environments investigates design alternatives such as object oriented data modelling The difficulty of automating chip architecture designs is caused by the complexity of the problem The explosion of design decions make a heuristic approach necessary PLAYOUT aims at the solution of system problems based on hierarchy top down planning silicon complier presentations advances in encoding logic synthesis and a microarchitecre and logic optimization system PLAYOUT supports the physical design from entering the structure of digital systems to the generation of the mask The concept for autonomous tools with a clear interface to the network description and the simple interface to the graphics is presented This enables the designer to have a great influence on the configuration of the placement of the schematic diagram Substantial progress is being made in behavioural and logic synthesis both of which depend upon specifications Switching and **Finite Automata Theory** Zvi Kohavi, Niraj K. Jha, 2010 Understand the structure behavior and limitations of logic machines with this thoroughly updated third edition Many new topics are included such as CMOS gates logic synthesis logic design for emerging nanotechnologies digital system testing and asynchronous circuit design to bring students up to speed with modern developments. The intuitive examples and minimal formalism of the previous edition are retained giving students a text that is logical and easy to follow yet rigorous Kohavi and Jha begin with the basics and then cover combinational logic design and testing before moving on to more advanced topics in finite state machine design and testing Theory is made easier to understand with 200 illustrative examples and students can test their understanding with over 350 end of chapter review questions Computer Literature Bibliography: 1964-1967 W. W. Youden, 1965 NBS Special Publication ,1968

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