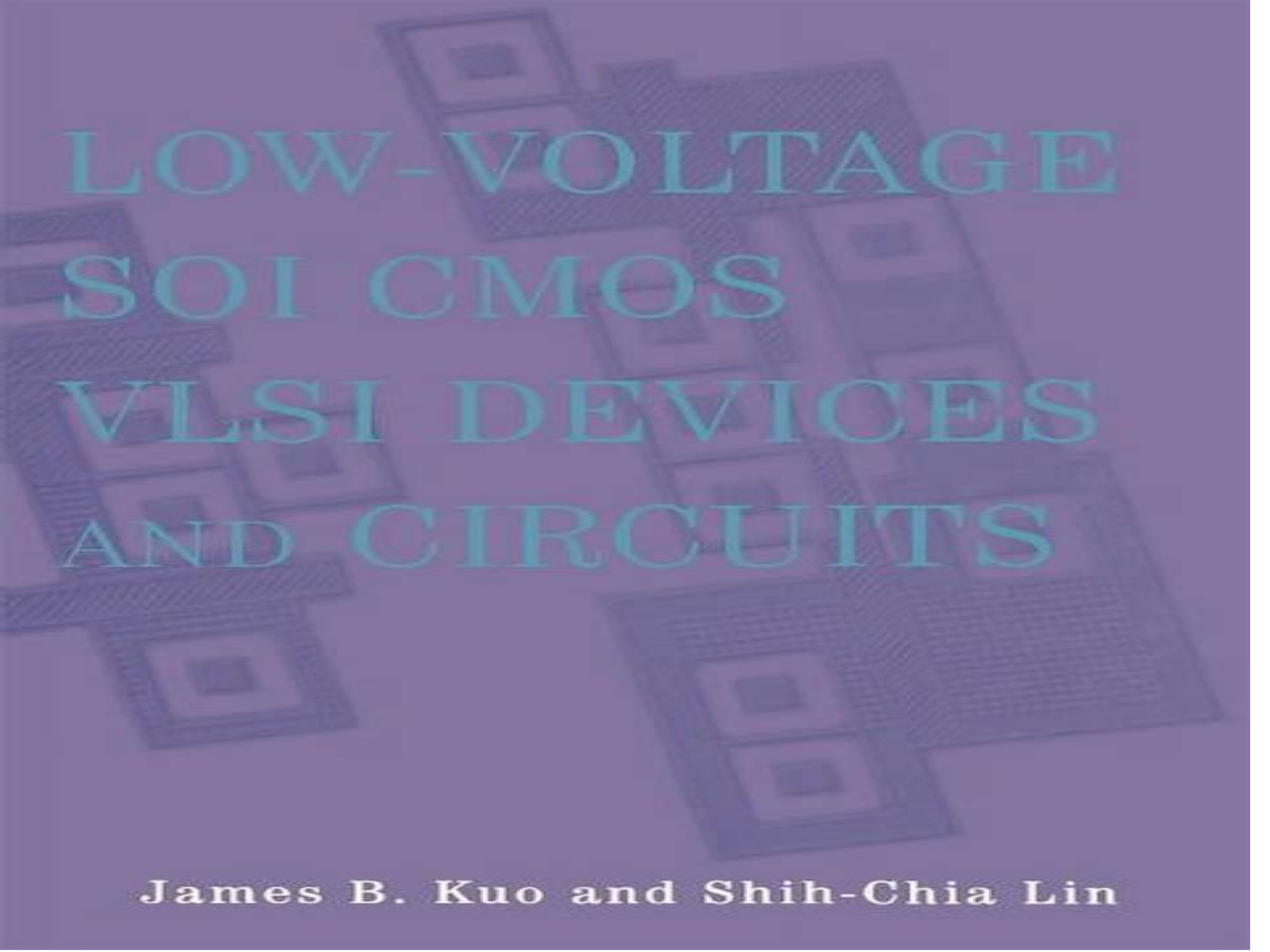


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LOW-VOLTAGE SOI CMOS VLSI DEVICES AND CIRCUITS

James B. Kuo and Shih-Chia Lin

Low Voltage Soi Cmos Vlsi Circuits And Devices

James B. Kuo, Ker-Wei Su



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Low-Voltage SOI CMOS VLSI Devices and Circuits James B. Kuo, Shih-Chia Lin, 2004-04-05 A practical comprehensive survey of SOI CMOS devices and circuits for microelectronics engineers The microelectronics industry is becoming increasingly dependent on SOI CMOS VLSI devices and circuits This book is the first to address this important topic with a practical focus on devices and circuits It provides an up to date survey of the current knowledge regarding SOI device behaviors and describes state of the art low voltage CMOS VLSI analog and digital circuit techniques Low Voltage SOI CMOS VLSI Devices and Circuits covers the entire field from basic concepts to the most advanced ideas Topics include SOI device behavior fundamental and floating body effects hot carrier effects sensitivity reliability self heating breakdown ESD dual gate devices accumulation mode devices short channel effects and narrow channel effects Low voltage SOI digital circuits floating body effects DRAM SRAM static logic dynamic logic gate array CPU frequency divider and DSP Low voltage SOI analog circuits op amps filters ADC DAC sigma delta modulators RF circuits VCO mixers low noise amplifiers and high temperature circuits With over 300 references to the state of the art and over 300 important figures on low voltage SOI CMOS devices and circuits this volume serves as an authoritative reliable resource for engineers designing these circuits in high tech industries

CMOS VLSI Engineering James B. Kuo, Ker-Wei Su, 1998-09-30 Silicon On Insulator SOI CMOS technology has been regarded as another major technology for VLSI in addition to bulk CMOS technology Owing to the buried oxide structure SOI technology offers superior CMOS devices with higher speed high density and reduced second order effects for deep submicron low voltage low power VLSI circuits applications In addition to VLSI applications and because of its outstanding properties SOI technology has been used to realize communication circuits microwave devices BICMOS devices and even fiber optics applications CMOS VLSI Engineering Silicon On Insulator addresses three key factors in engineering SOI CMOS VLSI processing technology device modelling and circuit designs are all covered with their mutual interactions Starting from the SOI CMOS processing technology and the SOI CMOS digital and analog circuits behaviors of the SOI CMOS devices are presented followed by a CAD program ST SPICE which incorporates models for deep submicron fully depleted mesa isolated SOI CMOS devices and special purpose SOI devices including polysilicon TFTs CMOS VLSI Engineering Silicon On Insulator is written for undergraduate senior students and first year graduate students interested in CMOS VLSI It will also be suitable for electrical engineering professionals interested in microelectronics

Low Power Designs in Nanodevices and Circuits for Emerging Applications Shilpi Birla, Shashi Kant Dargar, Neha Singh, P. Sivakumar, 2023-11-14 This reference textbook discusses low power designs for emerging applications This book focuses on the research challenges associated with theory design and applications towards emerging Microelectronics and VLSI device design and developments about low power consumptions The advancements in large scale integration technologies are principally responsible for the growth of the electronics industry This book is focused on senior undergraduates graduate students and professionals in the field of

electrical and electronics engineering nanotechnology This book Discusses various low power techniques and applications for designing efficient circuits Covers advance nanodevices such as FinFETs TFETs CNTFETs Covers various emerging areas like Quantum Dot Cellular Automata Circuits and FPGAs and sensors Discusses applications like memory design for low power applications using nanodevices The number of options for ICs in control applications telecommunications high performance computing and consumer electronics continues to grow with the emergence of VLSI designs Nanodevices have revolutionized the electronics market and human life it has impacted individual life to make it more convenient They are ruling every sector such as electronics energy biomedicine food environment and communication This book discusses various emerging low power applications using CMOS and other emerging nanodevices **Electronic Devices and Circuit Design**

Suman Lata Tripathi, Smrity Dwivedi, 2022-02-02 This new volume offers a broad view of the challenges of electronic devices and circuits for IoT applications The book presents the basic concepts and fundamentals behind new low power high speed efficient devices circuits and systems in addition to CMOS It provides an understanding of new materials to improve device performance with smaller dimensions and lower costs It also looks at the new methodologies to enhance system performance and provides key parameters for exploring the devices and circuit performance based on smart applications The chapters delve into myriad aspects of circuit design including MOSFET structures depending on their low power applications for IoT enabled systems advanced sensor design and fabrication using MEMS indirect bootstrap techniques efficient CMOS comparators various encryption decryption algorithms IoT video forensics applications microstrip patch antennas in embedded IoT applications real time object detection using sound IOT and nanotechnologies based wireless sensors and much more **Low-Power CMOS Circuits**

Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published Low Power Electronics Design Low Power CMOS Circuits Technology Logic Design and CAD Tools addresses the design of low power circuitry in deep submicron technologies It provides a focused reference for specialists involved in designing low power circuitry from transistors to logic gates The book is organized into three broad sections for convenient access The first examines the history of low power electronics along with a look at emerging and possible future technologies It also considers other technologies such as nanotechnologies and optical chips that may be useful in designing integrated circuits The second part explains the techniques used to reduce power consumption at low levels These include clock gating leakage reduction interconnecting and communication on chips and adiabatic circuits The final section discusses various CAD tools for designing low power circuits This section includes three chapters that demonstrate the tools and low power design issues at three major companies that produce logic synthesizers Providing detailed examinations contributed by leading experts Low Power CMOS Circuits Technology Logic Design and CAD Tools supplies authoritative information on how to design and model for high performance with low power consumption in modern integrated circuits It is a must read

for anyone designing modern computers or embedded systems

MOS Devices for Low-Voltage and Low-Energy

Applications Yasuhisa Omura, Abhijit Mallik, Naoto Matsuo, 2017-02-28 Helps readers understand the physics behind MOS devices for low voltage and low energy applications Based on timely published and unpublished work written by expert authors Discusses various promising MOS devices applicable to low energy environmental and biomedical uses Describes the physical effects quantum tunneling of MOS devices Demonstrates the performance of devices helping readers to choose right devices applicable to an industrial or consumer environment Addresses some Ge based devices and other compound material based devices for high frequency applications and future development of high performance devices Seemingly innocuous everyday devices such as smartphones tablets and services such as on line gaming or internet keyword searches consume vast amounts of energy Even when in standby mode all these devices consume energy The upcoming Internet of Things IoT is expected to deploy 60 billion electronic devices spread out in our homes cars and cities Britain is already consuming up to 16 per cent of all its power through internet use and this rate is doubling every four years According to The UK's Daily Mail May 2015 if usage rates continue all of Britain's power supply could be consumed by internet use in just 20 years In 2013 U S data centers consumed an estimated 91 billion kilowatt hours of electricity corresponding to the power generated by seventeen 1000 megawatt nuclear power plants Data center electricity consumption is projected to increase to roughly 140 billion kilowatt hours annually by 2020 the equivalent annual output of 50 nuclear power plants Natural Resources Defense Council USA Feb 2015 All these examples stress the urgent need for developing electronic devices that consume as little energy as possible The book MOS Devices for Low Voltage and Low Energy Applications explores the different transistor options that can be utilized to achieve that goal It describes in detail the physics and performance of transistors that can be operated at low voltage and consume little power such as subthreshold operation in bulk transistors fully depleted SOI devices tunnel FETs multigate and gate all around MOSFETs Examples of low energy circuits making use of these devices are given as well The book MOS Devices for Low Voltage and Low Energy Applications is a good reference for graduate students researchers semiconductor and electrical engineers who will design the electronic systems of tomorrow Dr Jean Pierre Colinge Taiwan Semiconductor Manufacturing Company TSMC The authors present a creative way to show how different MOS devices can be used for low voltage and low power applications They start with Bulk MOSFET following with SOI MOSFET FinFET gate all around MOSFET Tunnel FET and others It is presented the physics behind the devices models simulations experimental results and applications This book is interesting for researchers graduate and undergraduate students The low energy field is an important topic for integrated circuits in the future and none can stay out of this Prof Joao A Martino University of Sao Paulo Brazil

Smart CMOS Image Sensors and Applications Jun Ohta, 2020-05-12 Revised and expanded for this new edition Smart CMOS Image Sensors and Applications Second Edition is the only book available devoted to smart CMOS image sensors and applications The book describes the fundamentals of CMOS image sensors and

optoelectronic device physics and introduces typical CMOS image sensor structures such as the active pixel sensor APS Also included are the functions and materials of smart CMOS image sensors and present examples of smart imaging Various applications of smart CMOS image sensors are also discussed Several appendices supply a range of information on constants illuminance MOSFET characteristics and optical resolution Expansion of smart materials smart imaging and applications including biotechnology and optical wireless communication are included Features Covers the fundamentals and applications including smart materials smart imaging and various applications Includes comprehensive references Discusses a wide variety of applications of smart CMOS image sensors including biotechnology and optical wireless communication Revised and expanded to include the state of the art of smart image sensors **VLSI Design Theory and Practice** ,2013

Nanoscaled Semiconductor-on-Insulator Structures and Devices S. Hall,A.N. Nazarov,V.S. Lysenko,2007-07-09 This book offers combined views on silicon on insulator SOI nanoscaled electronics from experts in the fields of materials science device physics electrical characterization and computer simulation Coverage analyzes prospects of SOI nanoelectronics beyond Moore s law and explains fundamental limits for CMOS SOICMOS and single electron technologies **Device Physics, Modeling, Technology, and Analysis for Silicon MESFET** Iraj Sadegh Amiri,Hossein Mohammadi,Mahdiar Hosseinghadiry,2018-12-13 This book provides detailed and accurate information on the history structure operation benefits and advanced structures of silicon MESFET along with modeling and analysis of the device The authors explain the detailed physics that are important in modeling of SOI MESFETs and present the derivations of compact model expressions so that users can recognize the physical meaning of the model equations and parameters The discussion also includes advanced structures for SOI MESFET for submicron applications

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