

Retargetable Compilers for Embedded Core Processors

Methods and Experiences in
Industrial Applications

Clifford Liem



Foreword by
Pierre G. Paulin & Ahmed A. Jerraya

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Retargetable Compilers For Embedded Core Processors Methods And Experiences In Industrial Applications

Rainer Leupers



Retargetable Compilers For Embedded Core Processors Methods And Experiences In Industrial Applications:

Retargetable Compilers for Embedded Core Processors Clifford Liem, 2013-03-09 Embedded core processors are becoming a vital part of today's system on a chip in the growing areas of telecommunications multimedia and consumer electronics This is mainly in response to a need to track evolving standards with the flexibility of embedded software Consequently maintaining the high product performance and low product cost requires a careful design of the processor tuned to the application domain With the increased presence of instruction set processors retargetable software compilation techniques are critical not only for improving engineering productivity but to allow designers to explore the architectural possibilities for the application domain *Retargetable Compilers for Embedded Core Processors* with a Foreword written by Ahmed Jerraya and Pierre Paulin overviews the techniques of modern retargetable compilers and shows the application of practical techniques to embedded instruction set processors The methods are highlighted with examples from industry processors used in products for multimedia telecommunications and consumer electronics An emphasis is given to the methodology and experience gained in applying two different retargetable compiler approaches in industrial settings The book also discusses many pragmatic areas such as language support source code abstraction levels validation strategies and source level debugging In addition new compiler techniques are described which support address generation for DSP architecture trends The contribution is an address calculation transformation based on an architectural model *Retargetable Compilers for Embedded Core Processors* will be of interest to embedded system designers and programmers the developers of electronic design automation EDA tools for embedded systems and researchers in hardware software co design

Hardware/Software Co-Design Jørgen Staunstrup, Wayne Wolf, 2013-04-17 Introduction to Hardware Software Co Design presents a number of issues of fundamental importance for the design of integrated hardware software products such as embedded communication and multimedia systems This book is a comprehensive introduction to the fundamentals of hardware software co design Co design is still a new field but one which has substantially matured over the past few years This book written by leading international experts covers all the major topics including fundamental issues in co design hardware software co synthesis algorithms prototyping and emulation target architectures compiler techniques specification and verification system level specification Special chapters describe in detail several leading edge co design systems including Cosyma LYCOS and Cosmos Introduction to Hardware Software Co Design contains sufficient material for use by teachers and students in an advanced course of hardware software co design It also contains extensive explanation of the fundamental concepts of the subject and the necessary background to bring practitioners up to date on this increasingly important topic

Algorithms for VLSI Design Automation Sabih H. Gerez, 1999-01-05 Modern microprocessors such as Intel's Pentium chip typically contain many millions of transistors They are known generically as Very Large Scale Integrated VLSI systems and their sheer scale and complexity has necessitated the development of CAD tools to automate their design

This book focuses on the algorithms which are the building blocks of the design automation software which generates the layout of VLSI circuits Courses on this area are typically elective courses taken at senior undergrad or graduate level by students of Electrical and Electronic Engineering and sometimes in Computer Science or Computer Engineering

Encyclopedia of Computer Science and Technology Allen Kent, James G. Williams, 2000-04-28 Combining Artificial Neural Networks to Symbolic and Algebraic computation **System-Level Synthesis** Ahmed Amine Jerraya, Jean Mermet, 2012-12-06 System Level Synthesis deals with the concurrent design of electronic applications including both hardware and software The issue has become the bottleneck in the design of electronic systems including both hardware and software in several major industrial fields including telecommunications automotive and aerospace engineering The major difficulty with the subject is that it demands contributions from several research fields including system specification system architecture hardware design and software design Most existing book cover well only a few aspects of system level synthesis The present volume presents a comprehensive discussion of all the aspects of system level synthesis Each topic is covered by a contribution written by an international authority on the subject **Code Optimization Techniques for Embedded Processors** Rainer Leupers, 2013-03-09 The building blocks of today's and future embedded systems are complex intellectual property components or cores many of which are programmable processors Traditionally these embedded processors mostly have been programmed in assembly languages due to efficiency reasons This implies time consuming programming extensive debugging and low code portability The requirements of short time to market and dependability of embedded systems are obviously much better met by using high level language e.g. C compilers instead of assembly However the use of C compilers frequently incurs a code quality overhead as compared to manually written assembly programs Due to the need for efficient embedded systems this overhead must be very low in order to make compilers useful in practice In turn this requires new compiler techniques that take the specific constraints in embedded system design into account An example are the specialized architectures of recent DSP and multimedia processors which are not yet sufficiently exploited by existing compilers **The British National Bibliography** Arthur James Wells, 1998 **American Book Publishing Record**, 1997-09 **The Cumulative Book Index**, 1998 A world list of books in the English language Subject Guide to Books in Print, 1997

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