

# Residue Number System Arithmetic

For *addition* we can write

$$\gamma_i = A + B - \left[ \frac{A+B}{p_i} \right] p_i$$

for  $i = 1, 2, \dots, n$ .

Representation of  $A$  and  $B$  are

$$A = k_i p_i + \alpha_i,$$

$$B = l_i p_i + \beta_i,$$

$$i = 1, 2, \dots, n,$$

where  $k_i$  and  $l_i$  are non negative integers.

Then

$$A + B = (k_i + l_i) p_i + \alpha_i + \beta_i,$$

$$\left[ \frac{A+B}{p_i} \right] = k_i + l_i + \left[ \frac{\alpha_i + \beta_i}{p_i} \right],$$

$$i = 1, 2, \dots, n$$

From which

$$\gamma_i = \alpha_i + \beta_i - \left[ \frac{\alpha_i + \beta_i}{p_i} \right] p_i,$$

# Residue Number System Arithmetic

**Mi Lu**



## **Residue Number System Arithmetic:**

*Residue Number Systems* Amos R. Omondi, Benjamin Premkumar, 2007 Residue number systems RNSs and arithmetic are useful for several reasons First a great deal of computing now takes place in embedded processors such as those found in mobile devices for which high speed and low power consumption are critical the absence of carry propagation facilitates the realization of high speed low power arithmetic Second computer chips are now getting to be so dense that full testing will no longer be possible so fault tolerance and the general area of computational integrity have become more important RNSs are extremely good for applications such as digital signal processing communications engineering computer security cryptography image processing speech processing and transforms all of which are extremely important in computing today This book provides an up to date account of RNSs and arithmetic It covers the underlying mathematical concepts of RNSs the conversion between conventional number systems and RNSs the implementation of arithmetic operations various related applications are also introduced In addition numerous detailed examples and analysis of different implementations are provided Sample Chapter s Chapter 1 Introduction 301 KB Contents Introduction Mathematical Fundamentals Forward Conversion Addition Multiplication Comparison Overflow Detection Sign Determination Scaling and Division Reverse Conversion Applications Readership Graduate students academics and researchers in computer engineering and electrical electronic engineering

*Residue Number Systems* Amos R. Omondi, Benjamin Premkumar, 2007 Residue number systems RNSs and arithmetic are useful for several reasons First a great deal of computing now takes place in embedded processors such as those found in mobile devices for which high speed and low power consumption are critical the absence of carry propagation facilitates the realization of high speed low power arithmetic Second computer chips are now getting to be so dense that full testing will no longer be possible so fault tolerance and the general area of computational integrity have become more important RNSs are extremely good for applications such as digital signal processing communications engineering computer security cryptography image processing speech processing and transforms all of which are extremely important in computing today This book provides an up to date account of RNSs and arithmetic It covers the underlying mathematical concepts of RNSs the conversion between conventional number systems and RNSs the implementation of arithmetic operations various related applications are also introduced In addition numerous detailed examples and analysis of different implementations are provided

**Residue Number Systems** P.V. Ananda Mohan, 2012-12-06 There has been continuing interest in the improvement of the speed of Digital Signal processing The use of Residue Number Systems for the design of DSP systems has been extensively researched in literature Szabo and Tanaka have popularized this approach through their book published in 1967 Subsequently Jenkins and Leon have rekindled the interest of researchers in this area in 1978 from which time there have been several efforts to use RNS in practical system implementation An IEEE Press book has been published in 1986 which was a collection of Papers It is very interesting to note that in the recent past since 1988

the research activity has received a new thrust with emphasis on VLSI design using non ROM based designs as well as ROM based designs as evidenced by the increased publications in this area The main advantage in using RNS is that several small word length Processors are used to perform operations such as addition multiplication and accumulation subtraction thus needing less instruction execution time than that needed in conventional 16 bit/32 bit DSPs However the disadvantages of RNS have been the difficulty of detection of overflow sign detection comparison of two numbers scaling and division by arbitrary number RNS to Binary conversion and Binary to RNS conversion These operations unfortunately are computationally intensive and are time consuming

**Finite Precision Number Systems and Arithmetic** Peter Kornerup, David W. Matula, 2010-09-30 This comprehensive reference volume suitable for graduate teaching includes problems exercises solutions and an extensive bibliography

**Embedded Systems Design with Special Arithmetic and Number Systems** Amir Sabbagh Molahosseini, Leonel Seabra de Sousa, Chip-Hong Chang, 2017-03-20 This book introduces readers to alternative approaches to designing efficient embedded systems using unconventional number systems The authors describe various systems that can be used for designing efficient embedded and application specific processors such as Residue Number System Logarithmic Number System Redundant Binary Number System Double Base Number System Decimal Floating Point Number System and Continuous Valued Number System Readers will learn the strategies and trade offs of using unconventional number systems in application specific processors and be able to apply and design appropriate arithmetic operations from these number systems to boost the performance of digital systems

**Arithmetic and Logic in Computer Systems** Mi Lu, 2005-02-18 Arithmetic and Logic in Computer Systems provides a useful guide to a fundamental subject of computer science and engineering Algorithms for performing operations like addition subtraction multiplication and division in digital computer systems are presented with the goal of explaining the concepts behind the algorithms rather than addressing any direct applications Alternative methods are examined and explanations are supplied of the fundamental materials and reasoning behind theories and examples No other current books deal with this subject and the author is a leading authority in the field of computer arithmetic The text introduces the Conventional Radix Number System and the Signed Digit Number System as well as Residue Number System and Logarithmic Number System This book serves as an essential up to date guide for students of electrical engineering and computer and mathematical sciences as well as practicing engineers and computer scientists involved in the design application and development of computer arithmetic units

*Proceedings of the Future Technologies Conference (FTC) 2024, Volume 2* Kohei Arai, 2024-11-04 This book covers proceedings of the Future Technologies Conference FTC 2024 which showcase a collection of thoroughly researched studies presented at the ninth Future Technologies Conference held in London the UK This premier annual event highlights groundbreaking research in artificial intelligence computer vision data science computing ambient intelligence and related fields With 476 submissions FTC 2024 gathers visionary minds to explore innovative solutions to today's most pressing

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*Field-Programmable Logic and Applications: The Roadmap to Reconfigurable Computing* Reiner W. Hartenstein, Herbert Grünbacher, 2003-06-29 This book is the proceedings volume of the 10th International Conference on Field Programmable Logic and its Applications FPL held August 27 30 2000 in Villach Austria which covered areas like reconfigurable logic RL reconfigurable computing RC and its applications and all other aspects Its subtitle The Roadmap to Reconfigurable Computing reminds us that we are currently witnessing the runaway of a breakthrough The annual FPL series is the eldest international conference in the world covering configware and all its aspects It was founded 1991 at Oxford University UK and is 2 years older than its two most important competitors usually taking place at Monterey and Napa FPL has been held at Oxford Vienna Prague Darmstadt London Tallinn and Glasgow also see <http://www.fpl.uni-kl.de> FPL The New Case for Reconfigurable Platforms Converging Media Indicated by palmtops smart mobile phones many other portables and consumer electronics media such as voice sound video TV wireless cable telephone and Internet continue to converge This creates new opportunities and even necessities for reconfigurable platform usage The new converged media require high volume flexible multi purpose multi standard low power products adaptable to support evolving standards emerging new standards field upgrades bug fixes and to meet the needs of a growing number of different kinds of services offered to zillions of individual subscribers preferring different media mixes

*Journal of Research of the National Institute of Standards and Technology* ,1996 Supercomputing Vladimir Voevodin, Sergey Sobolev, 2018-12-31 This book constitutes the refereed proceedings of the 4th Russian Supercomputing Days RuSCDays 2018 held in Moscow Russia in September 2018 The 59 revised full papers and one revised short paper presented were carefully reviewed and selected from 136 submissions The papers are organized in topical sections on parallel algorithms supercomputer simulation high performance architectures tools and technologies

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