

- 
- A close-up photograph of a microchip assembly, showing a dark, rectangular chip with numerous gold-colored solder bumps and intricate circuitry on a substrate.
- The first comprehensive application guide to the newest low cost flip chip technologies
 - Inside: how to create effective and reliable packaging for any interconnect system
 - Loaded with in-depth technical detail

Low Cost Flip Chip Technologies

for

DCA, WLCSP, and
PBGA Assemblies

JOHN H. LAU

Low Cost Flip Chip Technologies For Dca Wlcsp And Pbga Assemblies Direct Chip Attack

David William Milner



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Flip chip technology offers numerous advantages over conventional packages by virtue of its electrical performance greater input output I O flexibility and small size This study focuses on two flip chip assembly process developments large size fine pitch lead free capillary flow flip chip and wafer applied bulk coated flip chip The assembly process for a lid attached on the backside of the die was also investigated

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