

Processor Pipelines and Static Worst-Case Execution Time Analysis

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Processor Pipelines Static Worstcase Execution Time Analysis

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Procebor Pipelines Static Worstcase Execution Time Analysis:

Worst-Case Execution Time Aware Compilation Techniques for Real-Time Systems Paul Lokuciejewski, Peter Marwedel, 2010-09-24 For real time systems the worst case execution time WCET is the key objective to be considered. Traditionally code for real time systems is generated without taking this objective into account and the WCET is computed only after code generation. Worst Case Execution Time Aware Compilation Techniques for Real Time Systems presents the first comprehensive approach integrating WCET considerations into the code generation process. Based on the proposed reconciliation between a compiler and a timing analyzer a wide range of novel optimization techniques is provided. Among others the techniques cover source code and assembly level optimizations, exploit machine learning techniques and address the design of modern systems that have to meet multiple objectives. Using these optimizations the WCET of real time applications can be reduced by about 30% to 45% on the average. This opens opportunities for decreasing clock speeds, costs and energy consumption of embedded processors. The proposed techniques can be used for all types of real time systems including automotive and avionics IT systems.

Handbook of Real-Time and Embedded Systems Insup Lee, Joseph Y-T. Leung, Sang H. Son, 2007-07-23 Real time and embedded systems are essential to our lives from controlling car engines and regulating traffic lights to monitoring plane takeoffs and landings to providing up to the minute stock quotes. Bringing together researchers from both academia and industry the Handbook of Real Time and Embedded Systems provides comprehensive coverage.

Transactions on High-Performance Embedded Architectures and Compilers II Per Stenström, David Whalley, 2009-04-22 Transactions on HiPEAC aims at the timely dissemination of research contributions in computer architecture and compilation methods for high performance embedded computer systems. Recognizing the convergence of embedded and general purpose computer systems this journal publishes original research on systems targeted at specific computing tasks as well as systems with broad application bases. The scope of the journal therefore covers all aspects of computer architecture, code generation and compiler optimization methods of interest to researchers and practitioners designing future embedded systems. This second issue contains 15 papers carefully reviewed and selected out of 31 submissions and is divided into two sections. The first section contains extended versions of the top five papers from the 2nd International Conference on High Performance Embedded Architectures and Compilers HiPEAC 2007 held in Ghent, Belgium in January 2007. The second section consists of ten papers covering topics such as microarchitecture, memory systems, code generation and performance modeling.

Verification, Model Checking, and Abstract Interpretation Gilles Barthe, 2010-01-08 This book constitutes the refereed proceedings of the 11th International Conference on Verification, Model Checking and Abstract Interpretation VMCAI 2010 held in Madrid, Spain in January 2010. The 21 papers included in this volume were carefully reviewed and selected from 57 submissions. In addition 3 invited talks and 3 invited tutorials are presented. Topics covered by VMCAI include program verification, program certification, model checking, debugging.

techniques abstract interpretation abstract domains static analysis type systems deductive methods and optimization

Static Analysis Manuel Hermenegildo, German Puebla, 2003-08-02 This book constitutes the refereed proceedings of the 9th International Static Analysis Symposium SAS 2002 held in Madrid Spain in September 2002 The 32 revised full papers presented were carefully reviewed and selected from 86 submissions The papers are organized in topical sections on theory data structure analysis type inference analysis of numerical problems implementation data flow analysis compiler optimizations security analyses abstract model checking semantics and abstract verification and termination analysis

Formal Modeling and Analysis of Timed Systems Kim G. Larsen, Peter Niebert, 2004-04-08 This book constitutes the thoroughly refereed post proceedings of the First International Workshop on Formal Modeling and Analysis of Timed Systems FORMATS 2003 held in Marseille France in September 2003 The 19 revised full papers presented together with an invited paper and the abstracts of two invited talks were carefully selected from 36 submissions during two rounds of reviewing and improvement All current aspects of formal method for modeling and analyzing timed systems are addressed among the timed systems dealt with are timed automata timed Petri nets max plus algebras real time systems discrete time systems timed languages and real time operating systems

Embedded Software Alberto Sangiovanni-Vincentelli, Joseph Sifakis, 2002-09-25 This book constitutes the refereed proceedings of the Second International Conference on Embedded Software EMSOFT 2002 held in Grenoble France in October 2002 The book presents 13 invited papers by leading researchers and 17 revised full papers selected during a competitive round of reviewing The book spans the whole range of embedded software including operating systems and middleware programming languages and compilers modeling and validation software engineering and programming methodologies scheduling and execution time analysis formal methods and communication protocols and fault tolerance

Techniques for Building Timing-Predictable Embedded Systems Nan Guan, 2016-02-03 This book describes state of the art techniques for designing real time computer systems The author shows how to estimate precisely the effect of cache architecture on the execution time of a program how to dispatch workload on multicore processors to optimize resources while meeting deadline constraints and how to use closed form mathematical approaches to characterize highly variable workloads and their interaction in a networked environment Readers will learn how to deal with unpredictable timing behaviors of computer systems on different levels of system granularity and abstraction

The Compiler Design Handbook Y.N. Srikant, Priti Shankar, 2018-10-03 Today s embedded devices and sensor networks are becoming more and more sophisticated requiring more efficient and highly flexible compilers Engineers are discovering that many of the compilers in use today are ill suited to meet the demands of more advanced computer architectures Updated to include the latest techniques The Compiler Design Handbook Second Edition offers a unique opportunity for designers and researchers to update their knowledge refine their skills and prepare for emerging innovations The completely revised handbook includes 14 new chapters addressing topics such as worst case execution time estimation

garbage collection and energy aware compilation The editors take special care to consider the growing proliferation of embedded devices as well as the need for efficient techniques to debug faulty code New contributors provide additional insight to chapters on register allocation software pipelining instruction scheduling and type systems Written by top researchers and designers from around the world The Compiler Design Handbook Second Edition gives designers the opportunity to incorporate and develop innovative techniques for optimization and code generation Multiprocessor Systems-on-Chips Ahmed Jerraya, Wayne Wolf, 2005 Modern system on chip SoC design shows a clear trend toward integration of multiple processor cores on a single chip Designing a multiprocessor system on chip MPSOC requires an understanding of the various design styles and techniques used in the multiprocessor Understanding the application area of the MPSOC is also critical to making proper tradeoffs and design decisions Multiprocessor Systems on Chips covers both design techniques and applications for MPSOCs Design topics include multiprocessor architectures processors operating systems compilers methodologies and synthesis algorithms and application areas covered include telecommunications and multimedia The majority of the chapters were collected from presentations made at the International Workshop on Application Specific Multi Processor SoC held over the past two years The workshop assembled internationally recognized speakers on the range of topics relevant to MPSOCs After having refined their material at the workshop the speakers are now writing chapters and the editors are fashioning them into a unified book by making connections between chapters and developing common terminology Examines several different architectures and the constraints imposed on them Discusses scheduling real time operating systems and compilers Analyzes design trade off and decisions in telecommunications and multimedia applications *Leveraging Applications of Formal Methods, Verification and Validation* Tiziana Margaria, Bernhard Steffen, 2008-11-05 This volume contains the conference proceedings of ISoLA 2008 the Third International Symposium on Leveraging Applications of Formal Methods Verification and Validation which was held in Porto Sani Kassandra Chalkidiki Greece during October 13 15 2008 sponsored by EASST and in cooperation with the IEEE Technical Committee on Complex Systems Following the tradition of its forerunners in 2004 and 2006 in Cyprus and the ISoLA Workshops in Greenbelt USA in 2005 and in Poitiers France in 2007 ISoLA 2008 provided a forum for developers users and researchers to discuss issues related to the adoption and use of rigorous tools and methods for the specification analysis verification certification construction test and maintenance of systems from the point of view of their different application domains Thus the ISoLA series of events serves the purpose of bridging the gap between designers and developers of rigorous tools and users in engineering and in other disciplines and to foster and exploit synergetic relationships among scientists engineers software developers decision makers and other critical thinkers in companies and organizations In particular by providing a venue for the discussion of common problems requirements algorithms methodologies and practices ISoLA aims at supporting researchers in their quest to improve the utility reliability flexibility and efficiency of tools for

building systems and users in their search for adequate solutions to their problems

From Model-Driven Design to Resource Management for Distributed Embedded Systems Bernd Kleinjohann, Lisa Kleinjohann, Ricardo J. Machado, Carlos Pereira, P.S. Thiagarajan, 2007-01-29 Embedded computing systems have started to carry out the key control functions in diverse domains such as telecommunications automotive electronics avionics and even complete industrial manufacturing lines Traditionally such embedded control systems have been implemented in a monolithic centralized manner However distributed and parallel solutions have been steadily gaining popularity In a distributed setup the control task is carried out by a number of controllers distributed over the entire system and interconnected as a network by communication components such as field buses More demanding local control applications require controllers based on parallel architectures or processors with dedicated co processors Distribution and parallelism in embedded system design increase the engineering challenges and demand new development methods and tools From Model Driven Design to Resource Management for Distributed Embedded Systems contains 16 original contributions as well as 12 invited papers by distinguished invited speakers These papers were presented at the Working Conference on Distributed and Parallel Embedded Systems DIPES 2006 which was held in October 2006 in Braga Portugal and sponsored by the International Federation for Information Processing IFIP This volume covers the following very timely topics model driven design test and evolution of embedded systems timing analysis and predictability scheduling allocation communication and resource management in distributed real time systems

Time-Predictable Architectures Christine Rochange, Pascal Sainrat, Sascha Uhrig, 2014-01-17 Building computers that can be used to design embedded real time systems is the subject of this title Real time embedded software requires increasingly higher performances The authors therefore consider processors that implement advanced mechanisms such as pipelining out of order execution branch prediction cache memories multi threading multicore architectures etc The authors of this book investigate the time predictability of such schemes

Leveraging Applications of Formal Methods Tiziana Margaria, Bernhard Steffen, 2006-11-02 This book constitutes the thoroughly refereed post proceedings of the First International Symposium on Leveraging Applications of Formal Methods ISoLA 2004 held in Paphos Cyprus in October November 2004 The 12 revised full papers discuss issues related to the adoption and use of rigorous tools and methods for the specification analysis verification certification construction test and maintenance of systems

Advances in Real-Time Systems Samarjit Chakraborty, Jörg Eberspächer, 2012-02-07 This volume contains the lectures given in honor to Georg F rber as tribute to his contributions in the area of real time and embedded systems The chapters of many leading scientists cover a wide range of aspects like robot or automotive vision systems or medical aspects

Processor Pipelines and Static Worst-case Execution Time Analysis Jakob Engblom, 2002

Embedded Systems Handbook Richard Zurawski, 2018-09-03 Considered a standard industry resource the Embedded Systems Handbook provided researchers and technicians with the authoritative information

needed to launch a wealth of diverse applications including those in automotive electronics industrial automated systems and building automation and control Now a new resource is required to report on current developments and provide a technical reference for those looking to move the field forward yet again Divided into two volumes to accommodate this growth the Embedded Systems Handbook Second Edition presents a comprehensive view on this area of computer engineering with a currently appropriate emphasis on developments in networking and applications Those experts directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews that explore cutting edge developments and deployments and identify potential trends This first self contained volume of the handbook Embedded Systems Design and Verification is divided into three sections It begins with a brief introduction to embedded systems design and verification It then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Those interested in taking their work with embedded systems to the network level should complete their study with the second volume Network Embedded Systems

Embedded Software and Systems Laurence T. Yang,Xingshe Zhou,Wei Zhao,Zhaohui Wu,Yian Zhu,Man Lin,2005-11-30 Welcome to the proceedings of the 2005 International Conference on Embedded Software and Systems ICESS 2005 held in Xian China December 16 18 2005 With the advent of VLSI system level integration and system on chip the center of gravity of the computer industry is now moving from personal computing into embedded computing Embedded software and systems are increasingly becoming a key technological component of all kinds of complex technical systems ranging from vehicles telephones aircraft toys security systems to medical diagnostics weapons pacemakers climate control systems etc The ICESS 2005 conference provided a premier international forum for researchers developers and providers from academia and industry to address all resulting profound challenges to present and discuss their new ideas search results applications and experience to improve international communication and cooperation and to promote embedded software and system industrialization and wide applications on all aspects of embedded software and systems

Embedded Systems Handbook 2-Volume Set Richard Zurawski,2018-10-08 During the past few years there has been an dramatic upsurge in research and development implementations of new technologies and deployments of actual solutions and technologies in the diverse application areas of embedded systems These areas include automotive electronics industrial automated systems and building automation and control Comprising 48 chapters and the contributions of 74 leading experts from industry and academia the Embedded Systems Handbook Second Edition presents a comprehensive view of embedded systems their design verification networking and applications The contributors directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews exploring new developments deployments and trends To accommodate the tremendous growth in the field the handbook is

now divided into two volumes New in This Edition Processors for embedded systems Processor centric architecture description languages Networked embedded systems in the automotive and industrial automation fields Wireless embedded systems Embedded Systems Design and Verification Volume I of the handbook is divided into three sections It begins with a brief introduction to embedded systems design and verification The book then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Networked Embedded Systems Volume II focuses on selected application areas of networked embedded systems It covers automotive field industrial automation building automation and wireless sensor networks This volume highlights implementations in fast evolving areas which have not received proper coverage in other publications Reflecting the unique functional requirements of different application areas the contributors discuss inter node communication aspects in the context of specific applications of networked embedded systems Robotic Systems Ashish Dutta,2012-02-03 This book brings together some of the latest research in robot applications control modeling sensors and algorithms Consisting of three main sections the first section of the book has a focus on robotic surgery rehabilitation self assembly while the second section offers an insight into the area of control with discussions on exoskeleton control and robot learning among others The third section is on vision and ultrasonic sensors which is followed by a series of chapters which include a focus on the programming of intelligent service robots and systems adaptations

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