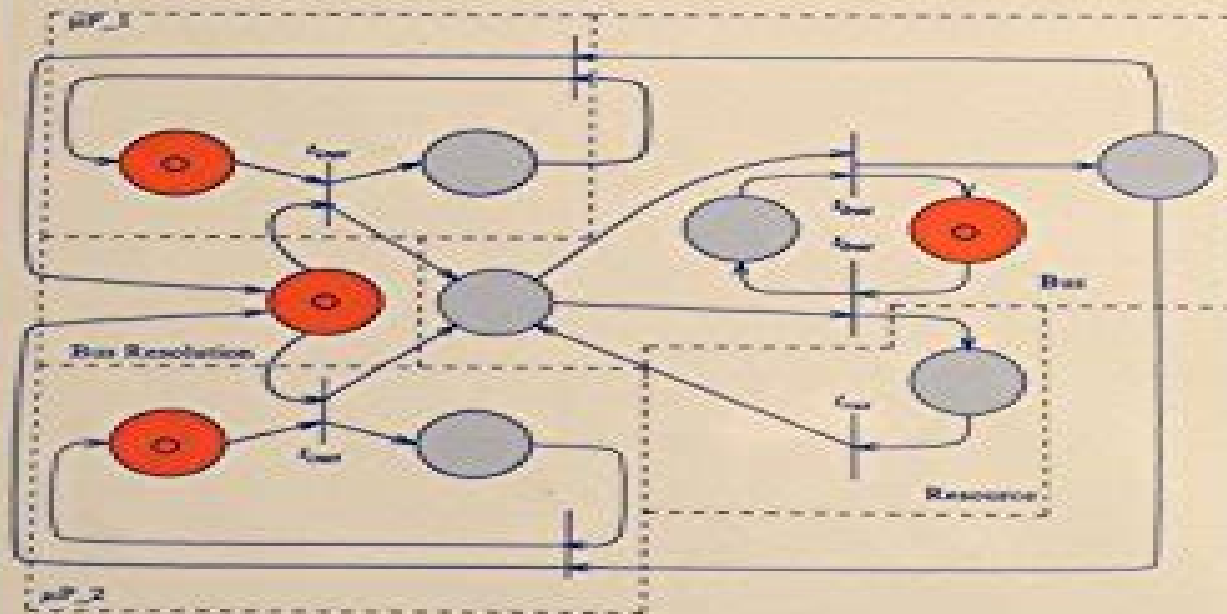


Performance and Fault Modeling with **VHDL**



JOEL M. SCHOEN, EDITOR

Performance And Fault Modeling With Vhdl

**Wolfgang Müller, Wolfgang
Rosenstiel, Jürgen Ruf**



Performance And Fault Modeling With Vhdl:

Performance and Fault Modeling with VHDL Joel Mark Schoen, 1992 Describes how VHDL can be used across the system design process from specification to the development of maintenance strategies The contributors provide advanced techniques and detail how these can be adapted to improve the system development process reducing time and cost

High-Level System Modeling Ronald Waxman, Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 In system design generation of high level abstract models that can be closely associated with evolving lower level models provides designers with the ability to incrementally test an evolving design against a model of a specification Such high level models may deal with areas such as performance reliability availability maintainability and system safety Abstract models also allow exploration of the hardware versus software design space in an incremental fashion as a fuller detailed design unfolds leaving behind the old practice of hardware software binding too early in the design process Such models may also allow the inclusion of non functional aspects of design e g space power heat in a simulatable information model dealing with the system s operation This book addresses Model Generation and Application specifically in the following domains Specification modeling linking object data modeling behavior modeling and activity modeling Operational specification modeling modeling the way the system is supposed to operate from a user s viewpoint Linking non functional parameters with specification models Hybrid modeling linking performance and functional elements Application of high level modeling to hardware software approaches Mathematical analysis techniques related to the modeling approaches Reliability modeling Applications of High Level Modeling Reducing High Level Modeling to Practice High Level System Modeling Specification and Design Methodologies describes the latest research and practice in the modeling of electronic systems and as such is an important update for all researchers design engineers and technical managers working in design automation and circuit design

Design Automation, Languages, and Simulations Wai-Kai Chen, 2003-03-26 As the complexity of electronic systems continues to increase the micro electronic industry depends upon automation and simulations to adapt quickly to market changes and new technologies Compiled from chapters contributed to CRC s best selling VLSI Handbook this volume of the Principles and Applications in Engineering series covers a broad rang *Dependable Computing - EDDC-3* Jan Hlavicka, Erik Maehle, Andras Pataticza, 2003-06-26 The idea of creating the European Dependable Computing Conference EDCC was born at the moment when the Iron Curtain fell A group of enthusiasts who were pre viously involved in research and teaching in the eld of fault tolerant computing in different European countries agreed that there is no longer any point in keeping pre viously independent activities apart and created a steering committee which took the responsibility for preparing the EDCC calendar and appointing the chairs for the in dividual conferences There is no single European or global professional organization that took over the responsibility for this conference but there are three national in terest groups that sent delegates to the steering committee and support its activities especially by promoting the conference materials As can be

seen from these materials they are the SEE Working Group Dependable Computing which is a successor organization of AFCET in France the GI ITG GMATechnical Committee on Dependability and Fault Tolerance in Germany and the AICA Working Group Dependability of Computer Systems in Italy In addition committees of several global professional organizations such as IEEE and IFIP support this conference Prague has been selected as a conference venue for several reasons It is an easily accessible location that may attract many visitors by its beauty and that has a tradition in organizing international events of this kind one of the last FTSD conferences took place here

Microelectrofluidic Systems Tianhao Zhang, Krishnendu Chakrabarty, Richard B. Fair, 2018-10-08 Composite systems that integrate microelectromechanical and microelectrofluidic MEF components with electronics are emerging as the next generation of system on a chip SOC designs However there remains a pressing need for a structured methodology for MEFS design automation including modeling techniques and simulation and optimization tools Integrating top down and bottom up design philosophies *Microelectrofluidic Systems* presents the first comprehensive design strategy for MEFS This strategy supports hierarchical modeling and simulation from the component level to the system level It leads to multi objective optimization tools valuable in all phases of the design process from conceptualization to final manufacturing The authors begin by defining the basic variables and elements needed to describe MEFS behavior then model that behavior across three layers of abstraction the low level component high level reconfigurable architecture and bio chemical application layers They have developed a hierarchical integrated design environment with SystemC and present its architecture and associated functional packages *Microelectrofluidic Systems* is visionary in its leverage of electronic design principles for microsystem design and heralds a new era of automated SOC design The strategy it presents holds the potential for significant reductions in design time and life cycle maintenance costs and its techniques and tools for robust design and application flexibility can lead to the high volume production needed for the inevitably growing product market

Rapid Prototyping of Application Specific Signal Processors Mark A. Richards, Anthony J. Gadiant, Geoffrey A. Frank, 1997-02-28 *Rapid Prototyping of Application Specific Signal Processors* presents leading edge research that focuses on design methodology infrastructure support and scalable architectures developed by the 150 million dollar DARPA United States Department of Defense RASSP Program The contributions to this edited work include an introductory overview chapter that explains the origin concepts and status of this effort The RASSP Program is a multi year DARPA Tri Service initiative intended to dramatically improve the process by which complex digital systems particularly embedded signal processors are designed manufactured upgraded and supported This program was originally driven by military applications for signal processing The requirements of military applications for real time signal processing are typically more demanding than those of commercial applications but the time gap between technology employed in advanced military prototypes and commercial products is narrowing rapidly The research on methodologies infrastructure and architectures presented in this book is applicable to commercial signal processing systems

that are in design now or will be developed before the end of the decade Rapid Prototyping of Application Specific Signal Processors is a valuable reference for developers of embedded digital systems particularly systems engineers for signal processing systems such as digital TV biomedical image processing systems and telecommunications and for military contractors who are developing signal processing systems This book will also be of interest to managers who are charged with responsibility for creating and maintaining environments and infrastructures for developing large embedded digital systems The chief value for managers will be the defining of methods and processes that reduce development time and cost

Models in System Design Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Models in System Design tracks the general trend in electronics in terms of size complexity and difficulty of maintenance System design is by nature combined with prototyping mixed domain design and verification and it is no surprise that today's modeling and models are used in various levels of system design and verification In order to deal with constraints induced by volume and complexity new methods and techniques have been defined Models in System Design provides an overview of the latest modeling techniques for use by system designers The first part of the book considers system level design discussing such issues as abstraction performance and trade offs There is also a section on automating system design The second part of the book deals with some of the newest aspects of embedded system design These include co verification and prototyping Finally the book includes a section on the use of the MCSE methodology for hardware software co design Models in System Design will help designers and researchers to understand these latest techniques in system design and as such will be of interest to all involved in embedded system design Computer Aided Design and Design Automation Wai-Kai Chen, 2018-03-12 This volume of The Circuits and Filters Handbook Third Edition focuses on computer aided design and design automation In the first part of the book international contributors address topics such as the modeling of circuit performances symbolic analysis methods numerical analysis methods design by optimization statistical design optimization and physical design automation In the second half of the text they turn their attention to RF CAD high performance simulation formal verification RTK behavioral synthesis system level design an Internet based micro electronic design automation framework performance modeling and embedded computing systems design *The Circuits and Filters Handbook (Five Volume Slipcase Set)* Wai-Kai Chen, 2018-12-14 Standard setting groundbreaking authoritative comprehensive these often overused words perfectly describe The Circuits and Filters Handbook Third Edition This standard setting resource has documented the momentous changes that have occurred in the field of electrical engineering providing the most comprehensive coverage available More than 150 contributing experts offer in depth insights and enlightened perspectives into standard practices and effective techniques that will make this set the first and most likely the only tool you select to help you with problem solving In its third edition this groundbreaking bestseller surveys accomplishments in the field providing researchers and designers with the comprehensive detail they need to optimize research and design All five volumes include valuable

information on the emerging fields of circuits and filters both analog and digital Coverage includes key mathematical formulas concepts definitions and derivatives that must be mastered to perform cutting edge research and design The handbook avoids extensively detailed theory and instead concentrates on professional applications with numerous examples provided throughout The set includes more than 2500 illustrations and hundreds of references Available as a comprehensive five volume set each of the subject specific volumes can also be purchased separately Hardware/Software Co-Design and Co-Verification Jean-Michel Bergé,Oz Levia,Jacques Rouillard,2013-03-09 Co Design is the set of emerging techniques which allows for the simultaneous design of Hardware and Software In many cases where the application is very demanding in terms of various performances time surface power consumption trade offs between dedicated hardware and dedicated software are becoming increasingly difficult to decide upon in the early stages of a design Verification techniques such as simulation or proof techniques that have proven necessary in the hardware design must be dramatically adapted to the simultaneous verification of Software and Hardware Describing the latest tools available for both Co Design and Co Verification of systems Hardware Software Co Design and Co Verification offers a complete look at this evolving set of procedures for CAD environments The book considers all trade offs that have to be made when co designing a system Several models are presented for determining the optimum solution to any co design problem including partitioning architecture synthesis and code generation When deciding on trade offs one of the main factors to be considered is the flow of communication especially to and from the outside world This involves the modeling of communication protocols An approach to the synthesis of interface circuits in the context of co design is presented Other chapters present a co design oriented flexible component data base and retrieval methods a case study of an ethernet bridge designed using LOTOS and co design methodologies and finally a programmable user interface based on monitors Hardware Software Co Design and Co Verification will help designers and researchers to understand these latest techniques in system design and as such will be of interest to all involved in embedded system design **The Codesign of Embedded Systems: A Unified**

Hardware/Software Representation Sanjaya Kumar,James H. Aylor,Barry W. Johnson,Wm.A. Wulf,1995-11-30 Current practice dictates the separation of the hardware and software development paths early in the design cycle These paths remain independent with very little interaction occurring between them until system integration In particular hardware is often specified without fully appreciating the computational requirements of the software Also software development does not influence hardware development and does not track changes made during the hardware design phase Thus the ability to explore hardware software tradeoffs is restricted such as the movement of functionality from the software domain to the hardware domain and vice versa or the modification of the hardware software interface As a result problems that are encountered during system integration may require modification of the software and or hardware resulting in potentially significant cost increases and schedule overruns To address the problems described above a cooperative design approach

one that utilizes a unified view of hardware and software is described This approach is called hardware software codesign The Codesign of Embedded Systems develops several fundamental hardware software codesign concepts and a methodology that supports them A unified representation referred to as a decomposition graph is presented which can be used to describe hardware or software using either functional abstractions or data abstractions Using a unified representation based on functional abstractions an abstract hardware software model has been implemented in a common simulation environment called ADEPT Advanced Design Environment Prototyping Tool This model permits early hardware software evaluation and tradeoff exploration Techniques have been developed which support the identification of software bottlenecks and the evaluation of design alternatives with respect to multiple metrics The application of the model is demonstrated on several examples A unified representation based on data abstractions is also explored This work leads to investigations regarding the application of object oriented techniques to hardware design The Codesign of Embedded Systems A Unified Hardware Software Representation describes a novel approach to a topic of immense importance to CAD researchers and designers alike

High-Level System Modeling Jean-Michel Bergé,Oz Levia,Jacques Rouillard,2012-12-06 A reactive system is one that is in continual interaction with its environment and executes at a pace determined by that environment Examples of reactive systems are network protocols air traffic control systems industrial process control systems etc Reactive systems are ubiquitous and represent an important class of systems Due to their complex nature such systems are extremely difficult to specify and implement Many reactive systems are employed in highly critical applications making it crucial that one considers issues such as reliability and safety while designing such systems The design of reactive systems is considered to be problematic and poses one of the greatest challenges in the field of system design and development In this paper we discuss specification modeling methodologies for reactive systems Specification modeling is an important stage in reactive system design where the designer specifies the desired properties of the reactive system in the form of a specification model This specification model acts as the guidance and source for the implementation To develop the specification model of complex systems in an organized manner designers resort to specification modeling methodologies In the context of reactive systems we can call such methodologies reactive system specification modeling methodologies

SystemC Wolfgang Müller,Wolfgang Rosenstiel,Jürgen Ruf,2007-05-08 SystemC has received a wide acceptance by users and tool vendors as the next generation system description language in order to deal with higher levels of abstraction for complex SoC designs SystemC Methodologies and Applications gives a comprehensive survey on the state of the art of SystemC in industry and research Organised into 11 self contained chapters selected SystemC experts present their approaches in the domains of modelling analysis synthesis Their contributions range from mixed signal and discrete system to embedded software The chapters give a broad overview of recent advances in SystemC methodologies and applications and are mainly based on presentations given at the European SystemC User Group meetings

Design and Test Technology for Dependable

Systems-on-Chip Ubar, Raimund, Raik, Jaan, Vierhaus, Heinrich Theodor, 2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC **Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology**, 1997 CONTROL SYSTEMS, ROBOTICS AND AUTOMATION - Volume IV Heinz Unbehauen, 2009-10-11 This Encyclopedia of Control Systems Robotics and Automation is a component of the global Encyclopedia of Life Support Systems EOLSS which is an integrated compendium of twenty one Encyclopedias This 22 volume set contains 240 chapters each of size 5000 30000 words with perspectives applications and extensive illustrations It is the only publication of its kind carrying state of the art knowledge in the fields of Control Systems Robotics and Automation and is aimed by virtue of the several applications at the following five major target audiences University and College Students Educators Professional Practitioners Research Personnel and Policy Analysts Managers and Decision Makers and NGOs *The Functional Verification of Electronic Systems* Brian Bailey, 2005-01-30 Addressing the need for full and accurate functional information during the design process this guide offers a comprehensive overview of functional verification from the points of view of leading experts at work in the electronic design industry **Fourth International Workshop on Hardware/Software Co-Design, Codes/CASHE '96** Donald E. Thomas, Rolf Ernst, 1996 Embedded architecture co synthesis and system integration B Lin S Vercauteren and H De Man A multi level transformation approach to HW SW codesign a case study T K Y Cheung G Hellestrand and P Kanthamanon Fully parallel hardware software codesign for multi dimensional DSP applications M Sheliga N L Passos and E H M Sha A co design methodology based on formal specification and high level estimation C Carreras and others Speed up estimation for HW SW systems W Hardt and W Rosenstiel A framework for interactive analysis of timing constraints in embedded systems R K Gupta The interplay of run time estimation and granularity in HW SW partitioning J Henkel and R Ernst Partitioning and exploration strategies in the TOSCA co design flow A Balboni W Fornaciari and D Sciuto Process partitioning for distributed embedded systems J Hou and W Wolf Two level partitioning of image processing algorithms for the parallel map oriented machine R W Hartenstein J Becker and R Kress PACE a dynamic programming algorithm for hardware software partitioning P V Knudsen and J Madsen A model for the coanalysis of hardware and software architectures F Rose and others A case study in co design of communication controllers R Gerndt Formal verification of embedded systems based on CFSM networks F Balarin and others Towards a model for hardware and software functional partitioning F Vahid and T dm Le Implications of codesign as a natural constituent of a systems engineering discipline for computer based systems M Voss and O Hammerschmidt Uninterpreted co simulation for performance evaluation of HW SW systems J P Calvez D Heller and O Pasquier Fast and accurate hardware software co simulation using software timing estimates C Passerone and others **DCIS2002** Salvador Bracho del Pino, Mar Martínez, Teresa Riesgo, Miguel Ángel Allende Recio, 2002 Este libro contiene las presentaciones de la

XVII Conferencia de Diseño de Circuitos y Sistemas Integrados celebrado en el Palacio de la Magdalena Santander en noviembre de 2002 Esta Conferencia ha alcanzado un alto nivel de calidad como consecuencia de su tradición y madurez que lo convierte en uno de los acontecimientos más importantes para los circuitos de microelectrónica y la comunidad de diseño de sistemas en el sur de Europa Desde su origen tiene una gran contribución de Universidades españolas aunque hoy los autores participan desde catorce países Fault Injection Techniques and Tools for Embedded Systems Reliability Evaluation Alfredo Benso, Paolo Prinetto, 2005-12-15 Fault Injection Techniques and Tools for Embedded Systems Reliability Evaluation intends to be a comprehensive guide to Fault Injection techniques used to evaluate the dependability of a digital system The description and the critical analysis of different Fault Injection techniques and tools will be authored by key scientists in the field of system dependability and fault tolerance

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