

# Logic Gate Cheat Sheet

NOT Gate



$$Y = \overline{A}$$

Input	Output
A	Y
0	1
1	0

AND Gate



$$Y = A \cdot B$$

Input		Output
A	B	Y
0	0	0
0	1	0
1	0	0
1	1	1

NAND Gate



$$Y = \overline{A \cdot B}$$

Input		Output
A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

OR Gate



$$Y = A + B$$

Input		Output
A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1

NOR Gate



$$Y = \overline{A + B}$$

Input		Output
A	B	Y
0	0	1
0	1	0
1	0	0
1	1	0

XOR Gate



$$Y = A \oplus B$$

Input		Output
A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0

# Manual Of Logic Circuits

**DJ Losen**



## **Manual Of Logic Circuits:**

**Manual of Logic Circuits** Gerald A. Maley, 1970      *A Definitive Guide to Logic Circuits and Advanced Circuits Mastering Digital Electronics*, 2024-01-18 Introduction The Aims and Objectives of the Book My main aim in writing this book is to introduce you to the exciting and challenging field of digital electronics I want to develop your desire and ability to understand how digital circuits work After reading this book you should be able to do some or all of the following You will understand what TTL and CMOS mean and appreciate their main differences You should know what the five main logic gates are and their respective symbols and Boolean expressions You should know the basics of Boolean algebra and use it to simplify logic expressions and circuits You should know what Karnaugh maps are and how to use them to simplify logic circuits and expressions You should know how to implement the 1st and 2nd canonical formats for Karnaugh maps You will know how the JK flip flop works and how it was born out of the SR latch You should be able to use the JK flip flop and the D type latch to create a series of counters and different shift registers such as SIPO SISO PIPO and PISO You should understand the difference between sequential and combinational logic You should be able to use a range of design techniques that is state diagrams transition tables etc You should be able to create a range of combinational logic circuits such as half and full adders binary subtractors multiplexers etc You should understand how the 555 timer IC works and how to configure it in a range of different applications such as the monostable the astable and PWM You should be able to design a range of logic circuits You should be able to use the ECAD software TINA 12      *Manual of Logic Circuits By Gerald A. Maley* Gerald A. Maley, 1970      *Digital Logic Testing and Simulation* Alexander Miczo, 1985-12 The new standard in the field presenting the latest design and testing methods for logic circuits and the development of a BASIC based simulation Offers designers and test engineers unique coverage of circuit design for testability stressing the incorporation of hardware into designs that facilitate testing and diagnosis by allowing greater access to internal circuits Examines various ways of representing a design as well as external testing methods that apply this information      **Circuits and Electronics: A Practical Guide** Pasquale De Marco, This book is a comprehensive guide to the basic principles of electronics It is written in a clear and concise style with plenty of illustrations and examples to help the reader understand the concepts being discussed No prior knowledge of electronics is required and the book is suitable for anyone who is interested in learning more about this fascinating field Whether you are a student a hobbyist or a professional engineer this book will provide you with a valuable foundation in electronics In this book we cover a wide range of topics including Basic electrical concepts such as voltage current and resistance Semiconductor diodes and transistors Amplifiers and oscillators Digital logic Electronic measurements Power electronics Industrial electronics Each chapter contains a number of exercises and problems to help you test your understanding of the material presented Answers to these exercises and problems are provided at the end of the book This book is an essential resource for anyone who wants to learn more about electronics It is a valuable reference

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