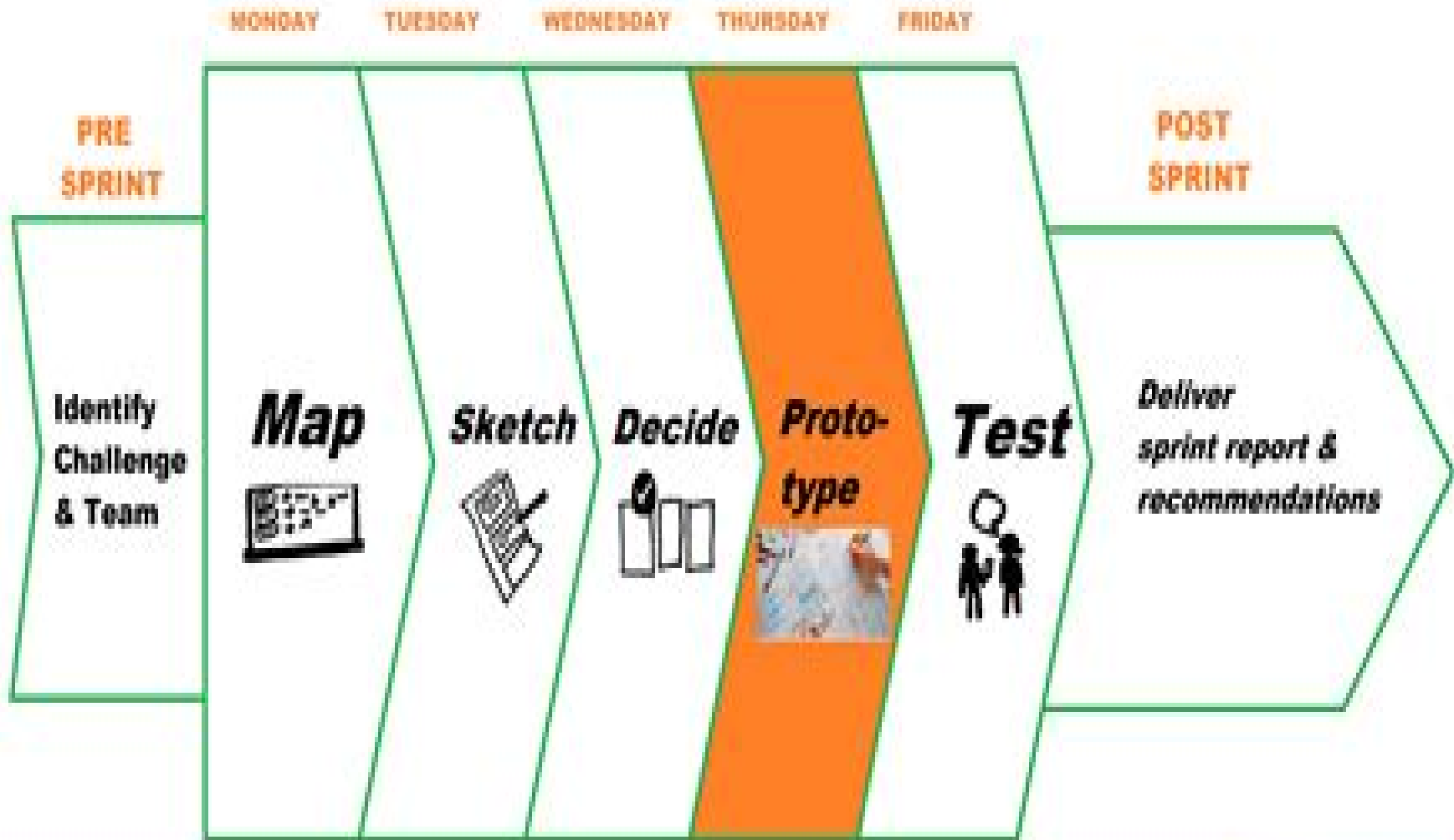


What is Rapid Prototyping?



Rapid Prototyping Of Digital System Necd

David Qu



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Rapid Prototyping of Digital Systems James O. Hamblen, Michael D. Furman, 2007-05-08 Rapid Prototyping of Digital Systems Second Edition provides an exciting and challenging laboratory component for an undergraduate digital logic design class The more advanced topics and exercises are also appropriate for consideration at schools that have an upper level course in digital logic or programmable logic Design engineers working in industry will also want to consider this book for a rapid introduction to FPLD technology and logic synthesis using commercial CAD tools especially if they have not had previous experience with the new and rapidly evolving technology Two tutorials on the Altera CAD tool environment an overview of programmable logic and a design library with several easy to use input and output functions were developed for this book to help the reader get started quickly Early design examples use schematic capture and library components VHDL is used for

more complex designs after a short introduction to VHDL based synthesis A coupon is included with the text for purchase of the new UP 1X board The additional logic and memory in the UP 1X s FLEX 10K70 is useful on larger design projects such as computers and video games The second edition includes an update chapter on programmable logic new robot sensors and projects optional Verilog examples and a meta assembler which can be used to develop assemble language programs for the computer designs in Chapters 8 and 13 *Rapid Prototyping of Digital Systems* James O. Hamblen,Michael D.

Furman,1999-09-30 *Rapid Prototyping of Digital Systems* provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses The more advanced topics and exercises also make this text useful for upper level courses in digital logic or programmable logic Design engineers working in industry will want to consider this text for a rapid introduction to PPLD technology and logic synthesis using commercial CAD tools *Rapid Prototyping of Digital Systems* includes two tutorials on the Altera CAD tool environment an overview of programmable logic and a design library with several easy to use input and output functions These features were developed to help students get started quickly Early design examples use schematic capture and library components VHDL is used for more complex designs after a short introduction to VHDL based synthesis The approach used in this text reflects contemporary practice in industry more accurately than the more traditional TTL protoboard based laboratory courses Designs containing up to twenty thousand gates are possible with the Altera Student Version CAD tools and the UP 1 board *Rapid Prototyping of Digital Systems* contains a number of interesting and challenging laboratory projects involving serial communications state machines with video output video games and graphics simple computers keyboard and mouse interfaces robotics and a RISC processor core These projects were all developed on the student version of the Altera CAD tools and can be implemented on the Altera UP 1 board *Rapid Prototyping of Digital Systems* James O. Hamblen,Tyson S. Hall,Michael D. Furman,2006-01-16 *Rapid*

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and Prototyping Using Field Programmable Logic Zoran Salcic, Asim Smailagic, 2012-12-06 Field programmable logic has been available for a number of years. The role of Field Programmable Logic Devices (FPLDs) has evolved from simply implementing the system glue logic to the ability to implement very complex system functions such as microprocessors and microcomputers. The speed with which these devices can be programmed makes them ideal for prototyping. Low production cost makes them competitive for small to medium volume productions. These devices make possible new sophisticated applications and bring up new hardware/software trade-offs and diminish the traditional hardware/software demarcation line. Advanced design tools are being developed for automatic compilation of complex designs and routings to custom circuits.

Digital Systems Design and Prototyping Using Field Programmable Logic covers the subjects of digital systems design and FPLDs, combining them into an entity useful for designers in the areas of digital systems and rapid system prototyping. It is also useful for the growing community of engineers and researchers dealing with the exciting field of FPLDs, reconfigurable and programmable logic. The authors' goal is to bring these topics to students studying digital system design, computer design, and related subjects in order to show them how very complex circuits can be implemented at the desk.

Digital Systems Design and Prototyping Using Field Programmable Logic makes a pioneering effort to present rapid prototyping and generation of computer systems using FPLDs.

From the Foreword: This is a ground-breaking book that bridges the gap between digital design theory and practice. It provides a unifying terminology for describing FPLD technology. In addition to introducing the technology, it also describes the design methodology and tools required to harness this technology. It introduces two hardware description languages: e.g., AHDL and VHDL. Design is best learned by practice, and the book supports this notion with abundant case studies.

Daniel P. Siewiorek, Carnegie Mellon University. CD-ROM INCLUDED.

Digital Systems Design and Prototyping Using Field Programmable Logic, First Edition, includes a CD-ROM that contains Altera's MAX PLUS II 7.21 Student Edition Programmable Logic Development Software. MAX PLUS II is a fully integrated design environment that offers unmatched flexibility and performance. The intuitive graphical interface is complemented by complete and instantly accessible on-line documentation, which makes learning and using MAX PLUS II quick and easy.

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- Design verification with full timing simulation.

Digital Systems Design and Prototyping Zoran Salcic, Asim Smailagic, 2007-05-08

Digital Systems Design and Prototyping Using Field Programmable Logic and Hardware Description Languages, Second Edition, covers the subject of digital systems design using two important technologies: Field Programmable Logic Devices (FPLDs) and Hardware Description Languages (HDLs). These two technologies are combined to aid in the design, prototyping, and implementation of a whole range of digital systems, from very simple ones replacing traditional glue logic to very complex ones customized as the

applications require Three HDLs are presented VHDL and Verilog the widely used standard languages and the proprietary Altera HDL AHDL The chapters on these languages serve as tutorials and comparisons are made that show the strengths and weaknesses of each language A large number of examples are used in the description of each language providing insight for the design and implementation of FPLDs With the addition of the Altera UP 1 prototyping board all examples can be tested and verified in a real FPLD Digital Systems Design and Prototyping Using Field Programmable Logic and Hardware Description Languages Second Edition is designed as an advanced level textbook as well as a reference for the professional engineer *PC/CPLD-based Rapid Prototyping System for Digital Systems Design* Man Sum Cheng,1997 Advanced Digital Systems Design with Rapid Prototyping on FPGAs Using VHDL Lucien Ngalamou,2012-08-01 Advanced Digital Systems Design with Rapid Prototyping on FPGAs using VHDL aims to provide students researcher and hardware designers in electrical computer engineering with a reference manual that covers the main aspects of hardware implementation of complex algorithms in the field of digital technology using FPGAs **Rapid Prototyping of Digital Systems** Jamaludin Omar,1997 The development and implementation of a framework for the rapid prototyping of digital systems using programmable hardware is presented The framework components developed consist of PC cards with programmable hardware components i e FPGAS PCB templates of the PC cards hardware modules for interfacing with PC systems software subroutines appropriate for hardware modules software utility programs for configuring the FPGAs on the PC cards and examples of designs which include hardware and software The framework allows a digital system to be developed rapidly and tested at realistic clock rates on the PC platform Hardware software and utilisation aspects of the rapid framework are described in detail The framework is shown to be very useful in rapid prototyping of a digital system A coprocessor card for computed tomography application was developed as an application using the rapid prototyping framework With limited capacity static RAM on board it was able to speed up implementation of the filtered backprojection algorithm up to 5 times

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