

2022 IEEE 28th International Symposium on On-Line Testing and Robust System Design (IOLTS 2022)

**Torino, Italy
12-14 September 2022**

IEEE Catalog Number: CFP22OLT-POD
ISBN: 978-1-6654-7356-9



On Line Testing Symposium Iolts 2003 Proceedings

Wei Zhang



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On-Line Testing Symposium, 2003. IOLTS 2003. 9th IEEE C. Metra, 2003 IOLTS 2003, 2003 *Proceedings, 9th IEEE International On-Line Testing Symposium, 2003* Proceedings of the 7th China Aeronautical Science and Technology Conference Chinese Soc. of Aeronautics&Astronautics, 2025-03-22 This book contains the selected papers from the 7th China Aeronautical Science and Technology Conference Topics include but are not limited to key technologies for aircraft including fixed wing rotorcraft new concept aircraft etc design and overall optimization aerodynamics flight mechanics structural design advanced aviation materials including composite materials advanced aviation manufacturing and design and overall optimisation aerodynamics and flight mechanics structural design advanced aeronautical materials including composite materials advanced aeronautical manufacturing technology advanced aeronautical propulsion technology navigation guidance and control technology airborne systems electromechanical technology environmental control life saving technology key technologies for multi electric aircraft and all electric aircraft aviation testing technology critical technologies in the vicinity of space vehicles unmanned aerial vehicles and related technologies general aviation flight safety civil aviation transportation and air quality aviation science and technology and industrial development policy and planning other related technologies Make this book a valuable resource for researchers engineers and students Design and Test Technology for Dependable Systems-on-Chip Ubar, Raimund, Raik, Jaan, Vierhaus, Heinrich Theodor, 2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC *Embedded Processor-Based Self-Test* Dimitris Gizopoulos, A. Paschalis, Yervant Zorian, 2013-03-09 Embedded Processor Based Self Test is a guide to self testing strategies for embedded processors Embedded processors are regularly used today in most System on Chips SoCs Testing of microprocessors and embedded processors has always been a challenge because most traditional testing techniques fail when applied to them This is due to the complex sequential structure of processor architectures which consists of high performance datapath units and sophisticated control logic for performance optimization Structured Design for Testability DfT and hardware based self testing techniques which usually have a non trivial impact on a circuit s performance size and power can not be applied without serious consideration and careful incorporation into the processor design Embedded Processor Based Self Test shows how the powerful embedded functionality that processors offer can be utilized as a self testing resource Through a discussion of different strategies the book emphasizes on the emerging area of Software Based Self Testing SBST SBST is based on the idea of execution of embedded software programs to perform self testing of the processor itself and its surrounding blocks in the SoC SBST is a low cost strategy in terms of overhead area speed power development effort and test application cost as it is applied using low cost low speed test equipment Embedded Processor Based Self Test can be used by designers DfT engineers test practitioners researchers and students working on

digital testing and in particular processor and SoC test This book sets the framework for comparisons among different SBST methodologies by discussing key requirements It presents successful applications of SBST to a number of embedded processors of different complexities and instruction set architectures *FPGAs and Parallel Architectures for Aerospace Applications* Fernanda Kastensmidt, Paolo Rech, 2015-12-07 This book introduces the concepts of soft errors in FPGAs as well as the motivation for using commercial off the shelf COTS FPGAs in mission critical and remote applications such as aerospace The authors describe the effects of radiation in FPGAs present a large set of soft error mitigation techniques that can be applied in these circuits as well as methods for qualifying these circuits under radiation Coverage includes radiation effects in FPGAs fault tolerant techniques for FPGAs use of COTS FPGAs in aerospace applications experimental data of FPGAs under radiation FPGA embedded processors under radiation and fault injection in FPGAs Since dedicated parallel processing architectures such as GPUs have become more desirable in aerospace applications due to high computational power GPU analysis under radiation is also discussed **Reliable Software for Unreliable Hardware** Semeen Rehman, Muhammad Shafique, Jörg Henkel, 2016-04-20 This book describes novel software concepts to increase reliability under user defined constraints The authors approach bridges for the first time the reliability gap between hardware and software Readers will learn how to achieve increased soft error resilience on unreliable hardware while exploiting the inherent error masking characteristics and error stemming from soft errors aging and process variations mitigations potential at different software layers **Fault Detection** Wei Zhang, 2010-03-01 In this book a number of innovative fault diagnosis algorithms in recently years are introduced These methods can detect failures of various types of system effectively and with a relatively high significance Index of Conference Proceedings British Library. Document Supply Centre, 2003 Gain-Cell Embedded DRAMs for Low-Power VLSI Systems-on-Chip Pascal Meinerzhagen, Adam Teman, Robert Gitterman, Noa Edri, Andreas Burg, Alexander Fish, 2017-07-06 This book pioneers the field of gain cell embedded DRAM GC eDRAM design for low power VLSI systems on chip SoCs Novel GC eDRAMs are specifically designed and optimized for a range of low power VLSI SoCs ranging from ultra low power to power aware high performance applications After a detailed review of prior art GC eDRAMs an analytical retention time distribution model is introduced and validated by silicon measurements which is key for low power GC eDRAM design The book then investigates supply voltage scaling and near threshold voltage NTV operation of a conventional gain cell GC before presenting novel GC circuit and assist techniques for NTV operation including a 3 transistor full transmission gate write port reverse body biasing RBB and a replica technique for optimum refresh timing Next conventional GC bitcells are evaluated under aggressive technology and voltage scaling down to the subthreshold domain before novel bitcells for aggressively scaled CMOS nodes and soft error tolerance as presented including a 4 transistor GC with partial internal feedback and a 4 transistor GC with built in redundancy IoT and Cloud Computing Advancements in Vehicular Ad-Hoc Networks Rao, Ram Shringar, Jain, Vishal, Kaiwartya, Omprakash, Singh,

Nanhay,2020-03-20 The optimization of traffic management operations has become a considerable challenge in today's global scope due to the significant increase in the number of vehicles traffic congestions and automobile accidents Fortunately there has been substantial progress in the application of intelligent computing devices to transportation processes Vehicular ad hoc networks VANETs are a specific practice that merges the connectivity of wireless technologies with smart vehicles Despite its relevance empirical research is lacking on the developments being made in VANETs and how certain intelligent technologies are being applied within transportation systems IoT and Cloud Computing Advancements in Vehicular Ad Hoc Networks provides emerging research exploring the theoretical and practical aspects of intelligent transportation systems and analyzing the modern techniques that are being applied to smart vehicles through cloud technology Featuring coverage on a broad range of topics such as health monitoring node localization and fault tolerance this book is ideally designed for network designers developers analysts IT specialists computing professionals researchers academics and post graduate students seeking current research on emerging computing concepts and developments in vehicular ad hoc networks

Handbook of Research on Embedded Systems Design Bagnato, Alessandra,Indrusiak, Leandro Soares,Quadri, Imran Rafiq,Rossi, Matteo,2014-06-30 As real time and integrated systems become increasingly sophisticated issues related to development life cycles non recurring engineering costs and poor synergy between development teams will arise The Handbook of Research on Embedded Systems Design provides insights from the computer science community on integrated systems research projects taking place in the European region This premier references work takes a look at the diverse range of design principles covered by these projects from specification at high abstraction levels using standards such as UML and related profiles to intermediate design phases This work will be invaluable to designers of embedded software academicians students practitioners professionals and researchers working in the computer science industry

Robust Intelligent Systems Alfons Schuster,2008-08-06 Our time recognizes robustness as an important all pervading feature in the world around us Despite its omnipresence robustness is not entirely understood rather difficult to define and despite its obvious value in many situations rather difficult to achieve One of the goals of this edited book is to report on the topic of robustness from a variety and diverse range of fields and perspectives We are interested for instance in fundamental strategies nature applies to make systems robust and arguably intelligent and how these strategies may hold as general design principles in modern technology A particular focus is on computer based systems and applications This in mind the book has four main sections Part I has a look at robustness in terms of underlying technologies and infrastructures upon which many computer based intelligent systems reside and investigates robustness on the hardware and software level but also in larger environments such as the Internet and self managing systems The contributions in Part II target robustness in research areas that are inspired by biology including brain computer interfaces biological networks and biological immune systems for example Part III involves the exciting field of artificial intelligence The chapters here discuss the value of

robustness as a general design principle for artificial intelligence stressing its potential in areas such as humanoid robotics and image processing

Computer Safety, Reliability, and Security Bettina Buth, Gerd Rabe, Till Seyfarth, 2009-09-03

Computer based systems have become omnipresent commodities within our environment. While for a large variety of these systems such as transportation systems, nuclear or chemical plants or medical systems, their relation to safety is obvious, we often do not reflect that others are as directly related to risks concerning harm done to persons or matter as for example elevator control or mobile phones. At least we are not aware of the risk in our daily use of them. Safecomp as a community and a conference series has accompanied this development for 30 years up to Safecomp 2009 which was the 28th of the series. During this time the topics and methods as well as the community have undergone changes. These changes reflect the requirements of the above mentioned ubiquitous presence of safety related systems. Safecomp has always encouraged and will further encourage academia and industry to share and exchange their ideas and experiences. After 30 years we as the organizers of Safecomp 2009 found it imperative to take stock which methods found their way into the application areas which new approaches need to be checked for their practical applicability. As different application domains developed their own approaches over the previous decades we tried to attract people with different backgrounds for this conference. Though the years 2008 and 2009 were not easy with regard to the overall global economic situation we succeeded with this goal.

Analog Circuit Design Herman Casier, Michiel Steyaert, Arthur H.M. van Roermund, 2011-02-01

Analog Circuit Design contains the contribution of 18 tutorials of the 19th workshop on Advances in Analog Circuit Design. Each part discusses a specific to date topic on new and valuable design ideas in the area of analog circuit design. Each part is presented by six experts in that field and state of the art information is shared and overviewed. This book is number 20 in this successful series of *Analog Circuit Design* providing valuable information and excellent overviews of Robust Design chaired by Herman Casier, Consultant Sigma Delta Converters chaired by Prof. Michiel Steyaert, Catholic University Leuven, RFID chaired by Prof. Arthur van Roermund, Eindhoven University of Technology. *Analog Circuit Design* is an essential reference source for analog circuit designers and researchers wishing to keep abreast with the latest development in the field. The tutorial coverage also makes it suitable for use in an advanced design course.

Field Programmable Logic and Application Jürgen Becker, Marco Platzner, Serge Vernalde, 2004-08-19

This book constitutes the refereed proceedings of the 14th International Conference on Field Programmable Logic (FPL 2003) held in Leuven, Belgium, in August-September 2004. The 78 revised full papers, 45 revised short papers and 29 poster abstracts presented together with 3 keynote contributions and 3 tutorial summaries were carefully reviewed and selected from 285 papers submitted. The papers are organized in topical sections on organic and biologic computing, security and cryptography, platform based design, algorithms and architectures, acceleration, application architecture, physical design, arithmetic, multitasking, circuit technology, network processing, testing, applications, signal processing, computational models and compiler, dynamic reconfiguration, networks and optimisation, algorithms, system on

chip high speed design image processing network on chip power aware design IP based design co processing architectures system level design physical interconnect computational models cryptography and compression network applications and architecture and debugging and test **Advances in Intelligent Information Hiding and Multimedia Signal**

Processing Jeng-Shyang Pan, Pei-Wei Tsai, Junzo Watada, Lakhmi C. Jain, 2017-07-15 This volume includes papers presented at IIH MSP 2017 the 13th International Conference on Intelligent Information Hiding and Multimedia Signal Processing held from 12 to 15 August 2017 in Matsue Shimane Japan The conference addresses topics ranging from information hiding and security and multimedia signal processing and networking to bio inspired multimedia technologies and systems This volume of Smart Innovation Systems and Technologies focuses on subjects related to massive image video compression and transmission for emerging networks advances in speech and language processing information hiding and signal processing for audio and speech signals intelligent distribution systems and applications recent advances in security and privacy for multimodal network environments multimedia signal processing and machine learning Updated with the latest research outcomes and findings the papers presented appeal to researchers and students who are interested in the corresponding fields *Proceedings*, 2005 *General System Theory: Perspectives in Philosophy and Approaches in Complex Systems* Gianfranco Minati, Eliano Pessa, Ignazio Licata, 2018-07-09 This book is a printed edition of the Special Issue Second Generation General System Theory Perspectives in Philosophy and Approaches in Complex Systems that was published in Systems

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