

Semiconductor Device Modeling with SPICE

SECOND EDITION



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Semiconductor Device Modeling With Spice

Paolo Antognetti



Semiconductor Device Modeling With Spice:

Semiconductor Device Modeling with SPICE Giuseppe Massobrio, Paolo Antognetti, 1993 How to stimulate circuits faster and better with SPICE Table of Contents PN Junction Diode And Schottky Diode Bipolar Junction Transistor BJT Junction Field Effect Transistor JFET The MOS Transistor BJT Parameter Measurements MOS Parameter Measurements Noise and Distortion The SPICE Program MESFET ISFET And Thyristor Devices Appendix A The Two Terminal PN Structure Appendix B The Two Terminal MOS Structure Appendix C MS Junctions Index 100 illustrations **Semiconductor device modeling with SPICE** Giuseppe Massobrio, 1993 **Semiconductor Device Modeling with SPICE** Paolo Antognetti, 1988

Introduction to Device Modeling and Circuit Simulation Tor A. Fjeldly, Trond Ytterdal, Michael S. Shur, 1998 This book is a useful reference for practicing electrical engineers as well as a textbook for a junior senior or graduate level course in electrical engineering The authors combine two subjects device modeling and circuit simulation by providing a large number of well prepared examples of circuit simulations immediately following the description of many device models

Semiconductor Device Modelling Christopher M. Snowden, 2012-12-06 Semiconductor device modelling has developed in recent years from being solely the domain of device physicists to span broader technological disciplines involved in device and electronic circuit design and development The rapid emergence of very high speed high density integrated circuit technology and the drive towards high speed communications has meant that extremely small scale device structures are used in contemporary designs The characterisation and analysis of these devices can no longer be satisfied by electrical measurements alone Traditional equivalent circuit models and closed form analytical models cannot always provide consistently accurate results for all modes of operation of these very small devices Furthermore the highly competitive nature of the semiconductor industry has led to the need to minimise development costs and lead time associated with introducing new designs This has meant that there has been a greater demand for models capable of increasing our understanding of how these devices operate and capable of predicting accurate quantitative results The desire to move towards computer aided design and expert systems has reinforced the need for models capable of representing device operation under DC small signal large signal and high frequency operation It is also desirable to relate the physical structure of the device to the electrical performance This demand for better models has led to the introduction of improved equivalent circuit models and a upsurge in interest in using physical models **Springer Handbook of Semiconductor Devices**

Massimo Rudan, Rossella Brunetti, Susanna Reggiani, 2022-11-10 This Springer Handbook comprehensively covers the topic of semiconductor devices embracing all aspects from theoretical background to fabrication modeling and applications Nearly 100 leading scientists from industry and academia were selected to write the handbook's chapters which were conceived for professionals and practitioners material scientists physicists and electrical engineers working at universities industrial R D and manufacturers Starting from the description of the relevant technological aspects and fabrication steps the handbook

proceeds with a section fully devoted to the main conventional semiconductor devices like e g bipolar transistors and MOS capacitors and transistors used in the production of the standard integrated circuits and the corresponding physical models In the subsequent chapters the scaling issues of the semiconductor device technology are addressed followed by the description of novel concept based semiconductor devices The last section illustrates the numerical simulation methods ranging from the fabrication processes to the device performances Each chapter is self contained and refers to related topics treated in other chapters when necessary so that the reader interested in a specific subject can easily identify a personal reading path through the vast contents of the handbook

Semiconductor Physics: A Formula Handbook N.B. Singh, Semiconductor Physics A Formula Handbook is an indispensable guide that distills the complex principles of semiconductor physics into clear and concise formulas Covering essential topics such as band theory carrier transport semiconductor devices and optoelectronic phenomena this handbook provides quick access to key equations and principles needed for understanding semiconductor behavior and designing electronic and optoelectronic devices Whether you re a student researcher or industry professional in the field of electrical engineering or materials science this book serves as a valuable reference for mastering the fundamental aspects of semiconductor physics and its applications in modern technology

The Electrical Engineering Handbook, Second Edition Richard C. Dorf, 1997-09-26 In 1993 the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work Now this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today Every electrical engineer should have an opportunity to expand his expertise with this definitive guide In a single volume this handbook provides a complete reference to answer the questions encountered by practicing engineers in industry government or academia This well organized book is divided into 12 major sections that encompass the entire field of electrical engineering including circuits signal processing electronics electromagnetics electrical effects and devices and energy and the emerging trends in the fields of communications digital devices computer engineering systems and biomedical engineering A compendium of physical chemical material and mathematical data completes this comprehensive resource Every major topic is thoroughly covered and every important concept is defined described and illustrated Conceptually challenging but carefully explained articles are equally valuable to the practicing engineer researchers and students A distinguished advisory board and contributors including many of the leading authors professors and researchers in the field today assist noted author and professor Richard Dorf in offering complete coverage of this rapidly expanding field No other single volume available today offers this combination of broad coverage and depth of exploration of the topics The Electrical Engineering Handbook will be an invaluable resource for electrical engineers for years to come

MOSFET Models for VLSI Circuit Simulation Narain D. Arora, 2012-12-06 Metal Oxide Semiconductor MOS transistors are the basic building block of MOS integrated circuits I C Very Large Scale Integrated VLSI circuits using MOS technology have

emerged as the dominant technology in the semiconductor industry Over the past decade the complexity of MOS IC s has increased at an astonishing rate This is realized mainly through the reduction of MOS transistor dimensions in addition to the improvements in processing Today VLSI circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0.5 microns are in volume production Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator This problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models There is extensive literature available on modeling these short channel devices However there is a lot of confusion too Often it is not clear what model to use and which model parameter values are important and how to determine them After working over 15 years in the field of semiconductor device modeling I have felt the need for a book which can fill the gap between the theory and the practice of MOS transistor modeling This book is an attempt in that direction

Low-power HF Microelectronics Gerson A. S. Machado, 1996 This book brings together innovative modelling simulation and design techniques in CMOS SOI GaAs and BJT to achieve successful high yield manufacture for low power high speed and reliable by design analogue and mixed mode integrated systems

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