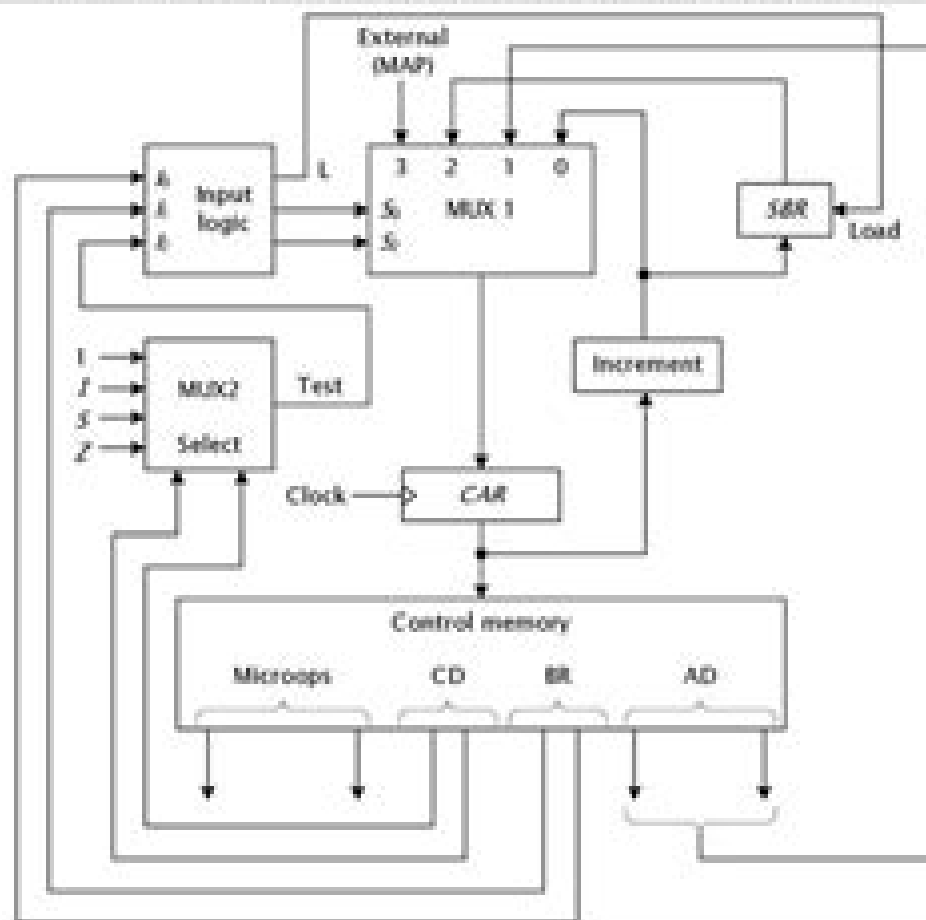


Design of Control Unit

■ Microprogram sequencer

Fig. 7-8 Microprogram sequence for a control memory



BR Field	Input A, I, S, Z	MUX S_1, S_0	Load SBR L
0 0	0 0 0	0 0	0
0 0	0 0 1	0 1	0
0 1	0 1 0	0 0	0
0 1	0 1 1	0 1	1
1 0	1 0 X	1 0	0
1 0	1 1 X	1 1	0

Principles Of Firmware Engineering In Microprogram Control Digital System

Rachel S Tattersall



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