



Low-Voltage, Low-Power Digital BiCMOS Circuits

Circuit Design, Comparative Study and Sensitivity Analysis

- BiCMOS design for today's fastest-growing applications
- Design concepts, in-depth analyses, and new methodologies
- Choosing the best BiCMOS circuit for your application
- Predicting and managing circuit performance sensitivities

SAMIR S. ROFAIL & KIAT-SENG YEO

Low Voltage Low Power Digital Bicmos Circuits Circuit Design Comparative Study And Sensitivity

Thomas Griffiths



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high performance VLSI circuits CMOS BiCMOS and bipolar circuits are covered in depth including state of the art circuit structures Recent advances in both the computer and telecommunications industries demand high performance VLSI digital circuits Digital processing of signals demands high speed circuit techniques for the GHz range The design of such circuits represents a great challenge one that is amplified when the power supply is scaled down to 3.3 V Moreover the requirements of low power high performance circuits adds an extra dimension to the design of such circuits High Performance Digital VLSI Circuit Design is a self contained text introducing the subject of high performance VLSI circuit design and explaining the speed power tradeoffs The first few chapters of the book discuss the necessary background material in the area of device design and device modeling respectively High performance CMOS circuits are then covered especially the new all N logic dynamic circuits Propagation delay times of high speed bipolar CML and ECL are developed analytically to give a thorough understanding of various interacting process device and circuit parameters High current phenomena of bipolar devices are also addressed as these devices typically operate at maximum currents for limited device area Different new high performance BiCMOS circuits are presented and compared to their conventional counterparts These new circuits find direct applications in the areas of high speed adders frequency dividers sense amplifiers level shifters input output clock buffers and PLLs The book concludes with a few system application examples of digital high performance VLSI circuits Audience A vital reference for practicing IC designers Can be used as a text for graduate and senior undergraduate students in the area

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variations metrics power and noise management verification signal integrity and low power design techniques Some of the topics are transition aware global signaling TAGS the interoperability of EDA tools for sequential logic synthesis statistical methods for the determination of process corners power supply noise suppression via clock skew scheduling and the relation between SAT and BDDs for equivalence checking No subject index Annotation copyrighted by Book News Inc Portland OR

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Table of Contents Low Voltage Low Power Digital Bcmos Circuits Circuit Design Comparative Study And Sensitivity

1. Understanding the eBook Low Voltage Low Power Digital Bcmos Circuits Circuit Design Comparative Study And Sensitivity
 - The Rise of Digital Reading Low Voltage Low Power Digital Bcmos Circuits Circuit Design Comparative Study And Sensitivity
 - Advantages of eBooks Over Traditional Books
2. Identifying Low Voltage Low Power Digital Bcmos Circuits Circuit Design Comparative Study And Sensitivity
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Low Voltage Low Power Digital Bcmos Circuits Circuit Design Comparative Study And Sensitivity
 - User-Friendly Interface
4. Exploring eBook Recommendations from Low Voltage Low Power Digital Bcmos Circuits Circuit Design Comparative Study And Sensitivity

- Personalized Recommendations
 - Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity User Reviews and Ratings
 - Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity and Bestseller Lists
5. Accessing Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity Free and Paid eBooks
- Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity Public Domain eBooks
 - Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity eBook Subscription Services
 - Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity Budget-Friendly Options
6. Navigating Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity eBook Formats
- ePub, PDF, MOBI, and More
 - Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity Compatibility with Devices
 - Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity Enhanced eBook Features
7. Enhancing Your Reading Experience
- Adjustable Fonts and Text Sizes of Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
 - Highlighting and Note-Taking Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
 - Interactive Elements Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
8. Staying Engaged with Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
- Joining Online Reading Communities

- Participating in Virtual Book Clubs
- Following Authors and Publishers Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
- 9. Balancing eBooks and Physical Books Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
 - Setting Reading Goals Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
 - Fact-Checking eBook Content of Low Voltage Low Power Digital Bicomos Circuits Circuit Design Comparative Study And Sensitivity
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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