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Low Dielectric Constant Materials for IC Applications



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Low Dielectric Constant Materials For Ic Applications

**Matteo Sonza Reorda, Zebo
Peng, Massimo Violante**



Low Dielectric Constant Materials For Ic Applications:

Low Dielectric Constant Materials for IC Applications Paul S. Ho, Jihperng Leu, Wei William Lee, 2003 Low dielectric constant materials are an important component of microelectronic devices This comprehensive book covers the latest low dielectric constant low k materials technology thin film materials characterization integration and reliability for back end interconnects and packaging applications in microelectronics Highly informative contributions from leading academic and industrial laboratories provide comprehensive information about materials technologies for *Low Dielectric Constant Materials for IC Applications* Paul S. Ho, Jihperng Leu, Wei William Lee, 2012-12-06 Low dielectric constant materials are an important component of microelectronic devices This comprehensive book covers the latest low dielectric constant low k materials technology thin film materials characterization integration and reliability for back end interconnects and packaging applications in microelectronics Highly informative contributions from leading academic and industrial laboratories provide comprehensive information about materials technologies for Handbook of Low and High Dielectric Constant Materials and Their Applications, Two-Volume Set Hari Singh Nalwa, 1999-09-07 Recent developments in microelectronics technologies have created a great demand for interlayer dielectric materials with a very low dielectric constant They will play a crucial role in the future generation of IC devices VLSI ULSI and high speed IC packaging Considerable efforts have been made to develop new low as well as high dielectric constant materials for applications in electronics industries Besides achieving either low or high dielectric constants other materials properties such as good processability high mechanical strength high thermal and environmental stability low thermal expansion low current leakage low moisture absorption corrosion resistant etc are of equal importance Many chemical and physical strategies have been employed to get desired dielectric materials with high performance This is a rapidly growing field of science both in novel materials and their applications to future packing technologies The experimental data on inorganic and organic materials having low or high dielectric constant remain scattered in the literature It is timely therefore to consolidate the current knowledge on low and high dielectric constant materials into a single reference source Handbook of Low and High Dielectric Constant Materials and Their Applications is aimed at bringing together under a single cover in two volumes all low and high dielectric constant materials currently studied in academic and industrial research covering all aspects of inorganic and organic materials from their synthetic chemistry processing techniques physics structure property relationship to applications in IC devices This book will summarize the current status of the field covering important scientific developments made over the past decade with contributions from internationally recognized experts from all over the world Fully cross referenced this book has clear precise and wide appeal as an essential reference source for all those interested in low and high dielectric constant material *Introduction to Organic Electronic and Optoelectronic Materials and Devices* Sam-Shajing Sun, Larry R. Dalton, 2016-10-03 This book covers the combined subjects of organic electronic and optoelectronic materials devices It is designed for classroom instruction at

the senior college level Highlighting emerging organic and polymeric optoelectronic materials and devices it presents the fundamentals principle mechanisms representative examples and key data *Low-dielectric Constant Materials-- Synthesis and Applications in Microelectronics* Toh-Ming Lu,1995 *Low-dielectric Constant Materials* ,1999 **Metal-Dielectric Interfaces in Gigascale Electronics** Ming He,Toh-Ming Lu,2012-02-02 Metal dielectric interfaces are ubiquitous in modern electronics As advanced gigascale electronic devices continue to shrink the stability of these interfaces is becoming an increasingly important issue that has a profound impact on the operational reliability of these devices In this book the authors present the basic science underlying the thermal and electrical stability of metal dielectric interfaces and its relationship to the operation of advanced interconnect systems in gigascale electronics Interface phenomena including chemical reactions between metals and dielectrics metallic atom diffusion and ion drift are discussed based on fundamental physical and chemical principles Schematic diagrams are provided throughout the book to illustrate interface phenomena and the principles that govern them Metal Dielectric Interfaces in Gigascale Electronics provides a unifying approach to the diverse and sometimes contradictory test results that are reported in the literature on metal dielectric interfaces The goal is to provide readers with a clear account of the relationship between interface science and its applications in interconnect structures The material presented here will also be of interest to those engaged in field effect transistor and memristor device research as well as university researchers and industrial scientists working in the areas of electronic materials processing semiconductor manufacturing memory chips and IC design **Smart Power ICs** Bruno Murari,2002-06-13 This book provides a survey of the state of the art of technology and future trends in the new family of Smart Power ICs and describes design and applications in a variety of fields ranging from automotive to telecommunications reliability evaluation and qualification procedures The book is a valuable source of information and reference for both power IC design specialists and to all those concerned with applications the development of digital circuits and with system architecture **Copper Interconnect Technology** Tapan Gupta,2010-01-22 Since overall circuit performance has depended primarily on transistor properties previous efforts to enhance circuit and system speed were focused on transistors as well During the last decade however the parasitic resistance capacitance and inductance associated with interconnections began to influence circuit performance and will be the primary factors in the evolution of nanoscale ULSI technology Because metallic conductivity and resistance to electromigration of bulk copper Cu are better than aluminum use of copper and low k materials is now prevalent in the international microelectronics industry As the feature size of the Cu lines forming interconnects is scaled resistivity of the lines increases At the same time electromigration and stress induced voids due to increased current density become significant reliability issues Although copper low k technology has become fairly mature there is no single book available on the promise and challenges of these next generation technologies In this book a leader in the field describes advanced laser systems with lower radiation wavelengths photolithography materials and mathematical modeling approaches

to address the challenges of Cu interconnect technology

Power Management of Digital Circuits in Deep Sub-Micron CMOS Technologies Stephan Henzler, 2006-11-24 In the deep sub micron regime the power consumption has become one of the most important issues for competitive design of digital circuits Due to dramatically increasing leakage currents the power consumption does not take advantage of technology scaling as before State of art power reduction techniques like the use of multiple supply and threshold voltages transistor stack forcing and power gating are discussed with respect to implementation and power saving capability Focus is given especially on technology dependencies process variations and technology scaling Design and implementation issues are discussed with respect to the trade off between power reduction performance degradation and system level constraints A complete top down design flow is demonstrated for power gating techniques introducing new design methodologies for the switch sizing task and circuit blocks for data retention and block activation The leakage reduction ratio and the minimum power down time are introduced as figures of merit to describe the power gating technique on system level and give a relation to physical circuit parameters Power Management of Digital Circuits in Deep Sub Micron CMOS Technologies mainly deals with circuit design but also addresses the interface between circuit and system level design on the one side and between circuit and physical design on the other side [Introduction to Microfabrication](#)

[Sami Franssila](#), 2010-10-29 This accessible text is now fully revised and updated providing an overview of fabrication technologies and materials needed to realize modern microdevices It demonstrates how common microfabrication principles can be applied in different applications to create devices ranging from nanometer probe tips to meter scale solar cells and a host of microelectronic mechanical optical and fluidic devices in between Latest developments in wafer engineering patterning thin films surface preparation and bonding are covered This second edition includes expanded sections on MEMS and microfluidics related fabrication issues new chapters on polymer and glass microprocessing as well as serial processing techniques 200 completely new and 200 modified figures more coverage of imprinting techniques process integration and economics of microfabrication 300 homework exercises including conceptual thinking assignments order of magnitude estimates standard calculations and device design and process analysis problems solutions to homework problems on the complementary website as well as PDF slides of the figures and tables within the book With clear sections separating basic principles from more advanced material this is a valuable textbook for senior undergraduate and beginning graduate students wanting to understand the fundamentals of microfabrication The book also serves as a handy desk reference for practicing electrical engineers materials scientists chemists and physicists alike www.wiley.com/go/Franssila_Micro2e

System-level Test and Validation of Hardware/Software Systems Matteo Sonza Reorda, Zebo Peng, Massimo Violante, 2006-03-30 New manufacturing technologies have made possible the integration of entire systems on a single chip This new design paradigm termed system on chip SOC together with its associated manufacturing problems represents a real challenge for designers SOC is also reshaping approaches to test and validation activities These are beginning to migrate

from the traditional register transfer or gate levels of abstraction to the system level Until now test and validation have not been supported by system level design tools so designers have lacked the infrastructure to exploit all the benefits stemming from the adoption of the system level of abstraction Research efforts are already addressing this issue This monograph provides a state of the art overview of the current validation and test techniques by covering all aspects of the subject including modeling of bugs and defects stimulus generation for validation and test purposes including timing errors design for testability

Integrated CMOS Circuits for Optical Communications Mark Ingels, Michiel Steyaert, 2013-03-09 This work investigates the feasibility of the integration of interface circuits for optical communication systems in a standard unmodified digital CMOS process This paves the way for single chip communication systems where the optical interfaces are integrated on the same die as the required digital circuitry The optical receiver is a key element in the optical communication link In this work a transimpedance amplifier which consists of a voltage amplifier with resistive feedback is used as the first stage Unlike for many other circuits the optimal place of its dominant pole is the input node It is also demonstrated that a high gain of the voltage amplifier is primordial to obtain good performances and that this may be obtained through the use of multiple stages Noise aspects are investigated and the conclusion is drawn that the amplifier's input capacitance can be smaller than the photodiode's capacitance for optimal performance

Silicon Optoelectronic Integrated Circuits Horst Zimmermann, 2013-03-09 Explains the circuit design of silicon optoelectronic integrated circuits OEICs which are central to advances in wireless and wired telecommunications The essential features of optical absorption are summarized as is the device physics of photodetectors and their integration in modern bipolar CMOS and BiCMOS technologies This information provides the basis for understanding the underlying mechanisms of the OEICs described in the main part of the book In order to cover the topic comprehensively Silicon Optoelectronic Integrated Circuits presents detailed descriptions of many OEICs for a wide variety of applications from various optical sensors smart sensors 3D cameras and optical storage systems DVD to fiber receivers in deep sub m CMOS Numerous detailed illustrations help to elucidate the material

Polyimides and Other High Temperature Polymers: Synthesis, Characterization and Applications, Volume 4 Kash L.

Mittal, 2007-07-10 This book is mostly based on papers presented at the Fourth International Symposium on this topic held in Savannah Georgia However in addition to these papers certain very relevant papers have also been included to broaden the scope and thus enhance the value of this book Currently there is tremendous interest in these material because of their

Gettering Defects in Semiconductors Victor A. Perevostchikov, Vladimir D. Skoupov, 2005-09-15 Gettering Defects in Semiconductors fulfills three basic purposes to systematize the experience and research in exploiting various gettering techniques in microelectronics and nanoelectronics to identify new directions in research particularly to enhance the perspective of professionals and young researchers and specialists to fill a gap in the contemporary literature on the underlying semiconductor material theory The authors address not only well established gettering techniques but also

describe contemporary trends in gettering technologies from an international perspective The types and properties of structural defects in semiconductors their generating and their transforming mechanisms during fabrication are described The primary emphasis is placed on classifying and describing specific gettering techniques their specificity arising from both their position in a general technological process and the regimes of their application This book addresses both engineers and material scientists interested in semiconducting materials theory and also undergraduate and graduate students in solid state microelectronics and nanoelectronics A comprehensive list of references provides readers with direction for further reading

Thin Film Materials, Processes, and Reliability Electrochemical Society. Meeting, 2001

Lumped Elements for RF and Microwave Circuits, Second Edition Inder J. Bahl, 2022-12-31 Fully updated and including entirely new chapters this Second Edition provides in depth coverage of the different types of RF and microwave circuit elements including inductors capacitors resistors transformers via holes airbridges and crossovers Featuring extensive formulas for lumped elements design trade offs and an updated and current list of references the book helps you understand the value and usefulness of lumped elements in the design of RF microwave and millimeter wave components and circuits You ll find a balanced treatment between standalone lumped elements and their circuits using MICs MMICs and RFICs technologies You ll also find detailed information on a broader range RFICs that was not available when the popular first edition was published The book captures in one consolidated volume the fundamentals equations modeling examples references and overall procedures to design test and produce microwave components that are indispensable in industry and academia today With its superb organization and expanded coverage of the subject this is a must have go to resource for practicing engineers and researchers in industry government and university and microwave engineers working in the antenna area Students will also find it a useful reference with its clear explanations many examples and practical modeling guidelines

Metal-Polymer Systems Jörg Florian Friedrich, 2017-12-04 The result of decades of research by a pioneer in the field this is the first book to deal exclusively with achieving high performance metal polymer composites by chemical bonding Covering both the academic and practical aspects the author focuses on the chemistry of interfaces between metals and polymers with a particular emphasis on the chemical bonding between the different materials He elucidates the various approaches to obtaining a stable interface including but not limited to thermodynamically driven redox reactions bond protection to prevent hydrolysis the introduction of barrier layers and stabilization by spacer molecules Throughout chemical bonding is promoted as a simple and economically viable alternative to adhesion based on reversible weak physical interaction Consequently the text equips readers with the practical tools necessary for designing high strength metal polymer composites with such desired properties as resilience flexibility rigidity or degradation resistance

Detection and Signal Processing Wilhelmus Jacobus Witteman, 2007-04-14 This comprehensive monograph deals with detectors signal processors and related noise phenomena Detailed quantitative analyses are developed in a consistent format for thermal detectors vacuum detectors

semiconductor detectors and avalanche detectors as well as their accompanying noise currents For signal processing applications the monograph treats in detail the operational amplifier signal averagers waveform analyzers correlation techniques and heterodyne detection Several original extensions are reported especially for correlation devices and heterodyne detection with noise rejection In addition results of analyses are illustrated with examples of operating systems and of applications in space communication and laser radar

The Enigmatic Realm of **Low Dielectric Constant Materials For Ic Applications**: Unleashing the Language is Inner Magic

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