

# PRINCIPLES AND TECHNOLOGY OF MODFETS

H. MORKOÇ · H. UNLU · G. JI

VOLUME 1



DESIGN AND MEASUREMENT IN  
ELECTRONIC ENGINEERING



# Principles And Technology Of Modfets 2 Volumes

**Nicolas Patin**



## **Principles And Technology Of Modfets 2 Volumes:**

Principles and Technology of MODFETs Hadis Morkoç, H. Unlu, G. Ji, 1991-07-09 An acknowledged world authority on modulation doped field effect transistors MODFETs offers a detailed comparison of MODFETs performance both as microwave and digital devices with other structures Concentrates on basic aspects of design and measurement in electronic engineering Introductory material on heterojunction and semiconductor physics include crystalline structures dynamics of interfaces carrier densities band discontinuities plus the treatment of stress and strain and their effect on the band structures *Principles and Technology of MODFETs* Hadis Morkoç, 1991 **Proceedings of the Multi-Conference 2011** Himanshu B. Soni, Apurva Shah, 2011-06-06 The International Conference on Signals Systems and Automation ICSSA 2011 aims to spread awareness in the research and academic community regarding cutting edge technological advancements revolutionizing the world The main emphasis of this conference is on dissemination of information experience and research results on the current topics of interest through in depth discussions and participation of researchers from all over the world The objective is to provide a platform to scientists research scholars and industrialists for interacting and exchanging ideas in a number of research areas This will facilitate communication among researchers in different fields of Electronics and Communication Engineering The International Conference on Intelligent System and Data Processing ICISD 2011 is organized to address various issues that will foster the creation of intelligent solutions in the future The primary goal of the conference is to bring together worldwide leading researchers developers practitioners and educators interested in advancing the state of the art in computational intelligence and data processing for exchanging knowledge that encompasses a broad range of disciplines among various distinct communities Another goal is to promote scientific information interchange between researchers developers engineers students and practitioners working in India and abroad

**Encyclopedia of Plasma Technology - Two Volume Set** J. Leon Shohet, 2016-12-12 Technical plasmas have a wide range of industrial applications The Encyclopedia of Plasma Technology covers all aspects of plasma technology from the fundamentals to a range of applications across a large number of industries and disciplines Topics covered include nanotechnology solar cell technology biomedical and clinical applications electronic materials sustainability and clean technologies The book bridges materials science industrial chemistry physics and engineering making it a must have for researchers in industry and academia as well as those working on application oriented plasma technologies Also Available Online This Taylor E mail e reference taylorandfrancis com International Tel 44 0 20 7017 6062 E mail online sales tandf co uk **Nitride Semiconductors: Volume 482** Materials Research Society. Meeting, 1998-04-20 This book is on recent experimental and theoretical progress in the rapidly growing field of III V nitrides Issues related to crystal growth bulk and thin films structure and microstructure formation of defects doping alloying formation of heterostructures determination of physical properties and device fabrication and evaluation are addressed Papers show much progress in the growth and

understanding of III V nitrides and in the production of optoelectronic devices based on these materials Most exciting is the fact that light emitting diodes and laser diodes have now reached amazing levels of performance which forecasts a revolution in lighting optical storage printing and display technologies Topics include crystal growth bulk growth early stages of epitaxy crystal growth MOCVD growth techniques MBE and HVPE novel substrates and growth techniques structural properties electronic properties luminescence and recombination characterization elemental and stress analysis physical modelling device processing implantation annealing device characterization contacts degradation and injection laser diodes and applications *Planning and operation of hybrid renewable energy systems, volume II* Weihao Hu,Amjad

Anvari-Moghaddam,Liansong Xiong,Haoran Zhao,Yuefang Du,Di Cao,Sayed Abulanwar,2023-06-06 *Modern Television Practice Principles,Technology and Servicing 2/Ed* , **Field Effect Transistors, A Comprehensive Overview** Pouya

Valizadeh,2016-02-01 This book discusses modern day Metal Oxide Semiconductor Field Effect Transistors MOSFETs and future trends of transistor devices This book provides an overview of Field Effect Transistors FETs by discussing the basic principles of FETs and exploring the latest technological developments in the field It covers and connects a wide spectrum of topics related to semiconductor device physics physics of transistors and advanced transistor concepts This book contains six chapters Chapter 1 discusses electronic materials and charge Chapter 2 examines junctions discusses contacts under thermal equilibrium metal semiconductor contacts and metal insulator semiconductor systems Chapter 3 covers traditional planar Metal Oxide Semiconductor Field Effect Transistors MOSFETs Chapter 4 describes scaling driving technological variations and novel dimensions of MOSFETs Chapter 5 analyzes Heterojunction Field Effect Transistors FETs and also discusses the challenges and rewards of heteroepitaxy Finally Chapter 6 examines FETs at molecular scales Links the discussion of contemporary transistor devices to physical processes Material has been class tested in undergraduate and graduate courses on the design of integrated circuit components taught by the author Contains examples and end of chapter problems **Field Effect Transistors A Comprehensive Overview From Basic Concepts to Novel Technologies** is a reference for senior undergraduate graduate students and professional engineers needing insight into physics of operation of modern FETs Pouya Valizadeh is Associate Professor in the Department of Electrical and Computer Engineering at Concordia University in Quebec Canada He received B S and M S degrees with honors from the University of Tehran and Ph D degree from The University of Michigan Ann Arbor all in Electrical Engineering in 1997 1999 and 2005 respectively Over the past decade Dr Valizadeh has taught numerous sections of five different courses covering topics such as semiconductor process technology semiconductor materials and their properties advanced solid state devices transistor design for modern CMOS technology and high speed transistors **Handbook of Semiconductor Manufacturing Technology** Yoshio Nishi,Robert

Doering,2017-12-19 Retaining the comprehensive and in depth approach that cemented the bestselling first edition s place as a standard reference in the field the **Handbook of Semiconductor Manufacturing Technology Second Edition** features new

and updated material that keeps it at the vanguard of today's most dynamic and rapidly growing field. Iconic experts Robert Doering and Yoshio Nishi have again assembled a team of the world's leading specialists in every area of semiconductor manufacturing to provide the most reliable, authoritative and industry leading information available. Stay Current with the Latest Technologies. In addition to updates to nearly every existing chapter, this edition features five entirely new contributions on Silicon on insulator SOI materials and devices, Supercritical CO<sub>2</sub> in semiconductor cleaning, Low dielectrics, Atomic layer deposition, Damascene copper electroplating, Effects of terrestrial radiation on integrated circuits, ICs. Reflecting rapid progress in many areas, several chapters were heavily revised and updated and in some cases rewritten to reflect rapid advances in such areas as interconnect technologies, gate dielectrics, photomask fabrication, IC packaging and 300 mm wafer fabrication. While no book can be up to the minute with the advances in the semiconductor field, the Handbook of Semiconductor Manufacturing Technology keeps the most important data, methods, tools and techniques close at hand.

**Physics Briefs**, 1992      *Transient Floating-Body Effects for Memory Applications in Fully-Depleted SOI MOSFETs*  
Maryline Bawedin, 2007. Memory devices based on floating body effects (FBE) in Silicon on Insulator (SOI) technology are among the most promising candidates for sub 100nm and low power Dynamic Random Access Memory (DRAM). This new type of DRAMs called Zero Capacitor RAM (Z-RAM) uses only one transistor in partially depleted PD-SOI technology and takes advantage of FBE which have been considered as parasitic phenomena until now. The Z-RAM programming principles are based on the threshold voltage ( $V_{TH}$ ) variations induced by the excess or lack of majority carriers in the floating body. In this dissertation, a new floating body effect, the Transient Floating Body Potential Effect (TFBPE) based on the body majority carriers non equilibrium and on the dual dynamic gate coupling in standard fully depleted FD-SOI MOSFETs is presented for the first time. The TFBPE occurs in a specific gate bias range and can induce strong hysteresis of the gate and drain current characteristics, although the FD-SOI transistors are usually known to be immune against the FBE and their aftermaths. Adapted from the same physics principles as in the drain current hysteresis that we called the Meta Stable Dip (MSD) effect, a new concept of one transistor capacitor less memory was also proposed: the Meta Stable DRAM (MSDRAM) which is dedicated for double gate operations. All the experimental results and physics interpretations were supported by 2D numerical simulations. A 1D semi-analytical model of the body potential for non equilibrium states was also proposed. For the first time, this original body potential model takes into account the majority carriers density variations, i.e. the quasi-Fermi level non equilibrium versus a transient gate voltage scan in a FD-MOS device.      Comprehensive Semiconductor Science and Technology, 2011-01-28. Semiconductors are at the heart of modern living. Almost everything we do, be it work, travel, communication or entertainment, all depend on some feature of semiconductor technology. Comprehensive Semiconductor Science and Technology Six Volume Set captures the breadth of this important field and presents it in a single source to the large audience who study, make and exploit semiconductors. Previous attempts at this achievement have been abbreviated.

and have omitted important topics Written and Edited by a truly international team of experts this work delivers an objective yet cohesive global review of the semiconductor world The work is divided into three sections The first section is concerned with the fundamental physics of semiconductors showing how the electronic features and the lattice dynamics change drastically when systems vary from bulk to a low dimensional structure and further to a nanometer size Throughout this section there is an emphasis on the full understanding of the underlying physics The second section deals largely with the transformation of the conceptual framework of solid state physics into devices and systems which require the growth of extremely high purity nearly defect free bulk and epitaxial materials The last section is devoted to exploitation of the knowledge described in the previous sections to highlight the spectrum of devices we see all around us Provides a comprehensive global picture of the semiconductor world Each of the work s three sections presents a complete description of one aspect of the whole Written and Edited by a truly international team of experts

**Power Electronics Applied to Industrial Systems and Transports, Volume 1** Nicolas Patin, 2015-04-09 Power electronics is based on the switching operating mode of semiconductor components On this basis the concepts of type voltage or current and reversibility of interconnected sources make it possible to apply a methodology for the synthesis of various types of converters Here the author presents the major types of components available always from a user s point of view with the gate drive fire control and other auxiliary circuits that are required for their proper functioning snubbers for example The different passive components capacitors coils and transformers are discussed as well as printed circuit technology especially in the aspect of their design This book also focuses on the importance of packaging by reviewing the electrical representation of components thermal models and the currently available electronics cooling technologies Modeling is discussed as well as different technological aspects used in the engineering design of an electronic power converter useful for obtaining satisfactory performance and reliability Presenting the essential notions in power electronics from both the theoretical and technological perspectives Dedicated chapters with a focus on connection rules reversibility and impact choices of switches for converter synthesis Presented from a user s perspective to enable you to apply the theory of power electronics to practical applications

MOSFET - Developments and Trends Yuxiang Tu, Raed Abd-Alhameed, Ashwain Rayit, 2024-11-13 This book covers a wide range of the latest innovations in MOSFETs including discussions of developments in various important mainstream MOSFETs It covers modeling and property studies in MOSFET fields including the compact model of the DMG GC DOTDGD cylindrical gate MOSFET and the electrophysical properties of nanofilms generated using the magnetron sputtering method The innovative analytical potential charge and current distributions in junctionless MOSFETs are conducted and the related differences between accurate and simplified models are discussed in detail Nanoscale devices design multiple solutions are also presented The book scope reflects the most recent MOSFET developments applications and trends which open up new horizons and present a perfect reference source for people working in MOSFET fields This book is also meaningful in

engineering fields especially in electronics engineering which includes radio frequency microwave electronics systems networks 5G 6G and so on Proceedings of the International Conference on Data Engineering and Communication Technology Suresh Chandra Satapathy,Vikrant Bhateja,Amit Joshi,2016-08-24 This two volume book contains research work presented at the First International Conference on Data Engineering and Communication Technology ICDECT held during March 10 11 2016 at Lavasa Pune Maharashtra India The book discusses recent research technologies and applications in the field of Computer Science Electrical and Electronics Engineering The aim of the Proceedings is to provide cutting edge developments taking place in the field data engineering and communication technologies which will assist the researchers and practitioners from both academia as well as industry to advance their field of study **High-Mobility Group-IV**

**Materials and Devices: Volume 809** Matty Caymax,2004-08-18 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners This book first published in 2004 brings together researchers interested in strained SiGe strain relaxed buffers strained Si on bulk Si and on SOI SiGe on SOI Ge substrates and Ge on insulator **The Mechatronics Handbook - 2 Volume Set** Robert H. Bishop,2018-10-08 The first comprehensive reference on mechatronics The Mechatronics Handbook was quickly embraced as the gold standard in the field From washing machines to coffeemakers to cell phones to the ubiquitous PC in almost every household what these days doesn't take advantage of mechatronics in its design and function In the scant five years since the initial publication of the handbook the latest generation of smart products has made this even more obvious Too much material to cover in a single volume Originally a single volume reference the handbook has grown along with the field The need for easy access to new material on rapid changes in technology especially in computers and software has made the single volume format unwieldy The second edition is offered as two easily digestible books making the material not only more accessible but also more focused Completely revised and updated Robert Bishop's seminal work is still the most exhaustive state of the art treatment of the field available Conference Proceedings of the 2024 4th International Joint Conference on Energy, Electrical and Power Engineering Yawei Hu,Wenping Cao,Cungang Hu,2025-05-19 Energy Electrical and Power Engineering are dynamic fields undergoing rapid change and innovation This volume encompasses cutting edge research and advances in electrical and power engineering covering a wide range of topics including power electronics technology renewable energy generation intelligent control systems and more With contributions from renowned experts and scholars it provides valuable insights and innovative solutions to address the challenges and opportunities in the ever evolving energy landscape This volume serves as a comprehensive resource for staying abreast of the latest trends and act as a catalyst for advancing this dynamic field Following the success of the CoEEPE 2021 2022 and 2023 this volume will provide resources for a diverse readership including professionals scientists practitioners researchers and graduate students *Reliability of high-k / metal gate field-effect transistors considering circuit operational constraints* Steve Kupke,2016-06-06 After many decades the scaling of

silicon dioxide based field effect transistors has reached insurmountable physical limits due to unintentional high gate leakage currents for gate oxide thicknesses below 2 nm. The introduction of high k metal gate stacks guaranteed the trend towards smaller transistor dimensions. The implementation of HfO<sub>2</sub> as high k dielectric also leads to a substantial number of manufacturing and reliability challenges. The deterioration of the gate oxide properties under thermal and electric stress jeopardizes the circuit operation and hence needs to be comprehensively understood. As a starting point, 6T static random access memory cells were used to identify the different single device operating conditions. The strongest deterioration of the gate stack was found for nMOS devices under positive bias temperature instability (PBTI) stress, resulting in a severe threshold voltage shift and increased gate leakage current. A detailed investigation of physical origin and temperature and voltage dependency was done. The reliability issues were caused by the electron trapping into already existing HfO<sub>2</sub> oxygen vacancies. The oxygen vacancies reside in different charge states depending on applied stress voltages. This in return also resulted in a strong threshold voltage and gate current relaxation after stress was cut off. The reliability assessment using constant voltage stress does not reflect realistic circuit operation which can result in a changed degradation behaviour. Therefore, the constant voltage stress measurements were extended by considering CMOS operational constraints where it was found that the supply voltage frequently switches between the gate and drain terminal. The additional drain off state bias leads to an increased  $V_t$  relaxation in comparison to zero bias voltage. The off state influence strongly depended on the gate length and became significant for short channel devices. The influence of the off state bias on the dielectric breakdown was studied and compared to the standard assessment methods. Different wear out mechanisms for drain only and alternating gate and drain stress were verified. Under drain only stress, the dielectric breakdown was caused by hot carrier degradation. The lifetime was correlated with the device length and amount of subthreshold leakage. The gate oxide breakdown under alternating gate and off state stress was caused by the continuous trapping and detrapping behaviour of high k metal gate devices.

Science and Technology of Semiconductor-On-Insulator Structures and Devices Operating in a Harsh Environment Denis Flandre, Alexei N. Nazarov, Peter L.F. Hemment, 2006-05-06. This proceedings volume archives the contributions of the speakers who attended the NATO Advanced Research Workshop on Science and Technology of Semiconductor On Insulator Structures and Devices Operating in a Harsh Environment held at the Sanatorium Puschka Ozerna, Kyiv, Ukraine from 25 to 29 April 2004. The semiconductor industry has maintained a very rapid growth during the last three decades through impressive technological achievements which have resulted in products with higher performance and lower cost per function. After many years of development, semiconductor on insulator materials have entered volume production and will increasingly be used by the manufacturing industry. The wider use of semiconductor, especially silicon on insulator materials, will not only enable the benefits of these materials to be further demonstrated but also will drive down the cost of substrates which in turn will stimulate the development of other novel devices and



applications In itself this trend will encourage the promotion of the skills and ideas generated by researchers in the Former Soviet Union and Eastern Europe and their incorporation in future collaborations

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