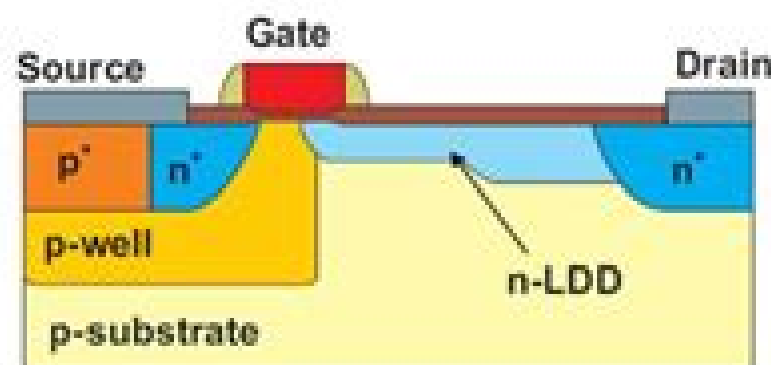


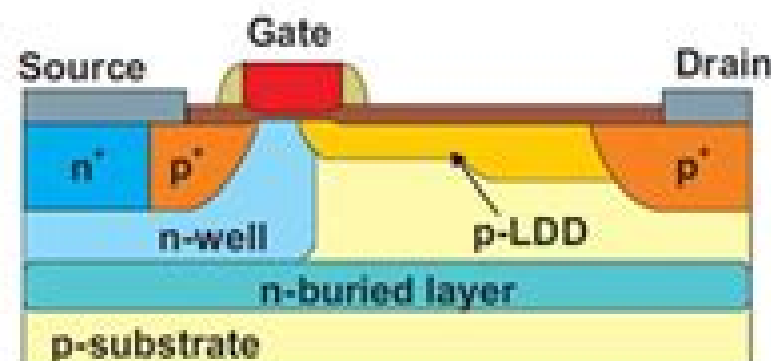
Ionisation Damage in LDMOS Transistors

- ❑ **LDMOS transistors will be used in DC-DC converters for the HL-LHC upgrade**



NLDMOS

$V_{DD} = 22\text{ V}$, $f_T = 20\text{ GHz}$



PLDMOS

$V_{DD} = -16\text{ V}$, $f_T = 10\text{ GHz}$

- ✓ Transistors from IHP Microelectronics (DE) (0.25 μm SGB25V GOD SiGe BiCMOS)
- ✓ Submitted to neutron irradiation (0.5, 5 and 10 Mrad)
- ❑ **First step:** Technological and electrical simulation of basic cell and fitting with experimental data
- ❑ **Second step:** Electric field distribution (oxides and semiconductors)

Radiation Damage In Semiconductors Proc

Wenbin Ji

A decorative graphic element consisting of a light blue horizontal bar with a rounded right end, and a red circular gradient shape partially visible behind it.

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Physics of Radiation Effects in Crystals R.A. Johnson,A.N. Orlov,2012-12-02 Physics of Radiation Effects in Crystals is presented in two parts The first part covers the general background and theory of radiation effects in crystals including the theory describing the generation of crystal lattice defects by radiation the kinetic approach to the study of the disposition of these defects and the effects of the diffusion of these defects on alloy compositions and phases Specific problems of current interest are treated in the second part and include anisotropic dimensional changes in x uranium zirconium and graphite acceleration of thermal creep in reactor materials and radiation damage of semiconductors and superconductors

Semiconductors and Semimetals ,2014-05-14 Semiconductors and Semimetals Second Symposium on Protection Against Radiations in Space Arthur Reetz,1965 *Neutron Bombardment Reduction of Transistor Current Gain* C. A. Goben,1964 *The Physics of Microfabrication* Ivor Brodie,Julius J. Muray,2013-11-11 The Physical Electronics Department of SRI International formerly Stanford Research Institute has been pioneering the development of devices fabricated to submicron tolerances for well over 20 years In 1961 a landmark paper on electron beam lithography and its associated technologies was published by K R Shoulderst then at SRI which set the stage for our subsequent efforts in this field He had the foresight to believe that the building of such small devices was actually within the range of human capabilities As a result of this initial momentum our experience in the technologies associated with microfabrication has become remarkably comprehensive despite the relatively small size of our research activity We have frequently been asked to deliver seminars or provide reviews on various aspects of micro fabrication These activities made us aware of the need for a comprehensive overview of the physics of microfabrication We hope that this book will fill that need *October 1* E Gutsche,K. Müller,P. Görlich,2022-07-18 No detailed description available for October 1 Compound Semiconductor Integrated Circuits Tho T. Vu,2003-01-01 This is the book version of a special issue of the International Journal of High Speed Electronics and Systems reviewing recent work in the field of compound semiconductor integrated circuits There are fourteen invited papers covering a wide range of applications frequencies and materials These papers deal with digital analog microwave and millimeter wave technologies devices and integrated circuits for wireline fiber optic lightwave transmissions and wireless radio frequency microwave and millimeter wave communications In each case the market is young and experiencing rapid growth for both commercial and millitary applications Many new semiconductor technologies compete for these new markets leading to an alphabet soup of semiconductor materials described in these papers The book also includes three papers focused on radiation effects and reliability in III V semiconductor electronics which are useful for reference and future directions Moreover reliability is covered in several papers separately for certain process technologies Contents Present and Future of High Speed Compound Semiconductor IC s T Otsuji The Transforming MMIC E J Martinez Distributed Amplifier for Fiber Optic Communication Systems H Shigematsu et al Microwave GaN Based Power Transistors on Large Scale Silicon Wafers S

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including those related to materials processing and devices Plenary and invited speakers address areas of major interest

The Physics of Micro/Nano-Fabrication Ivor Brodie, Julius J. Murray, 1992 In this revised and expanded edition the authors provide a comprehensive overview of the tools technologies and physical models needed to understand build and analyze microdevices Students specialists within the field and researchers in related fields will appreciate their unified presentation and extensive references

Deep Implants: Fundamentals and Applications G.G. Bentini, A. Golanski, S. Kalbitzer, 1989-01-01 Deep implants are produced by the high energy implantation of impurities in a host material The thus created subsurface layers have properties that are different from the very surface and the bulk and show great promise for application in the electronics industry

Miscellaneous Publication - National Bureau of Standards United States. National Bureau of Standards, 1934 Annual Report , 1967 *Nuclear Science Abstracts* , 1975-02

Publications, Reports, and Papers for 1961- from Oak Ridge National Laboratory Oak Ridge National Laboratory, 1964

Nanoscale Semiconductor Memories Santosh K. Kurinec, Krzysztof Iniewski, 2017-07-28 Nanoscale memories are used everywhere From your iPhone to a supercomputer every electronic device contains at least one such type With coverage of current and prototypical technologies Nanoscale Semiconductor Memories Technology and Applications presents the latest research in the field of nanoscale memories technology in one place It also covers a myriad of applications that nanoscale memories technology has enabled The book begins with coverage of SRAM addressing the design challenges as the technology scales then provides design strategies to mitigate radiation induced upsets in SRAM It discusses the current state of the art DRAM technology and the need to develop high performance sense amplifier circuitry The text then covers the novel concept of capacitorless 1T DRAM termed as Advanced RAM or A RAM and presents a discussion on quantum dot QD based flash memory Building on this foundation the coverage turns to STT RAM emphasizing scalable embedded STT RAM and the physics and engineering of magnetic domain wall racetrack memory The book also discusses state of the art modeling applied to phase change memory devices and includes an extensive review of RRAM highlighting the physics of operation and analyzing different materials systems currently under investigation The hunt is still on for universal memory that fits all the requirements of an ideal memory capable of high density storage low power operation unparalleled speed high endurance and low cost Taking an interdisciplinary approach this book bridges technological and application issues to provide the groundwork for developing custom designed memory systems

Hydrogen in Crystalline Semiconductors Stephen J. Pearton, James W. Corbett, Michael Stavola, 2013-03-08 Hardcoverausgabe

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