

# **Pipelined Adaptive Digital Filters**

**Naresh R. Shanbhag**

**Keshab K. Parhi**



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# Pipelined Adaptive Digital Filters

**Jeong-Taek Kong, David V. Overhauser**



## **Pipelined Adaptive Digital Filters:**

Pipelined Adaptive Digital Filters Naresh R. Shanbhag, Keshab K. Parhi, 2012-12-06 Adaptive filtering is commonly used in many communication applications including speech and video predictive coding mobile radio ISDN subscriber loops and multimedia systems Existing adaptive filtering topologies are non concurrent and cannot be pipelined Pipelined Adaptive Digital Filters presents new pipelined topologies which are useful in reducing area and power and in increasing speed If the adaptive filter portion of a system suffers from a power speed area bottleneck a solution is provided Pipelined Adaptive Digital Filters is required reading for all users of adaptive digital filtering algorithms Algorithm application and integrated circuit chip designers can learn how their algorithms can be tailored and implemented with lower area and power consumption and with higher speed The relaxed look ahead techniques are used to design families of new topologies for many adaptive filtering applications including least mean square and lattice adaptive filters adaptive differential pulse code modulation coders adaptive differential vector quantizers adaptive decision feedback equalizers and adaptive Kalman filters Those who use adaptive filtering in communications signal and image processing algorithms can learn the basis of relaxed look ahead pipelining and can use their own relaxations to design pipelined topologies suitable for their applications Pipelined Adaptive Digital Filters is especially useful to designers of communications speech and video applications who deal with adaptive filtering those involved with design of modems wireless systems subscriber loops beam formers and system identification applications This book can also be used as a text for advanced courses on the topic

**Pipelined Adaptive Digital Filters** Naresh R. Shanbhag, Keshab Parhi, 2012-10-08 Adaptive filtering is commonly used in many communication applications including speech and video predictive coding mobile radio ISDN subscriber loops and multimedia systems Existing adaptive filtering topologies are non concurrent and cannot be pipelined Pipelined Adaptive Digital Filters presents new pipelined topologies which are useful in reducing area and power and in increasing speed If the adaptive filter portion of a system suffers from a power speed area bottleneck a solution is provided Pipelined Adaptive Digital Filters is required reading for all users of adaptive digital filtering algorithms Algorithm application and integrated circuit chip designers can learn how their algorithms can be tailored and implemented with lower area and power consumption and with higher speed The relaxed look ahead techniques are used to design families of new topologies for many adaptive filtering applications including least mean square and lattice adaptive filters adaptive differential pulse code modulation coders adaptive differential vector quantizers adaptive decision feedback equalizers and adaptive Kalman filters Those who use adaptive filtering in communications signal and image processing algorithms can learn the basis of relaxed look ahead pipelining and can use their own relaxations to design pipelined topologies suitable for their applications Pipelined Adaptive Digital Filters is especially useful to designers of communications speech and video applications who deal with adaptive filtering those involved with design of modems wireless systems subscriber loops beam formers and system identification applications This

book can also be used as a text for advanced courses on the topic      Design of Pipelined VLSI Adaptive Digital Filters with Relaxed Look-ahead Naresh R. Shanbhag,1993      *Digital Signal Processing with Field Programmable Gate Arrays* Uwe Meyer-Baese,2013-03-09 Field Programmable Gate Arrays FPGAs are revolutionizing digital signal processing as novel FPGA families are replacing ASICs and PDSPs for front end digital signal processing algorithms So the efficient implementation of these algorithms is critical and is the main goal of this book It starts with an overview of today s FPGA technology devices and tools for designing state of the art DSP systems A case study in the first chapter is the basis for more than 30 design examples throughout The following chapters deal with computer arithmetic concepts theory and the implementation of FIR and IIR filters multirate digital signal processing systems DFT and FFT algorithms and advanced algorithms with high future potential Each chapter contains exercises The VERILOG source code and a glossary are given in the appendices while the accompanying CD ROM contains the examples in VHDL and Verilog code as well as the newest Altera Baseline software This edition has a new chapter on adaptive filters new sections on division and floating point arithmetics an up date to the current Altera software and some new exercises      *Digital Signal Processing for Multimedia Systems* Keshab K. Parhi,Takao Nishitami,2018-10-03 Addresses a wide selection of multimedia applications programmable and custom architectures for the implementations of multimedia systems and arithmetic architectures and design methodologies The book covers recent applications of digital signal processing algorithms in multimedia presents high speed and low priority binary and finite field arithmetic architectures details VHDL based implementation approaches and more      Advances in VLSI, Communication, and Signal Processing Debashis Dutta,Haranath Kar,Chiranjeev Kumar,Vijaya Bhadauria,2019-12-03 This book comprises select proceedings of the International Conference on VLSI Communication and Signal processing VCAS 2018 It looks at latest research findings in VLSI design and applications The book covers a wide range of topics in electronics and communication engineering especially in the area of microelectronics and VLSI design communication systems and networks and image and signal processing The contents of this book will be useful to researchers and professionals alike

**Proceedings of the Trends in Electronics Conference** ,1992      Technology Enabling Tomorrow ,1992      *Circuits, Signals, and Speech and Image Processing* Richard C. Dorf,2018-10-03 In two editions spanning more than a decade The Electrical Engineering Handbook stands as the definitive reference to the multidisciplinary field of electrical engineering Our knowledge continues to grow and so does the Handbook For the third edition it has expanded into a set of six books carefully focused on a specialized area or field of study Each book represents a concise yet definitive collection of key concepts models and equations in its respective domain thoughtfully gathered for convenient access *Circuits Signals and Speech and Image Processing* presents all of the basic information related to electric circuits and components analysis of circuits the use of the Laplace transform as well as signal speech and image processing using filters and algorithms It also examines emerging areas such as text to speech synthesis real time processing and embedded signal processing Each article includes defining

terms references and sources of further information Encompassing the work of the world s foremost experts in their respective specialties Circuits Signals and Speech and Image Processing features the latest developments the broadest scope of coverage and new material on biometrics Research in Progress ,1992 **Digital Timing Macromodeling for VLSI Design Verification** Jeong-Taek Kong,David V. Overhauser,2012-12-06 Digital Timing Macromodeling for VLSI Design Verification first of all provides an extensive history of the development of simulation techniques It presents detailed discussion of the various techniques implemented in circuit timing fast timing switch level timing switch level and gate level simulation It also discusses mixed mode simulation and interconnection analysis methods The review in Chapter 2 gives an understanding of the advantages and disadvantages of the many techniques applied in modern digital macromodels The book also presents a wide variety of techniques for performing nonlinear macromodeling of digital MOS subcircuits which address a large number of shortcomings in existing digital MOS macromodels Specifically the techniques address the device model detail transistor coupling capacitance effective channel length modulation series transistor reduction effective transconductance input terminal dependence gate parasitic capacitance the body effect the impact of parasitic RC interconnects and the effect of transmission gates The techniques address major sources of errors in existing macromodeling techniques which must be addressed if macromodeling is to be accepted in commercial CAD tools by chip designers The techniques presented in Chapters 4 6 can be implemented in other macromodels and are demonstrated using the macromodel presented in Chapter 3 The new techniques are validated over an extremely wide range of operating conditions much wider than has been presented for previous macromodels thus demonstrating the wide range of applicability of these techniques **Research in Progress** United States. Army Research Office,1992 Vols for 1977 consist of two parts Chemistry biological sciences engineering sciences metallurgy and materials science issued in the spring and Physics electronics mathematics geosciences issued in the fall Advanced Signal Processing Stergios Stergiopoulos,2017-09-29 Discover the Applicability Benefits and Potential of New Technologies As advances in algorithms and computer technology have bolstered the digital signal processing capabilities of real time sonar radar and non invasive medical diagnostics systems cutting edge military and defense research has established conceptual similarities in these areas Now civilian enterprises can use government innovations to facilitate optimal functionality of complex real time systems Advanced Signal Processing details a cost efficient generic processing structure that exploits these commonalities to benefit commercial applications Learn from a Renowned Defense Scientist Researcher and Innovator The author preserves the mathematical focus and key information from the first edition that provided invaluable coverage of topics including adaptive systems advanced beamformers and volume visualization methods in medicine Integrating the best features of non linear and conventional algorithms and explaining their application in PC based architectures this text contains new data on Advances in biometrics image segmentation registration and fusion techniques for 3D 4D ultrasound CT and MRI Fully digital 3D 4D

3D time ultrasound system technology computing architecture requirements and relevant implementation issues State of the art non invasive medical procedures non destructive 3D tomography imaging and biometrics and monitoring of vital signs Cardiac motion correction in multi slice X ray CT imaging Space time adaptive processing and detection of targets interference intense backgrounds comprised of clutter and jamming With its detailed explanation of adaptive synthetic aperture and fusion processing schemes with near instantaneous convergence in 2 D and 3 D sensors including planar circular cylindrical and spherical arrays the quality and illustration of this text s concepts and techniques will make it a favored reference

Mixed-Mode Simulation and Analog Multilevel Simulation Resve A. Saleh,Shyh-Jye Jou,A. Richard Newton,2013-03-09 Mixed Mode Simulation and Analog Multilevel Simulation addresses the problems of simulating entire mixed analog digital systems in the time domain A complete hierarchy of modeling and simulation methods for analog and digital circuits is described Mixed Mode Simulation and Analog Multilevel Simulation also provides a chronology of the research in the field of mixed mode simulation and analog multilevel simulation over the last ten to fifteen years In addition it provides enough information to the reader so that a prototype mixed mode simulator could be developed using the algorithms in this book Mixed Mode Simulation and Analog Multilevel Simulation can also be used as documentation for the SPLICE family of mixed mode programs as they are based on the algorithms and techniques described in this book

Cad Frameworks Pieter van der Wolf,2012-12-06 Since the early 1980s CAD frameworks have received a great deal of attention both in the research community and in the commercial arena It is generally agreed that CAD framework technology promises much advanced CAD frameworks can turn collections of individual tools into effective and user friendly design environments But how can this promise be fulfilled CAD Frameworks Principles and Architecture describes the design and construction of CAD frameworks It presents principles for building integrated design environments and shows how a CAD framework can be based on these principles It derives the architecture of a CAD framework in a systematic way using well defined primitives for representation This architecture defines how the many different framework sub topics ranging from concurrency control to design flow management relate to each other and come together into an overall system The origin of this work is the research and development performed in the context of the Nelsis CAD Framework which has been a working system for well over eight years gaining functionality while evolving from one release to the next The principles and concepts presented in this book have been field tested in the Nelsis CAD Framework CAD Frameworks Principles and Architecture is primarily intended for EDA professionals both in industry and in academia but is also valuable outside the domain of electronic design Many of the principles and concepts presented are also applicable to other design oriented application domains such as mechanical design or computer aided software engineering CASE It is thus a valuable reference for all those involved in computer aided design

**Logic Synthesis for Field-Programmable Gate Arrays** Rajeev Murgai,Robert K. Brayton,Alberto L. Sangiovanni-Vincentelli,2012-12-06 Short turnaround has become critical in the design of electronic

systems Software programmable components such as microprocessors and digital signal processors have been used extensively in such systems since they allow rapid design revisions However the inherent performance limitations of software programmable systems mean that they are inadequate for high performance designs Designers thus turned to gate arrays as a solution User programmable gate arrays field programmable gate arrays FPGAs have recently emerged and are changing the way electronic systems are designed and implemented The growing complexity of the logic circuits that can be packed onto an FPGA chip means that it has become important to have automatic synthesis tools that implement logic functions on these architectures Logic Synthesis for Field Programmable Gate Arrays describes logic synthesis for both look up table LUT and multiplexor based architectures with a balanced presentation of existing techniques together with algorithms and the system developed by the authors Audience A useful reference for VLSI designers developers of computer aided design tools and anyone involved in or with FPGAs **The Electrical Engineering Handbook, Second Edition** Richard C.

Dorf, 1997-09-26 In 1993 the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work Now this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today Every electrical engineer should have an opportunity to expand his expertise with this definitive guide In a single volume this handbook provides a complete reference to answer the questions encountered by practicing engineers in industry government or academia This well organized book is divided into 12 major sections that encompass the entire field of electrical engineering including circuits signal processing electronics electromagnetics electrical effects and devices and energy and the emerging trends in the fields of communications digital devices computer engineering systems and biomedical engineering A compendium of physical chemical material and mathematical data completes this comprehensive resource Every major topic is thoroughly covered and every important concept is defined described and illustrated Conceptually challenging but carefully explained articles are equally valuable to the practicing engineer researchers and students A distinguished advisory board and contributors including many of the leading authors professors and researchers in the field today assist noted author and professor Richard Dorf in offering complete coverage of this rapidly expanding field No other single volume available today offers this combination of broad coverage and depth of exploration of the topics The Electrical Engineering Handbook will be an invaluable resource for electrical engineers for years to come **High-Performance Digital VLSI Circuit Design** Richard

X. Gu, Khaled M. Sharaf, Mohamed I. Elmasry, 2012-12-06 High Performance Digital VLSI Circuit Design is the first book devoted entirely to the design of digital high performance VLSI circuits CMOS BiCMOS and bipolar circuits are covered in depth including state of the art circuit structures Recent advances in both the computer and telecommunications industries demand high performance VLSI digital circuits Digital processing of signals demands high speed circuit techniques for the GHz range The design of such circuits represents a great challenge one that is amplified when the power supply is scaled

down to 3.3 V. Moreover, the requirements of low power high performance circuits add an extra dimension to the design of such circuits. High Performance Digital VLSI Circuit Design is a self contained text introducing the subject of high performance VLSI circuit design and explaining the speed power tradeoffs. The first few chapters of the book discuss the necessary background material in the area of device design and device modeling respectively. High performance CMOS circuits are then covered, especially the new all N logic dynamic circuits. Propagation delay times of high speed bipolar CML and ECL are developed analytically to give a thorough understanding of various interacting process, device and circuit parameters. High current phenomena of bipolar devices are also addressed as these devices typically operate at maximum currents for limited device area. Different new high performance BiCMOS circuits are presented and compared to their conventional counterparts. These new circuits find direct applications in the areas of high speed adders, frequency dividers, sense amplifiers, level shifters, input/output clock buffers and PLLs. The book concludes with a few system application examples of digital high performance VLSI circuits.

**Audience:** A vital reference for practicing IC designers. Can be used as a text for graduate and senior undergraduate students in the area.

Co-Synthesis of Hardware and Software for Digital Embedded Systems Rajesh Kumar Gupta, 2012-12-06. Co Synthesis of Hardware and Software for Digital Embedded Systems with a Foreword written by Giovanni De Micheli presents techniques that are useful in building complex embedded systems. These techniques provide a competitive advantage over purely hardware or software implementations of time constrained embedded systems. Recent advances in chip level synthesis have made it possible to synthesize application specific circuits under strict timing constraints. This work advances the state of the art by formulating the problem of system synthesis using both application specific as well as reprogrammable components such as off the shelf processors. Timing constraints are used to determine what part of the system functionality must be delegated to dedicated application specific hardware while the rest is delegated to software that runs on the processor. This co synthesis of hardware and software from behavioral specifications makes it possible to realize real time embedded systems using off the shelf parts and a relatively small amount of application specific circuitry that can be mapped to semi custom VLSI such as gate arrays. The ability to perform detailed analysis of timing performance provides the opportunity of improving the system definition by creating better phototypes. Co Synthesis of Hardware and Software for Digital Embedded Systems is of interest to CAD researchers and developers who want to branch off into the expanding field of hardware software co design as well as to digital system designers who are interested in the present power and limitations of CAD techniques and their likely evolution.

**On Optimal Interconnections for VLSI** Andrew B. Kahng, Gabriel Robins, 2013-04-17. On Optimal Interconnections for VLSI describes from a geometric perspective algorithms for high performance high density interconnections during the global and detailed routing phases of circuit layout. First the book addresses area minimization with a focus on near optimal approximation algorithms for minimum cost Steiner routing. In addition to practical implementations of recent methods the implications of



recent results on spanning tree degree bounds and the method of Zelikovsky are discussed. Second, the book addresses delay minimization, starting with a discussion of accurate yet algorithmically tractable delay models. Recent minimum delay constructions are highlighted, including provably good cost-radius tradeoffs, critical sink routing algorithms, Elmore delay, optimal routing, graph Steiner arborescences, non-tree routing, and wiresizing. Third, the book addresses skew minimization for clock routing and prescribed delay routing formulations. The discussion starts with early matching-based constructions and goes on to treat zero skew routing with provably minimum wirelength, as well as planar clock routing. Finally, the book concludes with a discussion of multiple competing objectives, i.e., how to optimize area, delay, skew, and other objectives simultaneously. These techniques are useful when the routing instance has heterogeneous resources or is highly congested, as in FPGA routing, multi-chip packaging, and very dense layouts. Throughout the book, the emphasis is on practical algorithms and a complete self-contained development. *On Optimal Interconnections for VLSI* will be of use to both circuit designers, CAD tool users, as well as researchers and developers in the area of performance-driven physical design.

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