



Show Jumping International

Wenbin Ji



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web figure 1 flyback converter schematic flyback converter has two signal semi periods ton and toff which are named after
and controlled by the mosfet s switching states during ton the mosfet is in the on state and current flows from the input
through the primary inductor to linearly charge the coupled inductor

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output constantly there are different kinds of regulators available in the power supply domain but mainly in the case of dc to
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regulators control loop closed via sum of 5 v 12 v outputs all other outputs quasi regulated

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between rated bvdss and vdd typically 90 or less in such instances however it is not uncommon that greater than planned for
voltage spikes can occur so even the best designs may encounter an infrequent avalanche event

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