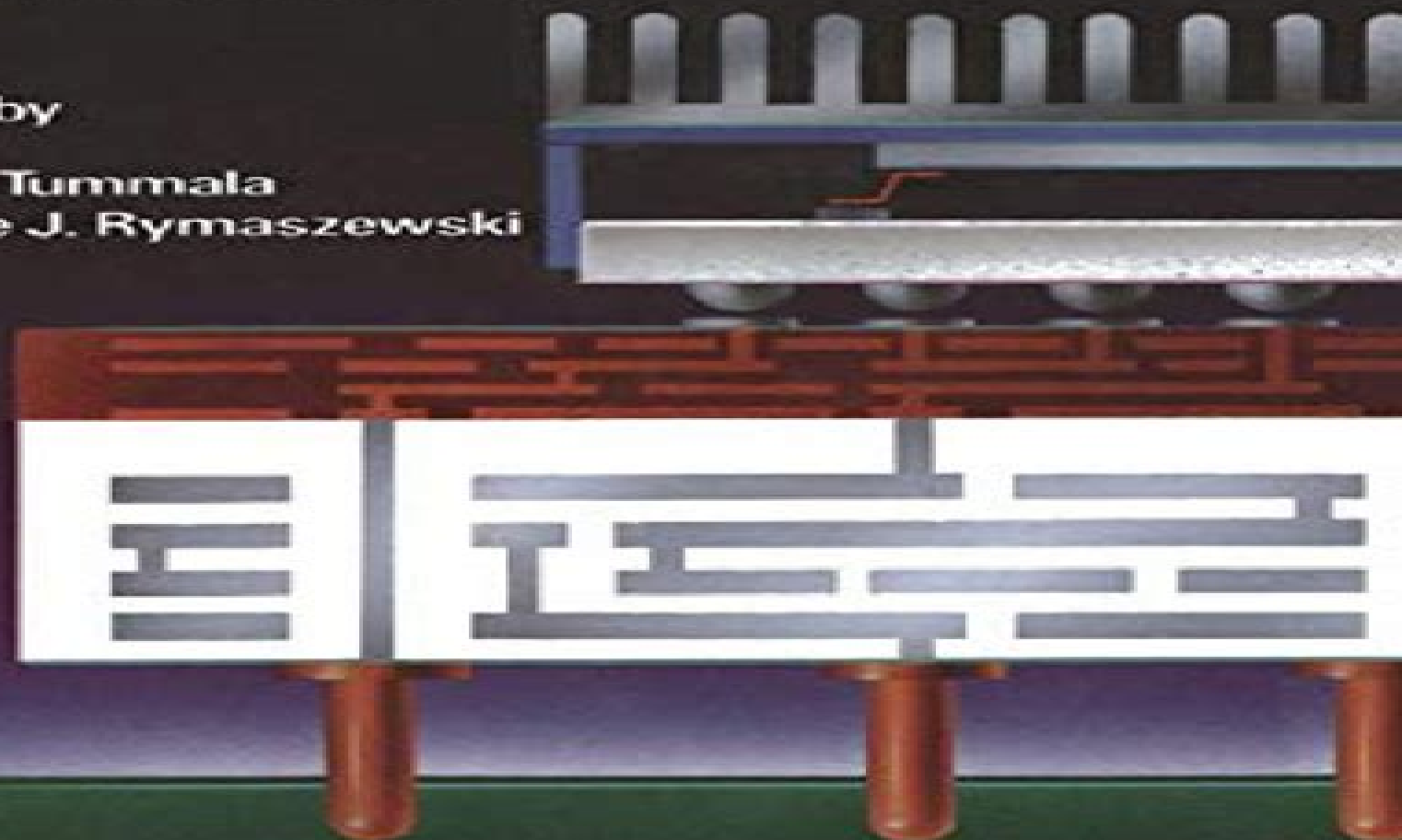


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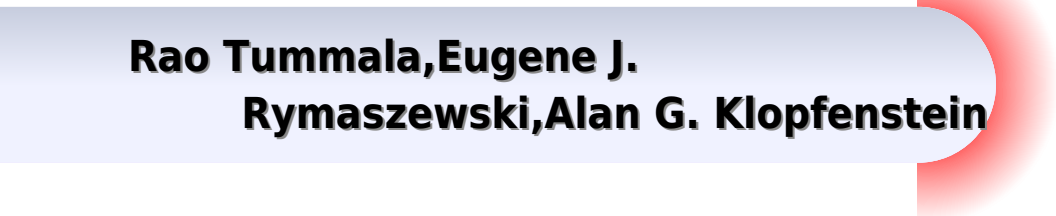
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Microelectronic Packaging Handbook

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This thoroughly revised and updated three volume set continues to be the standard reference in the field providing the latest in microelectronics design methods modeling tools simulation techniques and manufacturing procedures Unlike reference books that focus only on a few aspects of microelectronics packaging these outstanding volumes discuss state of the art packages that meet the power cooling protection and interconnection requirements of increasingly dense and fast microcircuitry Providing an excellent balance of theory and practical applications this dynamic compilation features step by step examples and vital technical data simplifying each phase of package design and production In addition the volumes contain over 2000 references 900 figures and 250 tables Part I Technology Drivers covers the driving force of microelectronics packaging electrical thermal and reliability It introduces the technology developer to aspects of manufacturing that must be considered during product development Part II Semiconductor Packaging discusses the interconnection of the IC chip to the first level of packaging and all first level packages Electrical test sealing and encapsulation technologies are also covered in detail Part III Subsystem Packaging explores board level packaging as well as connectors cables and optical packaging *Microelectronics Packaging Handbook* R.R. Tummala,1989-01-14 RALPH E GOMORY IBM SENIOR VICE PRESIDENT SCIENCE AND TECHNOLOGY Far from being passive containers for microelectronic devices the so called packages in today s advanced computers pose at least as many engineering challenges as the chips that they interconnect power and cool New pack aging concepts often lead their developers into uncharted areas of science Ultimately progress in packaging is likely to set the limits on how far computers can evolve And yet this technology has never captured the public imagination in the way that chips have Worse it has been largely overlooked by universities as a prime subject for teaching and research In part this academic oversight stems from packaging s multidiscipli nary nature Packaging involves the solution of electrical mechanical and thermal problems it requires understanding at the molecular level not only of silicon but of metals ceramics polymers glass and composites In short iii iv FOREWORD it is poorly matched to the traditional separation of university disciplines Another roadblock has been the lack of a comprehensive book dealing ade quately with state of the art packaging design and modern industry require ments *Microelectronics Packaging Handbook* TUMMALA RAO R.,1996-12-15 *Microelectronics Packaging Handbook* Rao Tummala,Eugene J. Rymaszewski,Alan G. Klopfenstein,1997-01-31 Electronics has become the largest industry surpassing agriCulture auto and heavy metal industries It has become the industry of choice for a country to prosper already having given rise to the phenomenal prosperity of Japan Korea Singapore Hong Kong and Ireland among others At the current growth rate total worldwide semiconductor sales will reach 300B by the year 2000 The key electronic technologies responsible for the growth of the industry include semiconductors the packaging of semiconductors for systems use in auto telecom computer consumer

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