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Noise in Electron Devices

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Noise In Electron Devices

Frank Neville Hosband Robinson



Noise In Electron Devices:

Noise in Electron Devices Louis D. Smullin, Hermann A. Haus, 2003-02-01 A symposium on noise stressing mathematical theory and basic physical phenomena and covering such topics as cathode noise phenomena signal amplification in microwave tubes solid state noise and methods of designing low noise tubes **Noise in Electronic Devices and Systems** Michael J. Buckingham, 1983 **NOISE IN ELECTRON DEVICES - COURSE- PROCEEDINGS- MASSACHUSETTS INSTITUTE OF TECHNOLOGY.** , **Noise in Electronic Devices** Institute of Physics (Great Britain), 1961 *Noise in Electron Devices. (Edited by L. D. Smullin and H. A. Haus.) [By various authors.]* Louis Dijour SMULLIN (and HAUS (Hermann A.)), Hermann A. HAUS, 1959 Advanced Experimental Methods for Noise Research in Nanoscale Electronic Devices Josef Sikula, Michael Levinshtein, 2006-02-21 A discussion of recently developed experimental methods for noise research in nanoscale electronic devices conducted by specialists in transport and stochastic phenomena in nanoscale physics The approach described is to create methods for experimental observations of noise sources their localization and their frequency spectrum voltage current and thermal dependences Our current knowledge of measurement methods for mesoscopic devices is summarized to identify directions for future research related to downscaling effects The directions for future research into fluctuation phenomena in quantum dot and quantum wire devices are specified Nanoscale electronic devices will be the basic components for electronics of the 21st century From this point of view the signal to noise ratio is a very important parameter for the device application Since the noise is also a quality and reliability indicator experimental methods will have a wide application in the future *Noise in Solid State Devices and Circuits* Albert Van der Ziel, 1986-05-13 Gives basic and up to date information about noise sources in electronic devices Demonstrates how this information can be used to calculate the noise performance in particular the noise figure of electronic circuits using these devices Optimization procedures both for the circuits and for the devices are then devised based on these data Gives an elementary treatment of thermal noise diffusion noise and velocity fluctuation noise including quantum effects in thermal noise and maser noise **III-Nitride Electronic Devices** , 2019-10-18 III Nitride Electronic Devices Volume 102 emphasizes two major technical areas advanced by this technology radio frequency RF and power electronics applications The range of topics covered by this book provides a basic understanding of materials devices circuits and applications while showing the future directions of this technology Specific chapters cover Electronic properties of III nitride materials and basics of III nitride HEMT Epitaxial growth of III nitride electronic devices III nitride microwave power transistors III nitride millimeter wave transistors III nitride lateral transistor power switch III nitride vertical devices Physics Based Modeling Thermal management in III nitride HEMT RF Microwave applications of III nitride transistor wireless power transfer and more Presents a complete review of III Nitride electronic devices from fundamental physics to applications in two key technical areas RF and power electronics Outlines fundamentals reviews state of the art circuits and applications and

introduces current and emerging technologies Written by a panel of academic and industry experts in each field Noise and Fluctuations in Electronic Devices and Circuits Frank Neville Hosband Robinson,1974 American Standard Methods of Measuring Noise in Electron Devices American Standards Association,1955 **Handbook of Nitride Semiconductors and Devices, GaN-based Optical and Electronic Devices** Hadis Morkoç,2009-07-30 The three volumes of this handbook treat the fundamentals technology and nanotechnology of nitride semiconductors with an extraordinary clarity and depth They present all the necessary basics of semiconductor and device physics and engineering together with an extensive reference section Volume 3 deals with nitride semiconductor devices and device technology Among the application areas that feature prominently here are LEDs lasers FETs and HBTs detectors and unique issues surrounding solar blind detection

Electron Dynamics of Diode Regions Charles K. Birdsall,1966-01-01 Electron Dynamics of Diode Regions describes the model construction and analysis of motion of charged particles of diode regions in time varying fields The models analyzed are simplified versions of parts of practical devices primarily active microwave devices tubes and semiconductor amplifiers while the most striking results obtained are due to electron inertia and space charge effects in terms of laboratory observable This book is composed of seven chapters and begins with an introduction to the general concepts of time dependent flow including induced current the techniques of linearization calculating variational transit time and obtaining equivalent circuits The following chapters present the classical linear analysis which includes the space charge effects with several applications These chapters also explore the existence of a maximum stable current in a space charge limited diode The discussion then shifts to the basics of high velocity klystron gap with nonuniform field distributions and the application of the multicavity klystron This text further covers the analysis and examples of crossed field gaps The final chapters deal with the fundamentals of velocity and current distributions obtained from common electron emitters with some attempt to show how the multivelocisty streams evolve into single velocity equivalents needed for the methods of earlier chapters Results of applying the Lagrangian starting analysis to semiconductor diode regions necessarily from a new equation of motion are also provided This book is intended for graduate courses seminars and research studies *RF and Microwave Transistor Oscillator Design* Andrei Grebennikov,2007-04-30 The increase of consumer electronics and communications applications using Radio Frequency RF and microwave circuits has implications for oscillator design Applications working at higher frequencies and using novel technologies have led to a demand for more robust circuits with higher performance and functionality but decreased costs size and power consumption As a result there is also a need for more efficient oscillators This book presents up to date information on all aspects of oscillator design enabling a selection of the best oscillator topologies with optimized noise reduction and electrical performance RF and Microwave Transistor Oscillator Design covers analyses of non linear circuit design methods including spectral domain analysis time domain analysis and the quasilinear method information on noise in oscillators including chapters on varactor and oscillator frequency tuning CMOS voltage

controlled oscillators and wideband voltage controlled oscillators information on the stability of oscillations with discussions on the stability of multi resonant circuits and the phase plane method optimized design and circuit techniques beginning with the empirical and analytic design approaches moving on to the high efficiency design technique general operation and design principles of oscillators including a section on the historical aspects of oscillator configurations A valuable reference for practising RF and Microwave designers and engineers RF and Microwave Transistor Oscillator Design is also useful for lecturers advanced students and research and design R D personnel

Optical Communication Receiver Design Stephen B. Alexander, 1997 This Tutorial Text provides an overview of design principles for receivers used in optical communication systems intended for practicing engineers The author reviews technologies used to construct optical links and illustrates the flow of system performance specifications into receiver requirements Photodetector fundamentals associated statistics characteristics and performance issues are presented together with a tutorial on noise analysis and the specific techniques needed to model optical receivers

Microwave and Millimeter-Wave Vacuum Electron Devices: Inductive Output Tubes, Klystrons, Traveling-Wave Tubes, Magnetrons, Crossed-Field Amplifiers, and Gyrotrons A.S. Gilmour, Jr., 2020-04-30 Written by an internationally recognized as an expert on the subject of microwave MW tubes this book presents and describes the many types of microwave tubes and despite competition from solid state devices those using GaN SiC et cetera which continue to be used widely and find new applications in defense communications medical and industrial drying Helix traveling wave tubes TWTs as well as coupled cavity TWTs are covered Klystrons and how they work are described along with the physics behind it and examples of devices and their uses Vacuum electron devices are explained in detail and examines the harsh environment that must exist in tubes if they are to operate properly The secondary emission process and its role in the operation of crossed field devices is also discussed The design of collectors for linear beam tubes including power dissipation and power recovery are explored Discussions of important noise sources and techniques that can be used to minimize their effects are also included Presented in full color this book contains a balance of practical and theoretical material so that those new to microwave tubes as well as experienced microwave tube technicians engineers and managers can benefit from its use

High-Frequency Bipolar Transistors Michael Reisch, 2012-12-06 This book provides a rather comprehensive presentation of the physics and modeling of high frequency bipolar transistors with particular emphasis given to silicon based devices I hope it will be found useful by those who do as well as by those who intend to work in the field as it compiles and extends material presented in numerous publications in a coherent fashion I ve worked on this project for years and did my best to avoid errors De spite all efforts it is possible that something has been overlooked during copy editing and proof reading If you find a mistake please let me know Michael Reisch Kempten December 2002 Notation It is intended here to use the most widely employed notation in cases where the standard textbook notation is different from the SPICE notation the latter is used In order to make formulas more readable model parameters represented in SPICE by a series of capital

letters are written here as one capital letter with the rest in the form of a subscript e g XCJC is used here instead of the XCJC used in the SPICE input Concerning the use of lower case and capital letters the following rules are applied Time dependent large signal quantities are represented by lower case letters The variables t , v and p therefore denote time dependent current voltage and power values

Quantum-based Electronic Devices And Systems, Selected Topics In Electronics And Systems, Vol 14 Mitra Dutta,1998-10-23 This volume includes highlights of the theories and experimental findings that underlie essential phenomena occurring in quantum based devices and systems as well as the principles of operation of selected novel quantum based electronic devices and systems A number of the emerging approaches to creating new types of quantum based electronic devices and systems are also discussed

Progress in SOI Structures and Devices Operating at Extreme Conditions Francis Balestra,Alexei N. Nazarov,Vladimir S. Lysenko,2012-12-06 A review of the electrical properties performance and physical mechanisms of the main silicon on insulator SOI materials and devices Particular attention is paid to the reliability of SOI structures operating in harsh conditions The first part of the book deals with material technology and describes the SIMOX and ELTRAN technologies the smart cut technique SiCOI structures and MBE growth The second part covers reliability of devices operating under extreme conditions with an examination of low and high temperature operation of deep submicron MOSFETs and novel SOI technologies and circuits SOI in harsh environments and the properties of the buried oxide The third part deals with the characterization of advanced SOI materials and devices covering laser recrystallized SOI layers ultrashort SOI MOSFETs and nanostructures gated diodes and SOI devices produced by a variety of techniques The last part reviews future prospects for SOI structures analyzing wafer bonding techniques applications of oxidized porous silicon semi insulating silicon materials self organization of silicon dots and wires on SOI and some new physical phenomena

21st Century Nanostructured Materials Phuong Pham,2022-04-20 Nanostructured materials NMs are attracting interest as low dimensional materials in the high tech era of the 21st century Recently nanomaterials have experienced breakthroughs in synthesis and industrial and biomedical applications This book presents recent achievements related to NMs such as graphene carbon nanotubes plasmonic materials metal nanowires metal oxides nanoparticles metamaterials nanofibers and nanocomposites along with their physical and chemical aspects Additionally the book discusses the potential uses of these nanomaterials in photodetectors transistors quantum technology chemical sensors energy storage silk fibroin composites drug delivery tissue engineering and sustainable agriculture and environmental applications

Tradeoffs and Optimization in Analog CMOS Design David Binkley,2008-09-15 Analog CMOS integrated circuits are in widespread use for communications entertainment multimedia biomedical and many other applications that interface with the physical world Although analog CMOS design is greatly complicated by the design choices of drain current channel width and channel length present for every MOS device in a circuit these design choices afford significant opportunities for optimizing circuit performance This book addresses tradeoffs and optimization of device and circuit

performance for selections of the drain current inversion coefficient and channel length where channel width is implicitly considered. The inversion coefficient is used as a technology independent measure of MOS inversion that permits design freely in weak, moderate, and strong inversion. This book details the significant performance tradeoffs available in analog CMOS design and guides the designer towards optimum design by describing an interpretation of MOS modeling for the analog designer motivated by the EKV MOS model using tabulated hand expressions and figures that give performance and tradeoffs for the design choices of drain current inversion coefficient and channel length. Performance includes effective gate source bias and drain source saturation voltages, transconductance efficiency, transconductance distortion, normalized drain source conductance, capacitances, gain, and bandwidth measures, thermal and flicker noise mismatch, and gate and drain leakage current. Measured data that validates the inclusion of important small geometry effects like velocity saturation, vertical field mobility reduction, drain induced barrier lowering, and inversion level increases in gate referred flicker noise voltage. In depth treatment of moderate inversion which offers low bias compliance voltages, high transconductance efficiency, and good immunity to velocity saturation effects for circuits designed in modern low voltage processes. Fabricated design examples that include operational transconductance amplifiers optimized for various tradeoffs in DC and AC performance and micropower low noise preamplifiers optimized for minimum thermal and flicker noise. A design spreadsheet available at the book web site that facilitates rapid optimum design of MOS devices and circuits. Tradeoffs and Optimization in Analog CMOS Design is the first book dedicated to this important topic. It will help practicing analog circuit designers and advanced students of electrical engineering build design intuition, rapidly optimize circuit performance during initial design, and minimize trial and error circuit simulations.

Fuel your quest for knowledge with is thought-provoking masterpiece, **Noise In Electron Devices** . This educational ebook, conveniently sized in PDF (PDF Size: *), is a gateway to personal growth and intellectual stimulation. Immerse yourself in the enriching content curated to cater to every eager mind. Download now and embark on a learning journey that promises to expand your horizons. .

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