

Numerical Analysis of Compound Semiconductor RF Devices

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Abstract

An overview of heterostructure RF device simulation for industrial application based on III-V compound semiconductors has been given in [1]. Here, we present the most recent achievements in numerical simulation for industrial heterostructure devices, together with relevant industrial applications (GaAs, InP, and SiGe HBTs).

INTRODUCTION

To cope with explosive development costs and strong competition in the semiconductor industry today, Technology Computer-Aided Design (TCAD) methodologies are extensively used in development and production. Several questions during device fabrication, such as performance optimization and process control, can be addressed by simulation. The choice of a given simulation tool or a combination of tools depends to a large extent on the complexity of the particular task, on the desired accuracy of the problem solution, and on the available human, computer, and time resources.

Optimization of geometry, doping, materials, and material compositions targets high output power, high breakdown voltage, high speed (high f_T and f_{max}), low leakage, low noise, and low power consumption. This is a challenging task that can be significantly supported by device simulation. While DC simulation is sufficient for optimization of breakdown voltages, turn-on voltages, or leakage currents, AC simulation is required for speed, noise, and power issues.

There are several challenges which are specific for modeling and simulation of heterostructure devices [2]. The characterization of the physical properties of III-V and SiGe compounds is required for wide ranges of material compositions, temperatures, doping concentrations, etc. The model parameters must be verified against several independent HEMT and HBT technologies to obtain a concise set used for all simulations.

For example, the database for novel materials, such as the GaN or the GaSb systems, which have entered the III-V world with impressive device results, is still relatively poor. Modeling of stress-induced changes of the physical properties of strained material layers and consideration of piezoelectrical effects is a subject of ongoing research [2]. Heterointerface modeling is a key issue for devices which in-

clude abrupt junctions. Thermionic emission, field emission, and tunneling effects critically determine the current transport, especially in double heterojunction bipolar transistors (DHBTs).

Advanced device simulation allows a precise physics-based extraction of small-signal parameters [2]. Measured bias-dependent S-parameters serve as a valuable source of information when compared at different bias points to simulated S-parameters from a device simulator. By simulating in the frequency domain, important small-signal figures of merit, such as the cut-off frequency f_T and the maximum oscillation frequency f_{max} can be efficiently extracted [3]. On the other hand, non-linear periodic steady-state analysis can be performed in the time domain to obtain large-signal figure-of-merit parameters, such as distortion, power, frequency, noise, etc. [4] as well in the context of coupled device and circuit simulation.

HETEROSTRUCTURE DEVICE SIMULATORS

The continuously increasing computational power of computer systems allows the use of TCAD tools on a very large scale. Several commercial device simulators (such as [5]-[10]) company-developed simulators (such as [11]-[13]), and university-developed simulators (like [14]-[19]) have been successfully employed for device engineering applications. These simulators differ considerably in dimensionality (one, quasi-two, two, quasi-three, or three), in choice of carrier transport model (drift-diffusion, energy-transport, or Monte Carlo statistical solution of the Boltzmann transport equation), and in the capability of including electrothermal effects. The drift-diffusion transport model [20] is by now the most popular model used for device simulation. With down-scaling of the feature sizes, non-local effects become more pronounced and must be accounted for by applying an energy-transport model or a hydrodynamic transport model [21]. During the last two decades, Monte Carlo methods for solving the Boltzmann transport equation have been developed [22] and applied for device simulation [23, 24]. However, reduction of computational resources is still an issue, and therefore Monte Carlo device simulation is still not feasible for industrial application on daily basis. An approach to preserve accuracy at lower computational cost is to calibrate lower order transport parameters to Monte Carlo simulation data.

Numerical Analysis For Semiconductor Devices

Kazutaka Tomizawa



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Numerical Analysis for Semiconductor Devices Mamoru Kurata,1982 **Analysis and Simulation of Semiconductor Devices** S. Selberherr,1984-07 The invention of semiconductor devices is a fairly recent one considering classical time scales in human life The bipolar transistor was announced in 1947 and the MOS transistor in a practically usable manner was demonstrated in 1960 From these beginnings the semiconductor device field has grown rapidly The first integrated circuits which contained just a few devices became commercially available in the early 1960s Immediately thereafter an evolution has taken place so that today less than 25 years later the manufacture of integrated circuits with over 400 000 devices per single chip is possible Coincident with the growth in semiconductor device development the literature concerning semiconductor device and technology issues has literally exploded In the last decade about 50 000 papers have been published on these subjects The advent of so called Very Large Scale Integration VLSI has certainly revealed the need for a better understanding of basic device behavior The miniaturization of the single transistor which is the major prerequisite for VLSI nearly led to a breakdown of the classical models of semiconductor devices Numerical Simulation of Submicron Semiconductor Devices Kazutaka Tomizawa,1993-01-01 Describes the basic theory of carrier transport develops numerical algorithms used for transport problems or device simulations and presents real world examples of implementation

Numerical Analysis of Semiconductor Devices and Integrated Circuits B. T. Browne,John James Henry Miller,1981

Numerical Analysis of Semiconductor Devices Pascal Swei Lin Chen,1982 *Noise in Semiconductor Devices* Fabrizio Bonani,Giovanni Ghione,2013-03-09 The design and optimization of electronic systems often requires appraisal an of the electrical noise generated by active devices and at a technological level the ability to properly design active elements in order to minimize when possible their noise Examples of critical applications are of course receiver front ends in RF and optoelectronic transmission systems but also front end stages in sensors and in a completely different context nonlinear circuits such as oscillators mixers and frequency multipliers The rapid development of silicon RF applications has recently fostered the interest toward low noise silicon devices for the lower microwave band such as low noise MOS transistors at the same time the RF and microwave ranges are becoming increasingly important in fast optical communication systems Thus high frequency noise modeling and simulation of both silicon and compound semiconductor based bipolar and field effect transistors can be considered as an important and timely topic This does not exclude of course low frequency noise which is relevant also in the RF and microwave ranges when ever it is up converted within a nonlinear system either autonomous as an oscillator or non autonomous as a mixer or frequency multiplier The aim of the present book is to provide a thorough introduction to the physics based numerical modeling of semiconductor devices operating both in small signal and in large signal conditions In the latter instance only the non autonomous case was considered and thus the present treatment does not directly extend to oscillators **An Introduction to the Numerical Analysis of Semiconductor Devices and**

Integrated Circuits John James Henry Miller, 1981 Companion volume to NASECODE II Conference proceedings

Numerical Analysis of Semiconductor Devices John Gary Shaw, 2014 NASECODE , 1985 *The Stationary Semiconductor Device Equations* P.A. Markowich, 2013-03-09 In the last two decades semiconductor device simulation has become a research area which thrives on a cooperation of physicists electrical engineers and mathematicians In this book the static semiconductor device problem is presented and analysed from an applied mathematician's point of view I shall derive the device equations as obtained for the first time by Van Roosbroeck in 1950 from physical principles present a mathematical analysis discuss their numerical solution by discretisation techniques and report on selected device simulation runs To me personally the most fascinating aspect of mathematical device analysis is that an interplay of abstract mathematics perturbation theory numerical analysis and device physics is prompting the design and development of new technology I very much hope to convey to the reader the importance of applied mathematics for technological progress Each chapter of this book is designed to be as self-contained as possible however the mathematical analysis of the device problem requires tools which cannot be presented completely here Those readers who are not interested in the mathematical methodology and rigor can extract the desired information by simply ignoring details and proofs of theorems Also at the beginning of each chapter I refer to textbooks which introduce the interested reader to the required mathematical concepts

Semiconductor Device Physics and Simulation J.S. Yuan, Juin-Jei Liou, 2013-11-22 The advent of the microelectronics technology has made ever increasing numbers of small devices on a same chip The rapid emergence of ultra large scaled integrated ULSI technology has moved device dimension into the sub quarter micron regime and put more than 10 million transistors on a single chip While traditional closed form analytical models furnish useful intuition into how semiconductor devices behave they no longer provide consistently accurate results for all modes of operation of these very small devices The reason is that in such devices various physical mechanisms affect the device performance in a complex manner and the conventional assumptions i.e. one dimensional treatment low level injection quasi static approximation etc employed in developing analytical models become questionable Thus the use of numerical device simulation becomes important in device modeling Researchers and engineers will rely even more on device simulation for device design and analysis in the future This book provides comprehensive coverage of device simulation and analysis for various modern semiconductor devices It will serve as a reference for researchers engineers and students who require in depth up to date information and understanding of semiconductor device physics and characteristics The materials of the book are limited to conventional and mainstream semiconductor devices photonic devices such as light emitting and laser diodes are not included nor does the book cover device modeling device fabrication and circuit applications Simulation of Semiconductor Devices and Processes Siegfried Selberherr, Hannes Stippel, Ernst Strasser, 2012-12-06 The Fifth International Conference on Simulation of Semiconductor Devices and Processes SISDEP 93 continues a series of conferences which was initiated in 1984 by K

Board and D R J Owen at the University College of Wales Swansea where it took place a second time in 1986 Its organization was succeeded by G Baccarani and M Rudan at the University of Bologna in 1988 and W Fichtner and D Aemmer at the Federal Institute of Technology in Zurich in 1991 This year the conference is held at the Technical University of Vienna Austria September 7 9 1993 This conference shall provide an international forum for the presentation of outstanding research and development results in the area of numerical process and device simulation The miniaturization of today's semiconductor devices the usage of new materials and advanced process steps in the development of new semiconductor technologies suggests the design of new computer programs This trend towards more complex structures and increasingly sophisticated processes demands advanced simulators such as fully three dimensional tools for almost arbitrarily complicated geometries With the increasing need for better models and improved understanding of physical effects the Conference on Simulation of Semiconductor Devices and Processes brings together the simulation community and the process and device engineers who need reliable numerical simulation tools for characterization prediction and development

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Nascode IV John James Henry Miller, 1985 **Mathematical Modelling and Simulation of Electrical Circuits and Semiconductor Devices** Randolph E. Bank, 1994 Progress in today's high technology industries is strongly associated with the development of new mathematical tools A typical illustration of this partnership is the mathematical modelling and numerical simulation of electric circuits and semiconductor devices At the second Oberwolfach conference devoted to this important and timely field scientists from around the world mainly applied mathematicians and electrical engineers from industry and universities presented their new results Their contributions forming the body of this work cover electric circuit simulation device simulation and process simulation Discussions on experiences with standard software packages and improvements of such packages are included In the semiconductor area special lectures were given on new modelling approaches numerical techniques and existence and uniqueness results In this connection mention is made for example of

mixed finite element methods an extension of the Baliga Patankar technique for a three dimensional simulation and the connection between semiconductor equations and the Boltzmann equations **Numerical Analysis of Semiconductor Devices and Integrated Circuits** J. J. H. Miller,1983 *Simulation of Semiconductor Devices and Processes* Heiner Ryssel,Peter Pichler,2012-12-06 SISDEP 95 provides an international forum for the presentation of state of the art research and development results in the area of numerical process and device simulation Continuously shrinking device dimensions the use of new materials and advanced processing steps in the manufacturing of semiconductor devices require new and improved software The trend towards increasing complexity in structures and process technology demands advanced models describing all basic effects and sophisticated two and three dimensional tools for almost arbitrarily designed geometries The book contains the latest results obtained by scientists from more than 20 countries on process simulation and modeling simulation of process equipment device modeling and simulation of novel devices power semiconductors and sensors on device simulation and parameter extraction for circuit models practical application of simulation numerical methods and software **Numerical Analysis of Semiconductor Devices** John J. H. Miller,1981 **Numerical analysis of semiconductor devices. Proceedings of the NASECODE Conference ; 5 ,1987 Numerical analysis of semiconductor devices. Proceedings of the NASECODE Conference ; 8 ,1992**

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Table of Contents Numerical Analysis For Semiconductor Devices

1. Understanding the eBook Numerical Analysis For Semiconductor Devices
 - The Rise of Digital Reading Numerical Analysis For Semiconductor Devices
 - Advantages of eBooks Over Traditional Books
2. Identifying Numerical Analysis For Semiconductor Devices
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Numerical Analysis For Semiconductor Devices
 - User-Friendly Interface
4. Exploring eBook Recommendations from Numerical Analysis For Semiconductor Devices
 - Personalized Recommendations
 - Numerical Analysis For Semiconductor Devices User Reviews and Ratings
 - Numerical Analysis For Semiconductor Devices and Bestseller Lists
5. Accessing Numerical Analysis For Semiconductor Devices Free and Paid eBooks
 - Numerical Analysis For Semiconductor Devices Public Domain eBooks
 - Numerical Analysis For Semiconductor Devices eBook Subscription Services
 - Numerical Analysis For Semiconductor Devices Budget-Friendly Options

6. Navigating Numerical Analysis For Semiconductor Devices eBook Formats
 - ePub, PDF, MOBI, and More
 - Numerical Analysis For Semiconductor Devices Compatibility with Devices
 - Numerical Analysis For Semiconductor Devices Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Numerical Analysis For Semiconductor Devices
 - Highlighting and Note-Taking Numerical Analysis For Semiconductor Devices
 - Interactive Elements Numerical Analysis For Semiconductor Devices
8. Staying Engaged with Numerical Analysis For Semiconductor Devices
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Numerical Analysis For Semiconductor Devices
9. Balancing eBooks and Physical Books Numerical Analysis For Semiconductor Devices
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Numerical Analysis For Semiconductor Devices
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Numerical Analysis For Semiconductor Devices
 - Setting Reading Goals Numerical Analysis For Semiconductor Devices
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Numerical Analysis For Semiconductor Devices
 - Fact-Checking eBook Content of Numerical Analysis For Semiconductor Devices
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements

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