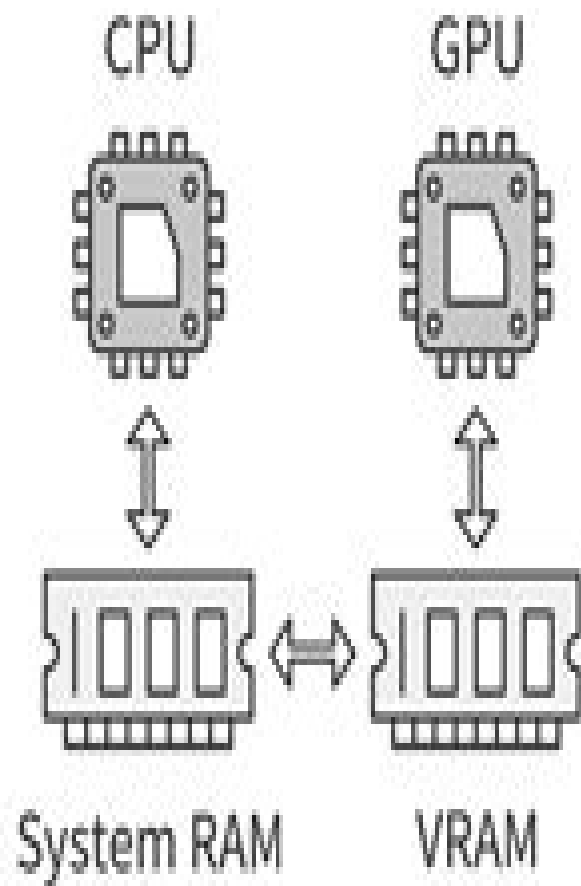
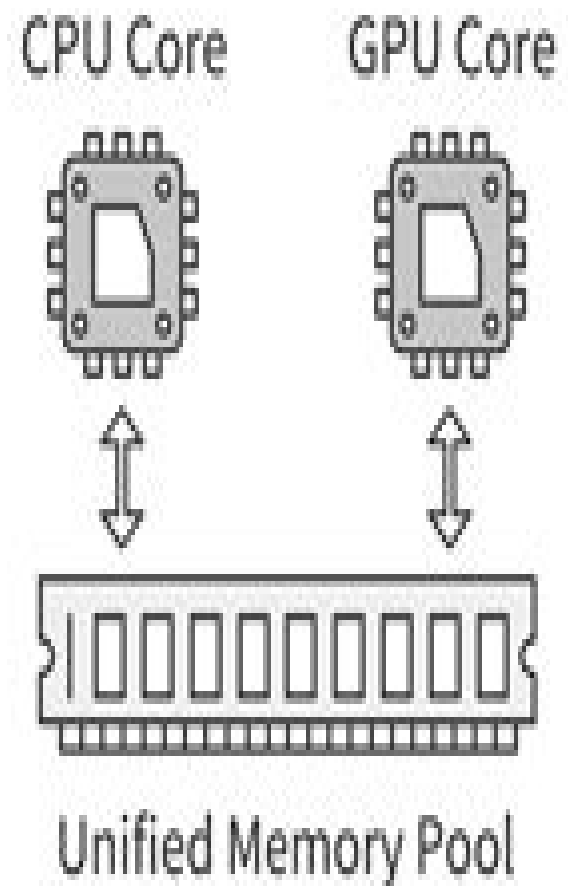




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Body, Memory, and Architecture Kent C. Bloomer, Charles Willard Moore, Robert J. Yudell, Buzz Yudell, 1977-01-01 Traces the significance of the human body in architecture from its early place as the divine organizing principle to its present near elimination

Designing Memory Sabina Tanović, 2019-11-28 This innovative study of memorial architecture investigates how design can translate memories of human loss into tangible structures creating spaces for remembering Using approaches from history psychology anthropology and sociology Sabina Tanović explores purposes behind creating contemporary memorials in a given location their translation into architectural concepts their materialisation in the face of social and political challenges and their influence on the transmission of memory Covering the period from the First World War to the present she looks at memorials such as the Holocaust museums in Mechelen and Drancy as well as memorials for the victims of terrorist attacks to unravel the private and public role of memorial architecture and the possibilities of architecture as a form of agency in remembering and dealing with a difficult past The result is a distinctive contribution to the literature on history and memory and on architecture as a link to the past

Spatial Recall Marc Treib, 2009 Essays from internationally renowned contributors give a variety of perspectives of the role of memory in the built environment how we have read it in the past and how we can create it in the future Essays range from broad topics of message and audience to specific ones of landscape production

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Body, Memory, and Architecture Kent C. Bloomer, 1979

Munich and Memory Gavriel David Rosenfeld, 2000 In the ever growing scholarship on German memory after World War II there is nothing comparable to Rosenfeld's impressively researched account of one city's attempt to master the past through reconstruction Rudy Koshar author of Germany's Transient Pasts In his fascinating history of Munich's postwar architectural reconstruction and social de Nazification Gavriel Rosenfeld shows how closely linked the clearing of both rubble and rabble from the German landscape were and how closely Germany's postwar architectural landscape came to resemble its new democratic mindset James E Young author of The Texture of Memory

Yad Vashem Moshe Safdie, Joan Ockman, 2006-10-20 175 meters long the museum bores like a triangular beam through the Har Hazikaron or Mount of Remembrance It juts out from the hillside at either end allowing visitors to enter and look out This spectacular architecture is the setting for a lavish and impressive exhibition commemorating the Holocaust The structure is the culmination of Moshe Safdie's work in Israel The architect a student of Louis Kahn who began his career with the sensational residential complex Habitat 67 at the 1967 Montreal World's Fair maintains offices in Boston Toronto and Jerusalem The museum's architecture and its series of interior spaces with their carefully designed exhibition facilities are documented in an in-depth photo essay and illustrated with texts and plans

Distributed Shared Memory Jelica Protic, Milo Tomasevic, Veljko Milutinović, 1997-08-10 The papers present in this text survey both distributed shared memory (DSM) efforts and commercial DSM systems The book discusses relevant issues that make the concept of DSM one of the most attractive approaches for building large scale high performance multiprocessor systems The authors provide a general introduction to the DSM field as well as a broad survey of the basic DSM concepts mechanisms design issues and systems The book concentrates on basic DSM algorithms their enhancements and their performance evaluation In addition it details implementations that employ DSM solutions at the software and the hardware level This guide is a research and development reference that provides state of the art information that will be useful to architects designers and programmers of DSM systems

Memory Architecture Exploration for Programmable Embedded Systems Peter Grun, Nikil D. Dutt, Alexandru Nicolau, 2007-05-08 Memory Architecture Exploration for

Programmable Embedded Systems addresses efficient exploration of alternative memory architectures assisted by a compiler in the loop that allows effective matching of the target application to the processor memory architecture This new approach for memory architecture exploration replaces the traditional black box view of the memory system and allows for aggressive co optimization of the programmable processor together with a customized memory system The book concludes with a set of experiments demonstrating the utility of this exploration approach The authors perform architecture and compiler exploration for a set of large real life benchmarks uncovering promising memory configurations from different perspectives such as cost performance and power Handbook of Parallel Computing Sanguthevar Rajasekaran,John Reif,2007-12-20

The ability of parallel computing to process large data sets and handle time consuming operations has resulted in unprecedented advances in biological and scientific computing modeling and simulations Exploring these recent developments the Handbook of Parallel Computing Models Algorithms and Applications provides comprehensive coverage on a Introduction to Magnetic Random-Access Memory Bernard Dieny,Ronald B. Goldfarb,Kyung-Jin Lee,2016-11-14

Magnetic random access memory MRAM is poised to replace traditional computer memory based on complementary metal oxide semiconductors CMOS MRAM will surpass all other types of memory devices in terms of nonvolatility low energy dissipation fast switching speed radiation hardness and durability Although toggle MRAM is currently a commercial product it is clear that future developments in MRAM will be based on spin transfer torque which makes use of electrons spin angular momentum instead of their charge MRAM will require an amalgamation of magnetics and microelectronics technologies However researchers and developers in magnetics and in microelectronics attend different technical conferences publish in different journals use different tools and have different backgrounds in condensed matter physics electrical engineering and materials science This book is an introduction to MRAM for microelectronics engineers written by specialists in magnetic materials and devices It presents the basic phenomena involved in MRAM the materials and film stacks being used the basic principles of the various types of MRAM toggle and spin transfer torque magnetized in plane or perpendicular to plane the back end magnetic technology and recent developments toward logic in memory architectures It helps bridge the cultural gap between the microelectronics and magnetics communities Distributed Database Systems Chhanda Ray,Ray,2009

Distributed Database Systems discusses the recent and emerging technologies in the field of distributed database technology The material is up to date highly readable and illustrated with numerous practical examples The mainstream areas of distributed database technology such as distributed database design distributed DBMS architectures distributed transaction management distributed concurrency control deadlock handling in distributed systems distributed recovery management distributed query processing and optimization data security and catalog management have been covered in detail The popular distributed database systems SDD 1 and R have also been included Computer Memory and Data Storage Azam Seyedi,2024-01-10 Computer Memory and Data Storage presents a comprehensive exploration of the intricacies of memory

design delving into the challenges and advanced techniques involved in optimizing power consumption performance reliability and data integrity The chapters provide a complete understanding of modern memory technologies ranging from radiation hardened memory for space applications to diverse memory designs and their trade offs **Memory Allocation Problems in Embedded Systems** Maria Soto,Marc Sevaux,André Rossi,Johann Laurent,2013-01-24 Memory Allocation Problems in Embedded Systems Optimization Methods Embedded systems are everywhere in contemporary life and are supposed to make our lives more comfortable In industry embedded systems are used to manage and control complex systems e g nuclear power plants telecommunications and flight control and they are also taking an important place in our daily activities e g smartphones security alarms and traffic lights In the design of embedded systems memory allocation and data assignment are among the main challenges that electronic designers have to face In fact they impact heavily on the main cost metrics power consumption performance and area in electronic devices Thus designers of embedded systems have to pay careful attention in order to minimize memory requirements thus improving memory throughput and limiting the power consumption by the system s memory Electronic designers attempt to minimize memory requirements with the aim of lowering the overall system costs A state of the art of optimization techniques for memory management and data assignment is presented in this book *Handbook of Parallel Computing and Statistics* Erricos John Kontoghiorghe,2005-12-21 Technological improvements continue to push back the frontier of processor speed in modern computers Unfortunately the computational intensity demanded by modern research problems grows even faster Parallel computing has emerged as the most successful bridge to this computational gap and many popular solutions have emerged based on its concepts

Architecture in Europe Since 1968 Alexander Tzonis,Liane Lefavre,1992 A comprehensive survey of architectural projects during a time of great ferment from the student movements and political upheaval of 1968 to the official unification of Europe in 1992 Following an extensive introductory essay by the authors four invited essays address the major issues of the period urban context and change typology technology ecology Some 75 individual projects are then presented each accompanied by a critical essay together with photographs drawings site diagrams and construction details 9x12 2

Annotation copyrighted by Book News Inc Portland OR **Still Image Compression on Parallel Computer**

Architectures Savitri Bevinakoppa,2012-12-06 Still Image Compression on Parallel Computer Architectures investigates the application of parallel processing techniques to digital image compression Digital image compression is used to reduce the number of bits required to store an image in computer memory and or transmit it over a communication link Over the past decade advancements in technology have spawned many applications of digital imaging such as photo videotex desktop publishing graphics arts color facsimile newspaper wire phototransmission and medical imaging For many other contemporary applications such as distributed multimedia systems rapid transmission of images is necessary Dollar cost as well as time cost of transmission and storage tend to be directly proportional to the volume of data Therefore application of

digital image compression techniques becomes necessary to minimize costs. A number of digital image compression algorithms have been developed and standardized. With the success of these algorithms, research effort is now directed towards improving implementation techniques. The Joint Photographic Experts Group (JPEG) and Motion Photographic Experts Group (MPEG) are international organizations which have developed digital image compression standards. Hardware VLSI chips which implement the JPEG image compression algorithm are available. Such hardware is specific to image compression only and cannot be used for other image processing applications. A flexible means of implementing digital image compression algorithms is still required. An obvious method of processing different imaging applications on general purpose hardware platforms is to develop software implementations. JPEG uses an 8x8 block of image samples as the basic element for compression. These blocks are processed sequentially. There is always the possibility of having similar blocks in a given image. If similar blocks in an image are located, then repeated compression of these blocks is not necessary. By locating similar blocks in the image, the speed of compression can be increased and the size of the compressed image can be reduced. Based on this concept, an enhancement to the JPEG algorithm is proposed called Block Comparator Technique (BCT). Still Image Compression on Parallel Computer Architectures is designed for advanced students and practitioners of computer science. This comprehensive reference provides a foundation for understanding digital image compression techniques and parallel computer architectures.

Memory Issues in Embedded Systems-on-Chip Preeti Ranjan Panda, Nikil D. Dutt, Alexandru Nicolau, 2012-12-06. Memory Issues in Embedded Systems On Chip Optimizations and Explorations is designed for different groups in the embedded systems on chip arena. First, it is designed for researchers and graduate students who wish to understand the research issues involved in memory system optimization and exploration for embedded systems on chip. Second, it is intended for designers of embedded systems who are migrating from a traditional micro controllers centered board based design methodology to newer design methodologies using IP blocks for processor core based embedded systems on chip. Also, since Memory Issues in Embedded Systems on Chip Optimization and Explorations illustrates a methodology for optimizing and exploring the memory configuration of embedded systems on chip, it is intended for managers and system designers who may be interested in the emerging capabilities of embedded systems on chip design methodologies for memory intensive applications.

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