

**G. Wachutka  
G. Schrag (eds.)**

**Simulation of Semiconductor  
Processes and Devices  
2004**



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# Simulation Of Semiconductor Processes And Devices 2004 Sispad 2004

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## **Simulation Of Semiconductor Processes And Devices 2004 Sispad 2004:**

Simulation of Semiconductor Processes and Devices 2004 Gerhard Wachutka, Gabriele Schrag, 2004-08-23 This volume contains the proceedings of the 10th edition of the International Conference on Simulation of Semiconductor Processes and Devices SISPAD 2004 held in Munich Germany on September 2-4 2004 The conference program included 7 invited plenary lectures and 82 contributed papers for oral or poster presentation which were carefully selected out of a total of 151 abstracts submitted from 14 countries around the world Like the previous meetings SISPAD 2004 provided a world wide forum for the presentation and discussion of recent advances and developments in the theoretical description physical modeling and numerical simulation and analysis of semiconductor fabrication processes device operation and system performance The variety of topics covered by the conference contributions reflects the physical effects and technological problems encountered in consequence of the progressively shrinking device dimensions and the ever growing complexity in device technology

**Simulation of Semiconductor Processes and Devices 2004** Gerhard Wachutka, Gabriele Schrag, 2011-03-30

**Simulation of Semiconductor Processes and Devices 2007** Tibor Grassler, Siegfried Selberherr, 2007-11-18 The Twelfth International Conference on Simulation of Semiconductor Processes and Devices SISPAD 2007 continues a long series of conferences and is held in September 2007 at the TU Wien Vienna Austria The conference is the leading forum for Technology Computer Aided Design TCAD held alternately in the United States Japan and Europe The first SISPAD conference took place in Tokyo in 1996 as the successor to three preceding conferences NUPAD VPAD and SISDEP With its longstanding history SISPAD provides a world wide forum for the presentation and discussion of outstanding recent advances and developments in the field of numerical process and device simulation Driven by the ongoing miniaturization in semiconductor fabrication technology the variety of topics discussed at this meeting reflects the ever growing complexity of the subject Apart from the classic topics like process device and interconnect simulation mesh generation a broad spectrum of numerical issues and compact modeling new simulation approaches like atomistic and first principles methods have emerged as important fields of research and are currently making their way into standard TCAD suites

*Physics of Semiconductor Devices* Massimo Rudan, 2017-09-27 This textbook describes the basic physics of semiconductors including the hierarchy of transport models and connects the theory with the functioning of actual semiconductor devices Details are worked out carefully and derived from the basic physical concepts while keeping the internal coherence of the analysis and explaining the different levels of approximation Coverage includes the main steps used in the fabrication process of integrated circuits diffusion thermal oxidation epitaxy and ion implantation Examples are based on silicon due to its industrial importance Several chapters are included that provide the reader with the quantum mechanical concepts necessary for understanding the transport properties of crystals The behavior of crystals incorporating a position dependent impurity distribution is described and the different hierarchical transport models for semiconductor

devices are derived from the Boltzmann transport equation to the hydrodynamic and drift diffusion models The transport models are then applied to a detailed description of the main semiconductor device architectures bipolar MOS CMOS including a number of solid state sensors The final chapters are devoted to the measuring methods for semiconductor device parameters and to a brief illustration of the scaling rules and numerical methods applied to the design of semiconductor devices

**International Conference on Simulation of Semiconductor Processes and Devices**, 2005 *Hot Carrier Degradation in Semiconductor Devices* Tibor Grasser, 2014-10-29 This book provides readers with a variety of tools to address the challenges posed by hot carrier degradation one of today's most complicated reliability issues in semiconductor devices Coverage includes an explanation of carrier transport within devices and book keeping of how they acquire energy become hot interaction of an ensemble of colder and hotter carriers with defect precursors which eventually leads to the creation of a defect and a description of how these defects interact with the device degrading its performance

*Fundamentals of Modern VLSI Devices* Yuan Taur, Tak H. Ning, 2021-12-02 A thoroughly updated third edition of an classic and widely adopted text perfect for practical transistor design and in the classroom Covering a variety of recent developments the internationally renowned authors discuss in detail the basic properties and designs of modern VLSI devices as well as factors affecting performance Containing around 25% new material coverage has been expanded to include high k gate dielectrics metal gate technology strained silicon mobility non GCA Gradual Channel Approximation modelling of MOSFETs short channel FinFETs and symmetric lateral bipolar transistors on SOI Chapters have been reorganized to integrate the appendices into the main text to enable a smoother learning experience and numerous additional end of chapter homework exercises 30% are included to engage students with real world problems and test their understanding A perfect text for senior undergraduate and graduate students taking advanced semiconductor devices courses and for practicing silicon device professionals in the semiconductor industry

[The Wigner Monte Carlo Method for Nanoelectronic Devices](#) Damien Querlioz, Philippe Dollfus, 2013-03-01 The emergence of nanoelectronics has led us to renew the concepts of transport theory used in semiconductor device physics and the engineering community It has become crucial to question the traditional semi classical view of charge carrier transport and to adequately take into account the wave like nature of electrons by considering not only their coherent evolution but also the out of equilibrium states and the scattering effects This book gives an overview of the quantum transport approaches for nanodevices and focuses on the Wigner formalism It details the implementation of a particle based Monte Carlo solution of the Wigner transport equation and how the technique is applied to typical devices exhibiting quantum phenomena such as the resonant tunnelling diode the ultra short silicon MOSFET and the carbon nanotube transistor In the final part decoherence theory is used to explain the emergence of the semi classical transport in nanodevices

**Fabless Semiconductor Manufacturing** Chinmay K. Maiti, 2022-11-17 This book deals with 3D nanodevices such as nanowire and nanosheet transistors at 7 nm and smaller technology nodes It

discusses technology computer aided design TCAD simulations of stress and strain engineered advanced semiconductor devices including III nitride and RF FDSOI CMOS for flexible and stretchable electronics The book focuses on how to set up 3D TCAD simulation tools from mask layout to process and device simulation including fabless intelligent manufacturing The simulation examples chosen are from the most popular devices in use today and provide useful technology and device physics insights In order to extend the role of TCAD in the More than Moore era the design issues related to strain engineering for flexible and stretchable electronics have been introduced for the first time

**EDA for IC Implementation, Circuit Design, and Process Technology** Luciano Lavagno, Louis Scheffer, Grant Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The second volume EDA for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic to GDSII a file format used to transfer data of semiconductor physical layout analog mixed signal design physical verification and technology CAD TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale power supply network design and analysis design modeling and much more Save on the complete set

**Silicon Devices and Process Integration** Badih El-Kareh, 2009-01-09 Silicon Devices and Process Integration covers state of the art silicon devices their characteristics and their interactions with process parameters It serves as a comprehensive guide which addresses both the theoretical and practical aspects of modern silicon devices and the relationship between their electrical properties and processing conditions The book is compiled from the author's industrial and academic lecture notes and reflects years of experience in the development of silicon devices Features include A review of silicon properties which provides a foundation for understanding the device properties discussion including mobility enhancement by straining silicon State of the art technologies on high K gate dielectrics low K dielectrics Cu interconnects and SiGe BiCMOS CMOS only applications such as subthreshold current and parasitic latch up Advanced Enabling processes and process integration This book is written for engineers and scientists in semiconductor research development and manufacturing The problems at the end of each chapter and the numerous charts figures and tables also make it appropriate for use as a text in graduate and advanced undergraduate courses in electrical engineering and materials science

**FinFETs and Other Multi-Gate Transistors** J.-P. Colinge, 2008 This book explains the physics and properties of multi gate field effect transistors MuGFETs how they are made and how circuit designers can use them to improve the performances of integrated circuits It covers the emergence of quantum effects due to the reduced size of the devices and describes the evolution of the MOS transistor from classical structures to SOI silicon on insulator and then to MuGFETs

**Low-Power Variation-Tolerant Design in Nanometer Silicon** Swarup Bhunia, Saibal Mukhopadhyay, 2010-11-10 Design considerations for low power operations and robustness with respect to variations typically impose contradictory requirements Low power design techniques such as voltage scaling dual

threshold assignment and gate sizing can have large negative impact on parametric yield under process variations This book focuses on circuit architectural design techniques for achieving low power operation under parameter variations We consider both logic and memory design aspects and cover modeling and analysis as well as design methodology to achieve simultaneously low power and variation tolerance while minimizing design overhead This book will discuss current industrial practices and emerging challenges at future technology nodes

### **Electronic Design Automation for IC**

**Implementation, Circuit Design, and Process Technology** Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

*BSIM-Bulk MOSFET Model for IC Design - Digital, Analog, RF and High-Voltage* Chenming Hu,Harshit Agarwal,Chetan Gupta,Yogesh Singh Chauhan,2023-04-26 BSIM Bulk MOSFET Model for IC Design Digital Analog RF and High Voltage provides in depth knowledge of the internal operation of the model The authors not only discuss the fundamental core of the model but also provide details of the recent developments and new real device effect models In addition the book covers the parameter extraction procedures addressing geometrical scaling temperatures and more There is also a dedicated chapter on extensive quality testing procedures and experimental results This book discusses every aspect of the model in detail and hence will be of significant use for the industry and academia Those working in the semiconductor industry often run into a variety of problems like model non convergence or non physical simulation results This is largely due to a limited understanding of the internal operations of the model as literature and technical manuals are insufficient This also creates huge difficulty in developing their own IP models Similarly circuit designers and researcher across the globe need to know new features available to them so that the circuits can be more efficiently designed Reviews the latest advances in fabrication methods for metal chalcogenide based biosensors Discusses

the parameters of biosensor devices to aid in materials selection Provides readers with a look at the chemical and physical properties of reactive metals noble metals transition metals chalcogenides and their connection to biosensor device performance

**Electronic Devices Architectures for the NANO-CMOS Era** Simon Deleonibus,2019-05-08 In this book internationally recognized researchers give a state of the art overview of the electronic device architectures required for the nano CMOS era and beyond Challenges relevant to the scaling of CMOS nanoelectronics are addressed through different core CMOS and memory device options in the first part of the book The second part reviews new device concepts for nanoelectronics beyond CMOS The book covers the fundamental limits of core CMOS improving scaling by the introduction of new materials or processes new architectures using SOI multigates and multichannels and quantum computing

**Silicon-Germanium Heterojunction Bipolar Transistors for Mm-wave Systems Technology, Modeling and Circuit Applications** Niccolò Rinaldi,Michael Schröter,2022-09-01 The semiconductor industry is a fundamental building block of the new economy there is no area of modern life untouched by the progress of nanoelectronics The electronic chip is becoming an ever increasing portion of system solutions starting initially from less than 5% in the 1970 microcomputer era to more than 60% of the final cost of a mobile telephone 50% of the price of a personal computer representing nearly 100% of the functionalities and 30% of the price of a monitor in the early 2000 s Interest in utilizing the sub mm wave frequency spectrum for commercial and research applications has also been steadily increasing Such applications which constitute a diverse but sizeable future market span a large variety of areas such as health material science mass transit industrial automation communications and space exploration Silicon Germanium Heterojunction Bipolar Transistors for mm Wave Systems Technology Modeling and Circuit Applications provides an overview of results of the DOTSEVEN EU research project and as such focusses on key material developments for mm Wave Device Technology It starts with the motivation at the beginning of the project and a summary of its major achievements The subsequent chapters provide a detailed description of the obtained research results in the various areas of process development device simulation compact device modeling experimental characterization reliability sub mm wave circuit design and systems

*Toward Quantum FinFET*

Weihua Han,Zhiming M. Wang,2013-11-23 This book reviews a range of quantum phenomena in novel nanoscale transistors called FinFETs including quantized conductance of 1D transport single electron effect tunneling transport etc The goal is to create a fundamental bridge between quantum FinFET and nanotechnology to stimulate readers interest in developing new types of semiconductor technology Although the rapid development of micro nano fabrication is driving the MOSFET downscaling trend that is evolving from planar channel to nonplanar FinFET silicon based CMOS technology is expected to face fundamental limits in the near future Therefore new types of nanoscale devices are being investigated aggressively to take advantage of the quantum effect in carrier transport The quantum confinement effect of FinFET at room temperatures was reported following the breakthrough to sub 10nm scale technology in silicon nanowires With chapters written by leading

scientists throughout the world **Toward Quantum FinFET** provides a comprehensive introduction to the field as well as a platform for knowledge sharing and dissemination of the latest advances As a roadmap to guide further research in an area of increasing importance for the future development of materials science nanofabrication technology and nano electronic devices the book can be recommended for Physics Electrical Engineering and Materials Science departments and as a reference on micro nano electronic science and device design Offers comprehensive coverage of novel nanoscale transistors with quantum confinement effect Provides the keys to understanding the emerging area of the quantum FinFET Written by leading experts in each research area Describes a key enabling technology for research and development of nanofabrication and nanoelectronic devices     **Stress and Strain Engineering at Nanoscale in Semiconductor Devices** Chinmay K. Maiti, 2021-06-29 Anticipating a limit to the continuous miniaturization More Moore intense research efforts are being made to co integrate various functionalities More than Moore in a single chip Currently strain engineering is the main technique used to enhance the performance of advanced semiconductor devices Written from an engineering applications standpoint this book encompasses broad areas of semiconductor devices involving the design simulation and analysis of Si heterostructure silicon germanium SiGe and III N compound semiconductor devices The book provides the background and physical insight needed to understand the new and future developments in the technology CAD TCAD design at the nanoscale Features Covers stress strain engineering in semiconductor devices such as FinFETs and III V Nitride based devices Includes comprehensive mobility model for strained substrates in global and local strain techniques and their implementation in device simulations Explains the development of strain stress relationships and their effects on the band structures of strained substrates Uses design of experiments to find the optimum process conditions Illustrates the use of TCAD for modeling strain engineered FinFETs for DC and AC performance predictions This book is for graduate students and researchers studying solid state devices and materials microelectronics systems and controls power electronics nanomaterials and electronic materials and devices     **Large-Scale Scientific Computing** Ivan Lirkov, Svetozar D. Margenov, Jerzy Waśniewski, 2015-11-29 This book constitutes the thoroughly refereed post conference proceedings of the 10th International Conference on Large Scale Scientific Computations LSSC 2015 held in Sozopol Bulgaria in June 2015 The 49 revised full papers presented were carefully reviewed and selected from 64 submissions The general theme for LSSC 2015 was Large Scale Scientific Computing with a particular focus on the organized special sessions enabling exascale computation control and uncertain systems computational microelectronics from monte carlo to deterministic approaches numerical methods for multiphysics problems large scale models numerical methods parallel computations and applications mathematical modeling and analysis of PDEs describing physical problems a posteriori error control and iterative methods for maxwell type problems efficient algorithms for hybrid HPC systems multilevel methods on graphs and applications of metaheuristics to large scale problems



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