

# Performance of an Occam/transputer implementation of interval arithmetic\*

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## Abstract

Rounded interval arithmetic is very easy to implement by means of directed rounding arithmetic operators. Such operators are available in the IEEE floating point arithmetic of the transputer. When a few small pieces of assembly language code is used to access the directed rounding operators, the four basic rounded interval arithmetic operators can easily be expressed in the programming language Occam. The performance of this implementation is assessed and it is shown that the time consuming part of the calculation are not the directed rounding floating point operations as one might have expected. Most of the time is spent with transport of operands to and from the on-chip floating point unit and the procedure call/parameter passing overhead. Based on this experience the implementation is improved. This implementation runs with 0.15 MIOPS (Million Interval Operations Per Second) or 0.30 MFLOPS on an example interval calculation proposed by Moore. Furthermore, it is demonstrated that an advanced interval language compiler may provide a performance of 0.30 MIOPS or 0.59 MFLOPS on this example calculation.

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\*Presented at the conference on Numerical Analysis with Automatic Result Verification, Lafayette, Louisiana, 1993.

# Occam The Transputer Oug 9

**Hugh C. Webber**



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**Transputer and Occam Research** World Occam and Transputer User Group. Technical Meeting, 1993 The papers included in this book provide a wide coverage of current thinking and the new concepts which are being developed resulting from the introduction of the T9000 The role and use of the newly developed transputer and associated routing component the C104 is discussed and the use of existing range of transputers in the embedded systems market is also dealt with

**Transputer/Occam Japan 4** S. Noguchi, H. Umeo, 1992 Comprises discussions presented by leading international experts on the application of transputers Coverage includes formalism specifications and design concurrent systems development parallel algorithms real time systems and applications **Occam and the Transputers, Current**

**Developments** World Occam and Transputer User Group. Technical Meeting, 1991 Examines applications of transputers and OCCAM including numerical image processing neural networks natural language processing networks and biological systems as well as tools routing and mapping The T9000 new generation transputer introduction is further discussed

**Transputer and Occam Developments** World Occam and Transputer User Group. Technical Meeting, 1995 This volume contains papers presented at the 18th meeting of the World Occam and Transputer User Group Wotug The papers cover a wide range of transputer and OCCAM related topics such as the porting and development of the OCCAM language highlighting the need for cross platform implementations of OCCAM compilers design approaches and applications

**Transputer Applications and Systems '94** A. De Gloria, M. R. Jane, D. Marini, 1994 Proceedings Parallel Computing

**Transputers '94** Monique Becker, Luc Litzler, Michel Trehel, 1994 The research reports presented in this volume focus on the implications of the T9000 microprocessor which offers new elements in transputing and parallel programming Subjects discussed include genetic algorithms image analysis neural networks robotics and parallel architectures Transputer Research and Applications 5 North American Transputer Users Group. Conference, 1992 Parallel processing is now becoming a household word among computer researchers and designers This work contains 29 contributions from leading experts in the field attending the 1992 NATUG conference *Transputer Research and Applications 6* North American Transputer Users Group. Conference, 1993 Papers in this book report on a wide variety of multicomputer applications systems and architectures They all have one aspect in common which is message passing multiprocessors It includes research presentations of the T9000 TI C 40 and T8 i860 based multicomputers **Transputer Research and Applications 7** North

American Transputer Users Group. Conference, 1995 This work comprises the proceedings of the Transputer Research and Applications Conference held in Georgia from October 23rd to October 25th 1994 The conference is sponsored by the North American Transputer Users Group NATUG **Progress in Transputer and Occam Research** World Occam and Transputer User Group. Technical Meeting, 1994 The contributed chapters to this volume provide a broad coverage of the areas of research in current parallel computing architectures languages and tools graphics and fault tolerance Additionally

the Inmos approach to building an asynchronous transfer mode system and the University of Twente method for designing system level embedded controllers are featured in this work     The Transputer in Australasia Australian Transputer and OCCAM User Group,1990     Transputer Applications and Systems '93 Reinhard Grebe,1993 Proceedings Parallel Computing     **Transputer Applications** Mike R. Jane,Raymond J. Fawcett,Terry P. Mawby,1992 The symposium held in Reading in March 1992 celebrated the completion of a 5 year Initiative in the Engineering Applications of Transputers It reviewed achievements in a range of applications and supporting fields and predicted future developments This book represents a collection of articles presented at this meeting as well as independent reviews of the Transputer Initiative

**The Transputer in Australasia** John Hulskamp,Terry Bossomaier,Tom Hintz,1991 Covering recent developments involving transputer technology in the Australasian region this work contains 35 papers addressing transputer applications in communications networks fuzzy systems image processing and medical equipment     **Image Processing and Transputers**

Hugh C. Webber,1992     *Transputer Research and Applications 4* North American Transputer Users Group. Conference,North American Transputer Users Group,1990     *Parallel Computing and Transputers* D. Arnold,1994 The broadening of interest in parallel computing and transputers is reflected in this text Topics covered include concurrent programming graphics and image processing and robotics and control It is based on the proceedings of the 6th Australian Transputer and Occam User Group     **Real-time Systems with Transputers** Occam User Group. Technical Meeting,1990

Transputers and Parallel Applications John Hulskamp,David Jones,1992-11 Presents the proceedings of a Transputer and OCCAM User Group Conference held in Melbourne in November 1992 discussing recent developments in the field of transputers and parallel applications     **Parallel Programming and Applications** Peter Fritzson,Leif Finmo,1995 ZEUS Centres of European Supercomputing is a network for information exchange and co operation between European Supercomputer Centres During the fall of 1994 the idea was put forward to start an annual workshop to stimulate the exchange of ideas and experience in parallel programming and computing between researchers and users from industry and academia The first workshop in this series the ZEUS 95 Workshop on Parallel Programming and Computation is organized at Linkoping University where the Swedish ZEUS centre NSC National Supercomputer Centre is located This is open for all researchers and users in the field of parallel computing

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