

LOW-POWER VLSI DESIGN TECHNIQUES AND APPLICATIONS

LOW-POWER VLSI DESIGN: TECHNIQUES AND APPLICATIONS



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Low Power Vlsi Design And Technology

Gary K. Yeap



Low Power Vlsi Design And Technology:

Low Power Vlsi Design And Technology Farid N Najm, Garey K-h Yeap, 1996-08-30 Low power and low energy VLSI has become an important issue in today's consumer electronics. This book is a collection of pioneering applied research papers in low power VLSI design and technology. A comprehensive introductory chapter presents the current status of the industry and academic research in the area of low power VLSI design and technology. Other topics cover logic synthesis, floorplanning, circuit design and analysis from the perspective of low power requirements. The readers will have a sampling of some key problems in this area as the low power solutions span the entire spectrum of the design process. The book also provides excellent references on up to date research and development issues with practical solution techniques.

Low-Power VLSI Circuits and Systems Ajit Pal, 2014-11-17 The book provides a comprehensive coverage of different aspects of low power circuit synthesis at various levels of design hierarchy starting from the layout level to the system level. For a seamless understanding of the subject, basics of MOS circuits have been introduced at transistor, gate and circuit level, followed by various low power design methodologies such as supply voltage scaling, switched capacitance minimization techniques and leakage power minimization approaches. The content of this book will prove useful to students, researchers as well as practicing engineers.

Low Power VLSI Design Angsuman Sarkar, Swapnadip De, Manash Chanda, Chandan Kumar Sarkar, 2016-08-08 This book teaches basic and advanced concepts, new methodologies and recent developments in VLSI technology with a focus on low power design. It provides insight on how to use Tanner, Spice, Cadence tools, Xilinx tools, VHDL programming and Synopsis to design simple and complex circuits using latest state of the art technologies. Emphasis is placed on fundamental transistor circuit level design concepts.

Design and Modeling of Low Power VLSI Systems

Sharma, Manoj, Gautam, Ruchi, Khan, Mohammad Ayoub, 2016-06-06 Very Large Scale Integration VLSI Systems refer to the latest development in computer microchips which are created by integrating hundreds of thousands of transistors into one chip. Emerging research in this area has the potential to uncover further applications for VLSI technologies in addition to system advancements. Design and Modeling of Low Power VLSI Systems analyzes various traditional and modern low power techniques for integrated circuit design in addition to the limiting factors of existing techniques and methods for optimization. Through a research based discussion of the technicalities involved in the VLSI hardware development process cycle, this book is a useful resource for researchers, engineers and graduate level students in computer science and engineering.

Practical Low Power Digital VLSI Design Gary K. Yeap, 2012-12-06 Practical Low Power Digital VLSI Design emphasizes the optimization and trade off techniques that involve power dissipation in the hope that the readers are better prepared the next time they are presented with a low power design problem. The book highlights the basic principles, methodologies and techniques that are common to most CMOS digital designs. The advantages and disadvantages of a particular low power technique are discussed. Besides the classical area performance trade off, the impact to design cycle time, complexity risk

testability and reusability are discussed. The wide impacts to all aspects of design are what make low power problems challenging and interesting. Heavy emphasis is given to top down structured design style with occasional coverage in the semicustom design methodology. The examples and design techniques cited have been known to be applied to production scale designs or laboratory settings. The goal of Practical Low Power Digital VLSI Design is to permit the readers to practice the low power techniques using current generation design style and process technology. Practical Low Power Digital VLSI Design considers a wide range of design abstraction levels spanning circuit logic architecture and system. Substantial basic knowledge is provided for qualitative and quantitative analysis at the different design abstraction levels. Low power techniques are presented at the circuit logic architecture and system levels. Special techniques that are specific to some key areas of digital chip design are discussed as well as some of the low power techniques that are just appearing on the horizon. Practical Low Power Digital VLSI Design will be of benefit to VLSI design engineers and students who have a fundamental knowledge of CMOS digital design.

Low Power VLSI Circuits and Systems Mr. Rohit Manglik, 2024-04-06 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources. Specializing in competitive exams and academic support, EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels.

Low-Power Digital VLSI Design Abdellatif Bellaouar, Mohamed Elmasry, 2012-12-06 Low Power Digital VLSI Design Circuits and Systems addresses both process technologies and device modeling. Power dissipation in CMOS circuits, several practical circuit examples and low power techniques are discussed. Low voltage issues for digital CMOS and BiCMOS circuits are emphasized. The book also provides an extensive study of advanced CMOS subsystem design. A low power design methodology is presented with various power minimization techniques at the circuit logic architecture and algorithm levels. Features: Low voltage CMOS device modeling, technology files, design rules, Switching activity concept, low power guidelines to engineering practice, Pass transistor logic families, Power dissipation of I/O circuits, Multi and low VT CMOS logic, static power reduction circuit techniques, State of the art design of low voltage BiCMOS and CMOS circuits, Low power techniques in CMOS SRAMS and DRAMS, Low power on chip voltage down converter design, Numerous advanced CMOS subsystems e.g. adders, multipliers, data path memories, regular structures, phase locked loops with several design options trading power, delay and area. Low power design methodology, power estimation techniques, Power reduction techniques at the logic architecture and algorithm levels. More than 190 circuits explained at the transistor level.

The Electrical Engineering Handbook Wai Kai Chen, 2004-11-16 The Electrical Engineer's Handbook is an invaluable reference source for all practicing electrical engineers and students. Encompassing 79 chapters, this book is intended to enlighten and refresh knowledge of the practicing engineer or to help educate engineering students. This text will most likely be the engineer's first choice in looking for a solution. Extensive complete references to other sources are provided throughout. No other book has the breadth and depth of

coverage available here This is a must have for all practitioners and students The Electrical Engineer s Handbook provides the most up to date information in Circuits and Networks Electric Power Systems Electronics Computer Aided Design and Optimization VLSI Systems Signal Processing Digital Systems and Computer Engineering Digital Communication and Communication Networks Electromagnetics and Control and Systems About the Editor in Chief Wai Kai Chen is Professor and Head Emeritus of the Department of Electrical Engineering and Computer Science at the University of Illinois at Chicago He has extensive experience in education and industry and is very active professionally in the fields of circuits and systems He was Editor in Chief of the IEEE Transactions on Circuits and Systems Series I and II President of the IEEE Circuits and Systems Society and is the Founding Editor and Editor in Chief of the Journal of Circuits Systems and Computers He is the recipient of the Golden Jubilee Medal the Education Award and the Meritorious Service Award from the IEEE Circuits and Systems Society and the Third Millennium Medal from the IEEE Professor Chen is a fellow of the IEEE and the American Association for the Advancement of Science 77 chapters encompass the entire field of electrical engineering THOUSANDS of valuable figures tables formulas and definitions Extensive bibliographic references

Energy Systems Design for Low-Power Computing Gatti, Rathishchandra Ramachandra, Singh, Chandra, P., Srividya, Bhat, Sandeep, 2023-03-07 With the advancement in computing technologies the need for power is also increasing Approximately 3% of the total power consumption is spent by data centers and computing devices This percentage will rise when more internet of things IoT devices are connected to the web The handling of this data requires immense power Energy Systems Design for Low Power Computing disseminates the current research and the state of the art technologies topologies standards and techniques for the deployment of energy intelligence in edge computing distributed computing and centralized computing infrastructure Covering topics such as electronic cooling stochastic data analysis and energy consumption this premier reference source is an excellent resource for data center designers VLSI designers network developers students and teachers of higher education librarians researchers and academicians

Low Power Design Methodologies Jan M. Rabaey, Massoud Pedram, 2012-12-06 Low Power Design Methodologies presents the first in depth coverage of all the layers of the design hierarchy ranging from the technology circuit logic and architectural levels up to the system layer The book gives insight into the mechanisms of power dissipation in digital circuits and presents state of the art approaches to power reduction Finally it introduces a global view of low power design methodologies and how these are being captured in the latest design automation environments The individual chapters are written by the leading researchers in the area drawn from both industry and academia Extensive references are included at the end of each chapter Audience A broad introduction for anyone interested in low power design Can also be used as a text book for an advanced graduate class A starting point for any aspiring researcher

Gain-Cell Embedded DRAMs for Low-Power VLSI Systems-on-Chip Pascal Meinerzhagen, Adam Teman, Robert Gitterman, Noa Edri, Andreas Burg, Alexander Fish, 2017-07-06 This book pioneers the field of gain cell

embedded DRAM GC eDRAM design for low power VLSI systems on chip SoCs Novel GC eDRAMs are specifically designed and optimized for a range of low power VLSI SoCs ranging from ultra low power to power aware high performance applications After a detailed review of prior art GC eDRAMs an analytical retention time distribution model is introduced and validated by silicon measurements which is key for low power GC eDRAM design The book then investigates supply voltage scaling and near threshold voltage NTV operation of a conventional gain cell GC before presenting novel GC circuit and assist techniques for NTV operation including a 3 transistor full transmission gate write port reverse body biasing RBB and a replica technique for optimum refresh timing Next conventional GC bitcells are evaluated under aggressive technology and voltage scaling down to the subthreshold domain before novel bitcells for aggressively scaled CMOS nodes and soft error tolerance as presented including a 4 transistor GC with partial internal feedback and a 4 transistor GC with built in redundancy

Intelligent Systems Design and Applications Ajith Abraham, Aswani Kumar Cherukuri, Patricia Melin, Niketa Gandhi, 2019-04-11 This book highlights recent research on Intelligent Systems and Nature Inspired Computing It presents 212 selected papers from the 18th International Conference on Intelligent Systems Design and Applications ISDA 2018 and the 10th World Congress on Nature and Biologically Inspired Computing NaBIC which was held at VIT University India ISDA NaBIC 2018 was a premier conference in the field of Computational Intelligence and brought together researchers engineers and practitioners whose work involved intelligent systems and their applications in industry and the real world Including contributions by authors from over 40 countries the book offers a valuable reference guide for all researchers students and practitioners in the fields of Computer Science and Engineering

Smart Intelligent Computing and Communication Technology V.D. Ambeth Kumar, S. Malathi, V.E. Balas, 2021-10-07 Recent developments in the fields of intelligent computing and communication have paved the way for the handling of current and upcoming problems and brought about significant technological advancements This book presents the proceedings of IConIC 2021 the 4th International Conference on Intelligent Computing held on 26 and 27 March 2021 in Chennai India The principle objective of the annual IConIC conference is to provide an international scientific forum where participants can exchange innovative ideas in relevant fields and interact in depth through discussion with their peer group The theme of the 2021 conference and this book is Smart Intelligent Computing and Communication Technology and the 109 papers included here focus on the technological innovations and trendsetting initiatives in medicine industry education and security that are improving and optimizing business and technical processes and enabling inclusive growth The papers are grouped under 2 headings Evolution of Computing Intelligence and Computing and Communication and cover a broad range of intelligent computing research and applications The book provides an overview of the cutting edge developments and emerging areas of study in the technological fields of intelligent computing and will be of interest to researchers and practitioners from both academia and industry

Logic Synthesis and Verification Soha Hassoun, Tsutomu Sasao, 2012-12-06 Research and development of

logic synthesis and verification have matured considerably over the past two decades. Many commercial products are available and they have been critical in harnessing advances in fabrication technology to produce today's plethora of electronic components. While this maturity is assuring, the advances in fabrication continue to seemingly present unwieldy challenges. Logic Synthesis and Verification provides a state of the art view of logic synthesis and verification. It consists of fifteen chapters, each focusing on a distinct aspect. Each chapter presents key developments, outlines future challenges, and lists essential references. Two unique features of this book are technical strength and comprehensiveness. The book chapters are written by twenty-eight recognized leaders in the field and reviewed by equally qualified experts. The topics collectively span the field. Logic Synthesis and Verification fills a current gap in the existing CAD literature. Each chapter contains essential information to study a topic at a great depth and to understand further developments in the field. The book is intended for seniors, graduate students, researchers, and developers of related Computer Aided Design (CAD) tools.

From the foreword: The commercial success of logic synthesis and verification is due in large part to the ideas of many of the authors of this book. Their innovative work contributed to design automation tools that permanently changed the course of electronic design.

by Aart J. de Geus, Chairman and CEO, Synopsys Inc. Micro and Nanoelectronics Devices, Circuits and Systems

Trupti Ranjan Lenka, Samar K. Saha, Lan Fu, 2023-09-02

This book presents select proceedings of the International Conference on Micro and Nanoelectronics Devices, Circuits and Systems (MNDCS 2023). The book includes cutting-edge research papers in the emerging fields of micro and nanoelectronics devices, circuits, and systems from experts working in these fields over the last decade. The book is a unique collection of chapters from different areas with a common theme and is immensely useful to academic researchers and practitioners in the industry who work in this field.

Smart Computing

Mohammad Ayoub Khan, Sanjay Gairola, Bhola Jha, Pushkar Praveen, 2021-05-12

The field of SMART technologies is an interdependent discipline. It involves the latest burning issues ranging from machine learning, cloud computing, optimisations, modelling techniques, Internet of Things, data analytics, and Smart Grids, among others, that are all new fields. It is an applied and multi-disciplinary subject with a focus on Specific, Measurable, Achievable, Realistic, Timely system operations combined with Machine Intelligence, Real Time computing. It is not possible for any one person to comprehensively cover all aspects relevant to SMART Computing in a limited extent work. Therefore, these conference proceedings address various issues through the deliberations by distinguished Professors and researchers. The SMARTCOM 2020 proceedings contain tracks dedicated to different areas of smart technologies such as Smart System and Future Internet, Machine Intelligence, and Data Science, Real Time, and VLSI Systems, Communication, and Automation Systems. The proceedings can be used as an advanced reference for research and for courses in smart technologies taught at the graduate level.

Software Radio Jeffrey Hugh Reed, 2002

This guide to radio engineering covers every technique. DSP and RF engineers need to build software radios for a wide variety of wireless systems using DSP techniques. Included are practical guidelines for choosing DSP microprocessors.

and systematic object oriented software design techniques Computing and Network Sustainability Sheng-Lung Peng, Nilanjan Dey, Mahesh Bunde, 2019-05-02 This book offers a compilation of technical papers presented at the International Research Symposium on Computing and Network Sustainability IRSCNS 2018 held in Goa India on 30-31st August 2018. It covers areas such as sustainable computing and security, sustainable systems and technologies, sustainable methodologies and applications, sustainable networks, applications and solutions, user-centered services and systems, and mobile data management. Presenting novel and recent technologies, it is a valuable resource for researchers and industry professionals alike.

Compact Models and Performance Investigations for Subthreshold Interconnects Rohit Dhiman, Rajeevan Chandel, 2014-11-07 The book provides a detailed analysis of issues related to sub-threshold interconnect performance from the perspective of analytical approach and design techniques. Particular emphasis is laid on the performance analysis of coupling noise and variability issues in sub-threshold domain to develop efficient compact models. The proposed analytical approach gives physical insight of the parameters affecting the transient behavior of coupled interconnects. Remedial design techniques are also suggested to mitigate the effect of coupling noise. The effects of wire width, spacing between the wires, wire length are thoroughly investigated. In addition, the effect of parameters like driver strength on peak coupling noise has also been analyzed. Process voltage and temperature variations are prominent factors affecting sub-threshold design and have also been investigated. The process variability analysis has been carried out using parametric analysis, process corner analysis, and Monte Carlo technique. The book also provides a qualitative summary of the work reported in the literature by various researchers in the design of digital sub-threshold circuits. This book should be of interest for researchers and graduate students with deeper insights into sub-threshold interconnect models. In particular, in this sense, this book will best fit as a text book and/or a reference book for students who are initiated in the area of research and advanced courses in nanotechnology, interconnect design, and modeling.

Integrated Technologies in Electrical, Electronics and Biotechnology Engineering Gaurav Aggarwal, Ashutosh Tripathi, Himani Goyal, Sharma, Tripti Sharma, Rishabh Dev Shukla, 2025-03-31 The conference was aimed to bring researchers, practicing engineers, faculty members, and students from across the globe to a common platform to share their research ideas that would pave the way to attain solutions to various real-time problems. Many eminent researchers from different countries participated and interacted with the young students and budding researchers from various institutions. The objective of this conference was to connect with junior and senior scholars working with educational architecture of the past, present, or future in the area of Semiconductor Devices, Electronic Circuit Design, Machine Vision, Signal Processing, Communication Technologies and Systems, Electromagnetic RF, Microwave, Wearable Technology, Nano Technologies, IC Fabrication, Biotechnology, Automation, Robotics, Electrical Machines, and Adjustable Speed Drives, Renewable Energy Sources, Smart grids, Technologies Applications. Key features included keynote presentations from renowned experts, paper presentations showcasing novel research, interactive panel discussions, and

exploring practical applications of emerging technologies

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