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Ioan Susnea, Marian Mitescu



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to systematize the experience and research in exploiting various gettering techniques in microelectronics and nanoelectronics to identify new directions in research particularly to enhance the perspective of professionals and young researchers and specialists to fill a gap in the contemporary literature on the underlying semiconductor material theory The authors address not only well established gettering techniques but also describe contemporary trends in gettering technologies from an international perspective The types and properties of structural defects in semiconductors their generating and their transforming mechanisms during fabrication are described The primary emphasis is placed on classifying and describing specific gettering techniques their specificity arising from both their position in a general technological process and the regimes of their application This book addresses both engineers and material scientists interested in semiconducting materials theory and also undergraduate and graduate students in solid state microelectronics and nanoelectronics A comprehensive list of references provides readers with direction for further reading

Introduction to Microfabrication Sami Franssila, 2010-10-29 This accessible text is now fully revised and updated providing an overview of fabrication technologies and materials needed to realize modern microdevices It demonstrates how common microfabrication principles can be applied in different applications to create devices ranging from nanometer probe tips to meter scale solar cells and a host of microelectronic mechanical optical and fluidic devices in between Latest developments in wafer engineering patterning thin films surface preparation and bonding are covered This second edition includes expanded sections on MEMS and microfluidics related fabrication issues new chapters on polymer and glass microprocessing as well as serial processing techniques 200 completely new and 200 modified figures more coverage of imprinting techniques process integration and economics of microfabrication 300 homework exercises including conceptual thinking assignments order of magnitude estimates standard calculations and device design and process analysis problems solutions to homework problems on the complementary website as well as PDF slides of the figures and tables within the book With clear sections separating basic principles from more advanced material this is a valuable textbook for senior undergraduate and beginning graduate students wanting to understand the fundamentals of microfabrication The book also serves as a handy desk reference for practicing electrical engineers materials scientists chemists and physicists alike www.wiley.com/go/Franssila_Micro2e

Microcontrollers in Practice Ioan Susnea, Marian Mitescu, 2005-07-06 Stressing common characteristics and real applications of the most used microcontrollers this practical guide provides readers with hands on knowledge of how to implement three families of microcontrollers HC11 AVR and 8051 Unlike the rest of the ocean of literature on individual chips Microcontrollers in Practice supplies side by side comparisons and an overview that treats the systems as resources available for implementation Packed with hundreds of practical examples and exercises to foster mastery of concepts and details the guide also includes several extended projects By treating the less expensive 8 bit and RISC microcontrollers this information dense manual equips students and home experimenters with the know how to put these devices into operation

Integrated CMOS Circuits for Optical Communications Mark Ingels, Michiel Steyaert, 2013-03-09 This work investigates the feasibility of the integration of interface circuits for optical communication systems in a standard unmodified digital CMOS process This paves the way for single chip communication systems where the optical interfaces are integrated on the same die as the required digital circuitry The optical receiver is a key element in the optical communication link In this work a transimpedance amplifier which consists of a voltage amplifier with resistive feedback is used as the first stage Unlike for many other circuits the optimal place of its dominant pole is the input node It is also demonstrated that a high gain of the voltage amplifier is primordial to obtain good performances and that this may be obtained through the use of multiple stages Noise aspects are investigated and the conclusion is drawn that the amplifier's input capacitance can be smaller than the photodiode's capacitance for optimal performance

Smart Power ICs Bruno Murari, 2002-06-13 This book provides a survey of the state of the art of technology and future trends in the new family of Smart Power ICs and describes design and applications in a variety of fields ranging from automotive to telecommunications reliability evaluation and qualification procedures The book is a valuable source of information and reference for both power IC design specialists and to all those concerned with applications the development of digital circuits and with system architecture

Copper Interconnect Technology Tapan Gupta, 2010-01-22 Since overall circuit performance has depended primarily on transistor properties previous efforts to enhance circuit and system speed were focused on transistors as well During the last decade however the parasitic resistance capacitance and inductance associated with interconnections began to influence circuit performance and will be the primary factors in the evolution of nanoscale ULSI technology Because metallic conductivity and resistance to electromigration of bulk copper Cu are better than aluminum use of copper and low k materials is now prevalent in the international microelectronics industry As the feature size of the Cu lines forming interconnects is scaled resistivity of the lines increases At the same time electromigration and stress induced voids due to increased current density become significant reliability issues Although copper low k technology has become fairly mature there is no single book available on the promise and challenges of these next generation technologies In this book a leader in the field describes advanced laser systems with lower radiation wavelengths photolithography materials and mathematical modeling approaches to address the challenges of Cu interconnect technology

Silicon Optoelectronic Integrated Circuits Horst Zimmermann, 2013-03-09 Explains the circuit design of silicon optoelectronic integrated circuits OEICs which are central to advances in wireless and wired telecommunications The essential features of optical absorption are summarized as is the device physics of photodetectors and their integration in modern bipolar CMOS and BiCMOS technologies This information provides the basis for understanding the underlying mechanisms of the OEICs described in the main part of the book In order to cover the topic comprehensively Silicon Optoelectronic Integrated Circuits presents detailed descriptions of many OEICs for a wide variety of applications from various optical sensors smart sensors 3D cameras and optical storage systems DVD to fiber receivers in

deep sub m CMOS Numerous detailed illustrations help to elucidate the material

Detection and Signal Processing Wilhelmus Jacobus Witteman, 2007-04-14 This comprehensive monograph deals with detectors signal processors and related noise phenomena Detailed quantitative analyses are developed in a consistent format for thermal detectors vacuum detectors semiconductor detectors and avalanche detectors as well as their accompanying noise currents For signal processing applications the monograph treats in detail the operational amplifier signal averagers waveform analyzers correlation techniques and heterodyne detection Several original extensions are reported especially for correlation devices and heterodyne detection with noise rejection In addition results of analyses are illustrated with examples of operating systems and of applications in space communication and laser radar

MEMS Materials and Processes Handbook Reza Ghodssi, Pinyen Lin, 2011-03-18 MEMS Materials and Processes Handbook is a comprehensive reference for researchers searching for new materials properties of known materials or specific processes available for MEMS fabrication The content is separated into distinct sections on Materials and Processes The extensive Material Selection Guide and a Material Database guides the reader through the selection of appropriate materials for the required task at hand The Processes section of the book is organized as a catalog of various microfabrication processes each with a brief introduction to the technology as well as examples of common uses in MEMS

Power Management of Digital Circuits in Deep Sub-Micron CMOS Technologies Stephan Henzler, 2006-11-24 In the deep sub micron regime the power consumption has become one of the most important issues for competitive design of digital circuits Due to dramatically increasing leakage currents the power consumption does not take advantage of technology scaling as before State of art power reduction techniques like the use of multiple supply and threshold voltages transistor stack forcing and power gating are discussed with respect to implementation and power saving capability Focus is given especially on technology dependencies process variations and technology scaling Design and implementation issues are discussed with respect to the trade off between power reduction performance degradation and system level constraints A complete top down design flow is demonstrated for power gating techniques introducing new design methodologies for the switch sizing task and circuit blocks for data retention and block activation The leakage reduction ratio and the minimum power down time are introduced as figures of merit to describe the power gating technique on system level and give a relation to physical circuit parameters

Power Management of Digital Circuits in Deep Sub Micron CMOS Technologies mainly deals with circuit design but also addresses the interface between circuit and system level design on the one side and between circuit and physical design on the other side

Metal-Polymer Systems Jörg Florian Friedrich, 2017-09-13 The result of decades of research by a pioneer in the field this is the first book to deal exclusively with achieving high performance metal polymer composites by chemical bonding Covering both the academic and practical aspects the author focuses on the chemistry of interfaces between metals and polymers with a particular emphasis on the chemical bonding between the different materials He elucidates the various approaches to

obtaining a stable interface including but not limited to thermodynamically driven redox reactions bond protection to prevent hydrolysis the introduction of barrier layers and stabilization by spacer molecules Throughout chemical bonding is promoted as a simple and economically viable alternative to adhesion based on reversible weak physical interaction Consequently the text equips readers with the practical tools necessary for designing high strength metal polymer composites with such

desired properties as resilience flexibility rigidity or degradation resistance **Metal-Dielectric Interfaces in Gigascale Electronics** Ming He, Toh-Ming Lu, 2012-02-02 Metal dielectric interfaces are ubiquitous in modern electronics As advanced gigascale electronic devices continue to shrink the stability of these interfaces is becoming an increasingly important issue that has a profound impact on the operational reliability of these devices In this book the authors present the basic science underlying the thermal and electrical stability of metal dielectric interfaces and its relationship to the operation of advanced interconnect systems in gigascale electronics Interface phenomena including chemical reactions between metals and dielectrics metallic atom diffusion and ion drift are discussed based on fundamental physical and chemical principles Schematic diagrams are provided throughout the book to illustrate interface phenomena and the principles that govern them Metal Dielectric Interfaces in Gigascale Electronics provides a unifying approach to the diverse and sometimes contradictory test results that are reported in the literature on metal dielectric interfaces The goal is to provide readers with a clear account of the relationship between interface science and its applications in interconnect structures The material presented here will also be of interest to those engaged in field effect transistor and memristor device research as well as university researchers and industrial scientists working in the areas of electronic materials processing semiconductor manufacturing memory chips and IC design Maleates—Advances in Research and Application: 2013 Edition , 2013-06-21 Maleates

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Polymers for Electronic Applications J.H. Lai, 2018-01-18 The object of this book is to review and to discuss some important applications of polymers in electronics The first three chapters discuss the current primary applications of polymers in semiconductor device manufacturing polymers as resist materials for integrated circuit fabrication polyimides as electronics packaging materials and polymers as integrated circuits encapsulates

Ordered Porous Solids Valentin Valtchev, Svetlana Mintova, Michael Tsapatsis, 2011-08-11 The developments in the area of ordered nanoporous solids have moved beyond the traditional catalytic and separation uses and given rise to a wide variety of new applications in different branches of chemistry physics material science etc The activity in this area is due to the outstanding properties of nanoporous materials that have attracted the attention of researchers from different communities However recent achievements in a specific field often remain out of the focus of collaborating communities This work summarizes the latest developments and prospects in the area of ordered porous solids including synthetic layered materials clays microporous zeolite type materials ordered mesoporous solids metal organic framework compounds MOFs carbon etc All aspects from synthesis via comprehensive characterization to the advanced applications of ordered porous materials are presented The chapters are written by leading experts in their respective fields with an emphasis on recent progress and the state of the art Summarizes the latest developments in the field of ordered nanoporous solids Presents state of the art coverage of applications related to porous solids Incorporates 28 contributions from experts across the disciplines

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