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SIXTH EDITION

# MICROELECTRONIC Circuit Design

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Graw  
Hill

Richard C. Jaeger | Travis N. Blalock | Benjamin J. Blalock

# Modern Microelectronic Circuit Desi 2vol

**ML Yell**



## **Modern Microelectronic Circuit Desi 2vol:**

*Introduction To Modern Planar Transmission Lines* Anand K. Verma, 2021-06-02 Provides a comprehensive discussion of planar transmission lines and their applications focusing on physical understanding analytical approach and circuit models Planar transmission lines form the core of the modern high frequency communication computer and other related technology This advanced text gives a complete overview of the technology and acts as a comprehensive tool for radio frequency RF engineers that reflects a linear discussion of the subject from fundamentals to more complex arguments Introduction to Modern Planar Transmission Lines Physical Analytical and Circuit Models Approach begins with a discussion of waves on transmission lines and waves in material medium including a large number of illustrative examples from published results After explaining the electrical properties of dielectric media the book moves on to the details of various transmission lines including waveguide microstrip line co planar waveguide strip line slot line and coupled transmission lines A number of special and advanced topics are discussed in later chapters such as fabrication of planar transmission lines static variational methods for planar transmission lines multilayer planar transmission lines spectral domain analysis resonators periodic lines and surfaces and metamaterial realization and circuit models Emphasizes modeling using physical concepts circuit models closed form expressions and full derivation of a large number of expressions Explains advanced mathematical treatment such as the variation method conformal mapping method and SDA Connects each section of the text with forward and backward cross referencing to aid in personalized self study Introduction to Modern Planar Transmission Lines is an ideal book for senior undergraduate and graduate students of the subject It will also appeal to new researchers with the inter disciplinary background as well as to engineers and professionals in industries utilizing RF microwave technologies *EEE*, 1966-07

**Scientific and Technical Books and Serials in Print**, 1989 American Book Publishing Record, 1996-05 **IEEE Circuits & Devices**, 2000 Инновационные технологии и оборудование субмикронной электроники Коллектив авторов, 2021-03-24 **Books in Print Supplement**, 2002 **Scientific and Technical Aerospace Reports**, 1983

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*Subject Index of Modern Books Acquired* British Library, 1961 *Subject Guide to Books in Print*, 1983 **The Vocational-technical Library Collection** Bruce Reinhart, 1970 The New Encyclopaedia Britannica: Macropaedia : Knowledge in depth, 2003 **Optoelectronics Guidebook** Robert W. Fox, 1977 **Homeowner's Guide to Solar Heating & Cooling** William Murchison Foster, 1976 The New Encyclopædia Britannica: Macropædia, 1993 This encyclopedia includes a two volume index a 12 volume Micropaedia Ready reference a 17 volume Macropaedia Knowledge in depth and the Propædia Index to Scientific & Technical Proceedings, 1978 Monthly with annual cumulation Published conference literature useful both as current awareness and retrospective tools that allow searching by authors of individual

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**Microelectronic Circuit Design** Richard C. Jaeger, 1997-01-01 The primary goal of this book is to develop a comprehensive understanding of the basic techniques of modern electronic circuit design analog and digital discrete and integrated With its balanced treatment of digital and analog electronics this book is one of the first written specifically to meet the demands for increased coverage of digital electronics The book is divided into three parts Part I is an introduction to electronics and solid state devices Part II covers digital electronics emphasizing the subject commensurate with its importance in modern circuit design Part III deals with traditional analog circuits and offers an innovative approach that stresses the design tradeoffs between the FET and the BJT

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