

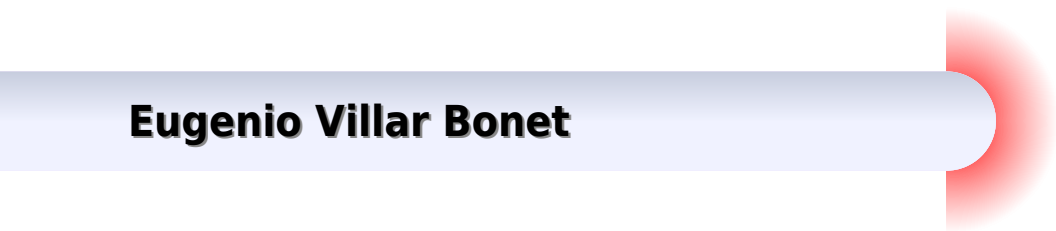
Reuse in Electronic Design

From Information
Modelling to
Intellectual
Properties

Peter Conradi

Reuse In Electronic Design From Information Modelling To Intellectual Properties

Eugenio Villar Bonet



Reuse In Electronic Design From Information Modelling To Intellectual Properties:

Reuse in Electronic Design Peter Conradi, 1999-06-02 Reuse in Electronic Design From Information Modelling to Intellectual Properties Peter Conradi Berlin Germany Reuse of existing IC intellectual property blocks is currently the hot topic in electronic design automation By cutting development time and improving designer productivity reuse offers a faster time to market and consequently increased profitability Conradi encompasses the fundamentals of physical system modelling design methodologies and basic design architectures plus reuse strategies and tasks Features include Graph oriented visualisations enabling the reader to understand the requirements of future interdisciplinary design tools Guidance on information modelling languages system classification and decomposition of systems under development Section on reuse strategies and tasks examining the practical aspects of the technique for both analogue and digital design Data management and retrieval methods including the algebra of the selection process Comprehensive appendices covering fine grained design representations VLSI design standards and data model extraction of a VHDL multiplier design This timely exposition of design capture and manipulation techniques will be a boon for postgraduate electronics students and researchers CAD engineers and electronic designers will profit from the industry examples and the practical applications of the reuse cycle

Understanding and Profiting from Intellectual Property in International Business Deli Yang, 2021-04-07 This book covers cross border strategies to understand and profit from intellectual property It starts with a basic overview of IP before focusing specifically on international business contexts The book then explores factors that affect IP related business activities in different countries Next follows a discussion of the importance of managing IP valuation people and products which leads into an examination of strategies for obtaining value from IP related activities including licensing This edition updates the contents and adds new contemporary cases such as internet based crimes and trademarked sport brands Readers will gain an understanding of the significance of IP to corporate success in the increasingly globalized world With updated knowledge on deriving value from IP this book will provide insights for practitioners to deal with cross border issues of IP and for scholars across disciplines to advance studies of cross border issues and conflicts in IP

Understanding and Profiting from Intellectual Property D. Yang, 2012-12-04 A new look at the strategic and managerial issues surrounding intellectual property IP and international commercialization in the international market An updated version which provides practitioners and analysts with guidelines and an action framework on how to benefit from IP

System Level Design Model with Reuse of System IP Patrizia Cavalloro, Christophe Gendarme, Klaus Kronlöf, Jean Mermet, J. van Sas, Kari Tiensyrjä, Nikolaos Voros, 2007-05-08 This book addresses system design providing a framework for assessing and developing system design practices that observe and utilise reuse of system design know how The know how accumulated in the companies represents an intellectual asset or property IP

Neural and Fuzzy Logic Control of Drives and Power Systems Marcian Cirstea, 2002-10-08 Introduces cutting edge control systems to a wide readership of engineers and

students The first book on neuro fuzzy control systems to take a practical applications based approach backed up with worked examples and case studies Learn to use VHDL in real world applications Introducing cutting edge control systems through real world applications Neural networks and fuzzy logic based systems offer a modern control solution to AC machines used in variable speed drives enabling industry to save costs and increase efficiency by replacing expensive and high maintenance DC motor systems The use of fast micros has revolutionised the field with sensorless vector control and direct torque control This book reflects recent research findings and acts as a useful guide to the new generation of control systems for a wide readership of advanced undergraduate and graduate students as well as practising engineers The authors guide readers quickly and concisely through the complex topics of neural networks fuzzy logic mathematical modelling of electrical machines power systems control and VHDL design Unlike the academic monographs that have previously been published on each of these subjects this book combines them and is based round case studies of systems analysis control strategies design simulation and implementation The result is a guide to applied control systems design that will appeal equally to students and professional design engineers The book can also be used as a unique VHDL design aid based on real world power engineering applications Intellectual Property for Electronic Systems Brian Bailey, Kathy Werner, 2007 Featuring articles by top experts from such companies as Rambus IBM Hewlett Packard and FreeScale this collection addresses the issues that concern those in the ICT field looking to keep systems safe and secure without sacrificing quality or ease of use This book cogently addresses verification standards handoff and legal issues to create a comprehensive look at one of the most important yet sometimes under appreciated topics in the industry *Intellectual Property Protection in VLSI Designs* Gang Qu, Miodrag Potkonjak, 2007-05-08 Intellectual Property Protection in VLSI Designs Theory and Practice provides an overview of the security problems in modern VLSI design with a detailed treatment of our newly developed constraint based protection paradigm for the protection of VLSI design IPs from FPGA design to standard cell placement from high level synthesis solutions to gate level netlist place and rout and from advanced CAD tools to physical design algorithms The problem of VLSI design IP protection is much more challenging than the protection of multimedia contents or software and our protection paradigm is also conceptually different from the state of the art approaches in those domains Intellectual Property Protection in VLSI Designs Theory and Practice contains the mathematical foundations for the developed IP protection paradigm detailed pseudo code and descriptions of its many techniques numerous examples and experimental validation on well known benchmarks and clear explanations and comparisons of the many protection methods *System-on-Chip Methodologies & Design Languages* Peter J. Ashenden, Jean Mermet, Ralf Seepold, 2013-03-14 System on Chip Methodologies the Forum on Design Languages FDL held in Europe and the Asia Pacific Chip Design Language APChDL Conference The papers cover a range of topics including design methods specification and modeling languages tool issues formal verification simulation and synthesis The results presented in these papers will help researchers and practicing

engineers keep abreast of developments in this rapidly evolving field **2nd Workshop on Libraries, Component Modeling and Quality Assurance** Eugenio Villar Bonet, 1997 Compendio de los trabajos presentados en Toledo durante el 2nd Workshop on Libraries component modeling and quality assurance Reuse Techniques for VLSI Design Ralf Seepold, Arno Kunzmann, 2012-12-06 Reuse Techniques for VLSI Design is a reflection on the current state of the art in design reuse for microelectronic systems To that end it is the first book to garner the input of leading experts from both research and application areas These experts document herein not only their more mature approaches but also their latest research results Firstly it sets out the background and support from international organisations that enforce System on a Chip SoC design by reuse oriented methodologies This overview is followed by a number of technical presentations covering different requirements of the reuse domain These are presented from different points of view i e IP provider IP user designer isolated reuse intra company or inter company reuse More general systems or case studies e g metrics are followed by comprehensive reuse systems e g reuse management systems partly including business models Since design reuse must not be restricted to digital components mixed signal and analog reuse approaches are also presented In parallel to the digital domain this area covers research in reuse database design Design verification and legal aspects are two important topics that are closely related to the realization of design reuse These hot topics are covered by presentations that finalize the survey of outstanding research development and application of design reuse for SoC design Reuse Techniques for VLSI Design is an invaluable reference for researchers and engineers involved in VLSI ASIC design

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