

# LES PROCESSUS DE RECOMBINAISON RADIATIVE DANS LES SEMI-CONDUCTEURS

C. Benoit a La Guillaume

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Transistors and Associated Semiconductor Devices ,1959

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Proceedings of the International Conference on Semiconductor Physics, Prague, 1960 ,1961 Luminescence of Inorganic Solids Paul G. Goldberg,1966 International Symposium on Dislocations in Tetrahedrally Coordinated Semiconductors, Hünfeld/Fulda (FRG) September 25-29, 1978 ,1979 University of California Union Catalog of Monographs Cataloged by the Nine Campuses from 1963 Through 1967: Authors & titles University of California (System). Institute of Library Research, University of California, Berkeley, 1972 The National union catalog, 1968-1972 ,1973

The Publishers Weekly ,1965 The National Union Catalogs, 1963-, 1964 Yearbook of International Congress *Proceedings*, 1970 Bibliography of reports arising out of meetings held by international organizations the Books .1966 Integrated Circuit Design for Radiation Environments Stephen J. Gaul, Nicolaas van Vonno, Steven H. Voldman, Wesley H. Morris, 2019-12-31 A practical guide to the effects of radiation on semiconductor components of electronic systems and techniques for the designing laying out and testing of hardened integrated circuits This book teaches the fundamentals of radiation environments and their effects on electronic components as well as how to design lay out and test cost effective hardened semiconductor chips not only for today s space systems but for commercial terrestrial applications as well It provides a historical perspective the fundamental science of radiation and the basics of semiconductors as well as radiation induced failure mechanisms in semiconductor chips Integrated Circuits Design for Radiation Environments starts by introducing readers to semiconductors and radiation environments including space atmospheric and terrestrial environments followed by circuit design and layout The book introduces radiation effects phenomena including single event effects total ionizing dose damage and displacement damage and shows how technological solutions can address both phenomena Describes the fundamentals of radiation environments and their effects on electronic components Teaches readers how to design lay out and test cost effective hardened semiconductor chips for space systems and commercial terrestrial applications Covers natural and man made radiation environments space systems and commercial terrestrial applications Provides up to date coverage of state of the art of radiation hardening technology in one concise volume Includes questions and answers for the reader to test their knowledge Integrated Circuits Design for Radiation Environments will appeal to researchers and product developers in the semiconductor space and defense industries as well as electronic engineers in the medical field The book is also helpful for system layout process device reliability applications ESD latchup and circuit design semiconductor engineers along with anyone involved in micro electronics used in harsh

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