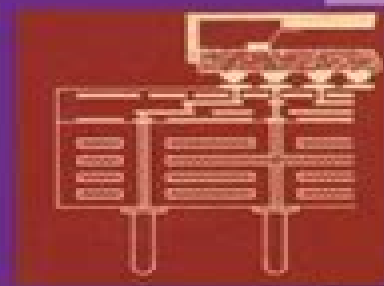


# MICROELECTRONICS PACKAGING HANDBOOK

*Technology Drivers*



PART I



**Second Edition**

Edited by  
Rao R. Tummala  
Eugene J. Rymaszewski  
Alan G. Klopfenstein

# Microelectronics Packaging Handbook

**R. R. Tummala, E.J. Rymaszewski**



## **Microelectronics Packaging Handbook:**

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RALPH E GOMORY IBM SENIOR VICE PRESIDENT SCIENCE AND TECHNOLOGY Far from being passive containers for microelectronic devices the so called packages in today s advanced computers pose at least as many engineering challenges as the chips that they interconnect power and cool New pack aging concepts often lead their developers into uncharted areas of science Ultimately progress in packaging is likely to set the limits on how far computers can evolve And yet this technology has never captured the public imagination in the way that chips have Worse it has been largely overlooked by universities as a prime subject for teaching and research In part this academic oversight stems from packaging s multidisciplinary nature Packaging involves the solution of electrical mechanical and thermal problems it requires understanding at the molecular level not only of silicon but of metals ceramics polymers glass and composites In short iii iv FOREWORD it is poorly matched to the traditional separation of university disciplines Another roadblock has been the lack of a comprehensive book dealing adequately with state of the art packaging design and modern industry requirements      *Microelectronics Packaging Handbook, 3-part set* Rao Tummala, Eugene J. Rymaszewski, Alan G. Klopfenstein, 1997-01-31 This thoroughly revised and updated three volume set continues to be the standard reference in the field providing the latest in microelectronics design methods modeling tools simulation techniques and manufacturing procedures Unlike reference books that focus only on a few aspects of microelectronics packaging these outstanding volumes discuss state of the art packages that meet the power cooling protection and interconnection requirements of increasingly dense and fast microcircuitry Providing an excellent balance of theory and practical applications this dynamic compilation features step by step examples and vital technical data simplifying each phase of package design and production In addition the volumes contain over 2000 references 900 figures and 250 tables Part I Technology Drivers covers the driving force of microelectronics packaging electrical thermal and reliability It introduces the technology developer to aspects of manufacturing that must be considered during product development Part II Semiconductor Packaging discusses the interconnection of the IC chip to the first level of packaging and all first level packages Electrical test sealing and encapsulation technologies are also covered in detail Part III Subsystem Packaging explores board level packaging as well as connectors cables and optical packaging      *Microelectronics Packaging Handbook* Rao R. Tummala, Eugene J. Rymaszewski, 1997      ***Microelectronics Packaging Handbook*** R.R. Tummala, Eugene J. Rymaszewski, Alan G. Klopfenstein, 1997-01-31 Electronics has become the largest industry surpassing agriculture auto and heavy metal industries It has become the industry of choice for a country to prosper already having given rise to the phenomenal prosperity of Japan Korea Singapore Hong Kong and Ireland among others At the current growth rate total worldwide semiconductor sales will reach 300B by the year 2000 The key electronic technologies responsible for the growth of the industry include

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Packaging is the only book to cover the field from wafer to systems including every major contributing technology This rigorous and thorough introduction to electronic packaging technologies gives you a solid grounding in microelectronics photonics RF packaging design assembly reliability testing and manufacturing and its relevance to both semiconductors and systems You ll find Full coverage of electrical mechanical chemical and materials aspects of each technology Easy to read schematics and block diagrams Fundamental approaches to all system issues Examples of all common configurations and technologies wafer level packaging single chip multichip RF opto electronic microvia boards thermal and others Details on chip to board connections sealing and encapsulation and manufacturing processes Basics of electrical and reliability testing

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In a fast-paced digital era where connections and knowledge intertwine, the enigmatic realm of language reveals its inherent magic. Its capacity to stir emotions, ignite contemplation, and catalyze profound transformations is nothing short of extraordinary. Within the captivating pages of **Microelectronics Packaging Handbook** a literary masterpiece penned by way of a renowned author, readers attempt a transformative journey, unlocking the secrets and untapped potential embedded within each word. In this evaluation, we shall explore the book's core themes, assess its distinct writing style, and delve into its lasting effect on the hearts and minds of those who partake in its reading experience.

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