



Pulse And Logic Circuits

Richard C. Dorf



Pulse And Logic Circuits:

Pulse and Logic Circuits Angelo Christopher Gilli, 1968 Pulse and Logic Circuits Richard L. Castellucis, 1976

Pulse and Digital Circuits: Venkata Rao K, Rama Sudha K, Manmadha Rao G, 2010 Pulse and Digital Circuits caters to the needs of undergraduate students of electronics and communication engineering. It covers key topics in the area of pulse and digital circuits. It is an introductory text on the basic concepts involved in the **Digital Logic Circuits** Dr. P. Kannan, Mrs. M. Saraswathi, Mr. C. Rameshkumar, PREFACE OF THE BOOK This book is extensively designed for the third semester EEE EIE students as per Anna university syllabus R 2013. The following chapters constitute the following units: Chapter 1 9 covers Unit 1; Chapter 2 and 3 covers Unit 2; Chapter 4 and 5 covers Unit 3; Chapter 6 and 7 covers Unit 4; Chapter 8 VHDL Unit 5. CHAPTER 1 Introduces the Number System, binary arithmetic and codes. CHAPTER 2 Deals with Boolean algebra, simplification using Boolean theorems, K map method, Quine McCluskey method, logic gates, implementation of switching function using basic Logical Gates and Universal Gates. CHAPTER 3 Describes the combinational circuits like Adder, Subtractor, Multiplier, Divider, magnitude comparator, encoder, decoder, code converters, Multiplexer and Demultiplexer. CHAPTER 4 Describes with Latches, Flip Flops, Registers and Counters. CHAPTER 5 Concentrates on the Analysis as well as design of synchronous sequential circuits, Design of synchronous counters, sequence generator and Sequence detector. CHAPTER 6 Concentrates the Design as well as Analysis of Fundamental Mode circuits, Pulse mode Circuits, Hazard Free Circuits, ASM Chart and Design of Asynchronous counters. CHAPTER 7 Discussion on memory devices which includes ROM, RAM, PLA, PAL, Sequential logic devices and ASIC. CHAPTER 8 The chapter concentrates on the design, fundamental building blocks, Data types, operates, subprograms, packages, compilation process used for VHDL. It discusses on Finite state machine as an important tool for designing logic level state machines. The chapter also discusses register transfer level designing and test benches usage in stimulation of the state logic machines. CHAPTER 9 Concentrate on the comparison, operation and characteristics of RTL, DTL, TTL, ECL and MOS families. We have taken enough care to present the definitions and statements of basic laws and theorems, problems with simple steps to make the students familiar with the fundamentals of Digital Design.

The Electrical Engineering Handbook, Second Edition Richard C. Dorf, 1997-09-26. In 1993 the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work. Now this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today. Every electrical engineer should have an opportunity to expand his expertise with this definitive guide. In a single volume this handbook provides a complete reference to answer the questions encountered by practicing engineers in industry, government or academia. This well organized book is divided into 12 major sections that encompass the entire field of electrical engineering including circuits, signal processing, electronics, electromagnetics, electrical effects and devices, and energy, and the emerging trends in the fields of communications, digital devices, computer engineering systems.

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Design and Performance of Fluoric Analog-to-pulse-frequency Converter Miles O. Dustin, 1968 Navy Electricity and Electronics Training Series Richard L. Baker, 1987 Electronics Installation and Maintenance Book, Electronics Circuits United States. Naval Ship Systems Command, Official Gazette of the United States Patent and Trademark Office, 2002 **Official Gazette of the United States Patent Office** United States. Patent Office, 1966 Ocean Systems Technician 3 & 2 (maintainer). Roy M. Derrick, 1985 *GCSE Computer Studies for You* Stephen Doyle, 1990 This second edition of a GCSE computer studies text includes chapters on personal computers and desktop publishing spreadsheets and their applications and detailed case studies illustrating how a computer system can revolutionize the working environment The Data Protection Act is also included together with project work an extended section on coursework advice on how to revise and hints on how to pass examinations Key words are explained in the text in context and highlighted with bold type and also explained in an extensive glossary

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