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Proceedings Of The Ieee 1998 Custom Integrated Circuits Conference

Paul Leroux, Michiel Steyaert



Proceedings Of The Ieee 1998 Custom Integrated Circuits Conference:

Proceedings of the IEEE 1998 Custom Integrated Circuits Conference, 1998 **LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers** Paul Leroux, Michiel Steyaert, 2006-03-30 LNA ESD Co Design for Fully Integrated CMOS Wireless Receivers fits in the quest for complete CMOS integration of wireless receiver front ends With a combined discussion of both RF and ESD performance it tackles one of the final obstacles on the road to CMOS integration The book is conceived as a design guide for those actively involved in the design of CMOS wireless receivers The book starts with a comprehensive introduction to the performance requirements of low noise amplifiers in wireless receivers Several popular topologies are explained and compared with respect to future technology and frequency scaling The ESD requirements are introduced and related to the state of the art protection devices and circuits LNA ESD Co Design for Fully Integrated CMOS Wireless Receivers provides an extensive theoretical treatment of the performance of CMOS low noise amplifiers in the presence of ESD protection circuitry The influence of the ESD protection parasitics on noise figure gain linearity and matching are investigated Several RF ESD co design solutions are discussed allowing both high RF performance and good ESD immunity for frequencies up to and beyond 5 GHz Special attention is also paid to the layout of both active and passive components LNA ESD Co Design for Fully Integrated CMOS Wireless Receivers offers the reader intuitive insight in the LNA's behavior as well as the necessary mathematical background to optimize its performance All material is experimentally verified with several CMOS implementations among which a fully integrated GPS receiver front end The book is essential reading for RF design engineers and researchers in the field and is also suitable as a text book for an advanced course on the subject

Digital Design and Fabrication Vojin G. Oklobdzija, 2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user's point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design

Circuits and Applications Using Silicon Heterostructure Devices John D. Cressler, 2018-10-03 No matter how you slice it semiconductor devices power the communications revolution Skeptical

Imagine for a moment that you could flip a switch and instantly remove all the integrated circuits from planet Earth. A moment's reflection would convince you that there is not a single field of human endeavor that would not come to a grinding halt: be it commerce, agriculture, education, medicine, or entertainment. Life as we have come to expect it would simply cease to exist. Drawn from the comprehensive and well-reviewed *Silicon Heterostructure Handbook*, this volume covers SiGe circuit applications in the real world. Edited by John D. Cressler with contributions from leading experts in the field, this book presents a broad overview of the merits of SiGe for emerging communications systems. Coverage spans new techniques for improved LNA design, RF to millimeter wave IC design, SiGe MMICs, SiGe Millimeter Wave ICs, and wireless building blocks using SiGe HBTs. The book provides a glimpse into the future as envisioned by industry leaders. ***RF-Frontend Design for Process-Variation-Tolerant Receivers*** Pooyan Sakian, Reza Mahmoudi, Arthur van Roermund, 2012-02-18. This book discusses wireless receiver design challenges given the shrinking of circuitry into ever smaller sizes and resulting complications on manufacturability, production yield, and end price of the products. Includes countermeasures for RF designers. ***Analog and VLSI Circuits*** Wai-Kai Chen, 2018-10-08. Featuring hundreds of illustrations and references, this volume in the third edition of the *Circuits and Filters Handbook* provides the latest information on analog and VLSI circuits, omitting extensive theory and proofs in favor of numerous examples throughout each chapter. The first part of the text focuses on analog integrated circuits, presenting up-to-date knowledge on monolithic device models, analog circuit cells, high performance analog circuits, RF communication circuits, and PLL circuits. In the second half of the book, well-known contributors offer the latest findings on VLSI circuits, including digital systems, data converters, and systolic arrays. ***EDA for IC Implementation, Circuit Design, and Process Technology*** Luciano Lavagno, Louis Scheffer, Grant Martin, 2018-10-03. Presenting a comprehensive overview of the design automation algorithms, tools, and methodologies used to design integrated circuits, the *Electronic Design Automation for Integrated Circuits Handbook* is available in two volumes. The second volume, *EDA for IC Implementation: Circuit Design and Process Technology*, thoroughly examines real-time logic to GDSII, a file format used to transfer data of semiconductor physical layout, analog mixed signal design, physical verification, and technology CAD/TCAD. Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale, power supply network design and analysis, design modeling, and much more. Save on the complete set. ***Analog Circuit Design*** Michiel Steyaert, Arthur van Roermund, Andrea Baschiroto, 2011-09-15. *Analog Circuit Design* contains the contribution of 18 tutorials of the 20th workshop on Advances in Analog Circuit Design. Each part discusses a specific, to-date topic on new and valuable design ideas in the area of analog circuit design. Each part is presented by six experts in that field, and state-of-the-art information is shared and overviewed. This book is number 20 in this successful series of *Analog Circuit Design*, providing valuable information and excellent overviews of: Topic 1: Low Voltage Low Power, chairman: Andrea Baschiroto; Topic 2: Short Range Wireless Front Ends, chairman: Arthur van Roermund; Topic 3: Power Management and DC/DC, chairman: Michiel.

Steyaert Analog Circuit Design is an essential reference source for analog circuit designers and researchers wishing to keep abreast with the latest development in the field The tutorial coverage also makes it suitable for use in an advanced design course

High-Frequency Magnetic Components Marian K. Kazimierczuk, 2011-08-24 If you are looking for a complete study of the fundamental concepts in magnetic theory read this book No other textbook covers magnetic components of inductors and transformers for high frequency applications in detail This unique text examines design techniques of the major types of inductors and transformers used for a wide variety of high frequency applications including switching mode power supplies SMPS and resonant circuits It describes skin effect and proximity effect in detail to provide you with a sound understanding of high frequency phenomena As well as this you will discover thorough coverage on integrated inductors and the self capacitance of inductors and transformers with expressions for self capacitances in magnetic components criteria for selecting the core material as well as core shape and size and an evaluation of soft ferromagnetic materials used for magnetic cores winding resistance at high frequencies expressions for winding and core power losses when non sinusoidal inductor or transformer current waveforms contain harmonics Case studies practical design examples and procedures using the area product method and the geometry coefficient method are expertly combined with concept orientated explanations and student friendly analysis Supplied at the end of each chapter are summaries of the key concepts review questions and problems the answers to which are available in a separate solutions manual Such features make this a fantastic textbook for graduates senior level undergraduates and professors in the area of power electronics in addition to electrical and computer engineering This is also an inimitable reference guide for design engineers of power electronics circuits high frequency transformers and inductors in areas such as SMPS and RF power amplifiers and circuits

Multi-Threshold CMOS Digital Circuits Mohab Anis, Mohamed Elmasry, 2012-12-06 Multi Threshold CMOS Digital Circuits Managing Leakage Power discusses the Multi threshold voltage CMOS MTCMOS technology that has emerged as an increasingly popular technique to control the escalating leakage power while maintaining high performance The book addresses the leakage problem in a number of designs for combinational sequential dynamic and current steering logic Moreover computer aided design methodologies for designing low leakage integrated circuits are presented The book give an excellent survey of state of the art techniques presented in the literature as well as proposed designs that minimize leakage power while achieving high performance Multi Threshold CMOS Digital Circuits Managing Leakage Power is written for students of VLSI design as well as practicing circuit designers system designers CAD tool developers and researchers It assumes a basic knowledge of digital circuit design and device operation and covers a broad range of circuit design techniques

Adaptable Embedded Systems Antonio Carlos Schneider Beck, Carlos Arthur Lang Lisbôa, Luigi Carro, 2012-11-27 As embedded systems become more complex designers face a number of challenges at different levels they need to boost performance while keeping energy consumption as low as possible they need to reuse existent software code and at the same time they need to take advantage

of the extra logic available in the chip represented by multiple processors working together This book describes several strategies to achieve such different and interrelated goals by the use of adaptability Coverage includes reconfigurable systems dynamic optimization techniques such as binary translation and trace reuse new memory architectures including homogeneous and heterogeneous multiprocessor systems communication issues and NOCs fault tolerance against fabrication defects and soft errors and finally how one can combine several of these techniques together to achieve higher levels of performance and adaptability The discussion also includes how to employ specialized software to improve this new adaptive system and how this new kind of software must be designed and programmed

Fine- and Coarse-Grain Reconfigurable Computing Stamatis Vassiliadis,Dimitrios Soudris,2007-10-12 Fine and Coarse Grain Reconfigurable Computing gives the basic concepts and building blocks for the design of Fine or FPGA and Coarse Grain Reconfigurable Architectures Recently developed integrated architecture design and software supported design flow of FPGA and coarse grain reconfigurable architecture are also described Part I consists of two extensive surveys of FPGA and Coarse Grain Reconfigurable Architectures The FPGA technology is defined which includes architecture logic block structure interconnect and configuration methods and existing fine grain reconfigurable architectures emerged from both academia and industry Additionally the implementation techniques and CAD tools developed to facilitate the implementation of a system in reconfigurable hardware by the industry and academia are provided In addition the features the advantages and limitations of the coarse grain reconfigurable systems the specific issues that should be addressed during the design phase as well as representative existing coarse grain reconfigurable systems are explained In Part II case studies innovative research results about reconfigurable architectures and design frameworks from three projects AMDREL MOLEN and ADRES DRESC and a new classification according to microcoded architectural criteria are described Fine and Coarse Grain Reconfigurable Computing is an essential reference for researchers and professionals and can be used as a textbook by undergraduate graduate students and professors Foreword by Yale Patt Jim Smith and Mateo Valero

Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller

geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

On-Chip Communication Architectures Sudeep Pasricha,Nikil Dutt,2010-07-28 Over the past decade system on chip SoC designs have evolved to address the ever increasing complexity of applications fueled by the era of digital convergence Improvements in process technology have effectively shrunk board level components so they can be integrated on a single chip New on chip communication architectures have been designed to support all inter component communication in a SoC design These communication architecture fabrics have a critical impact on the power consumption performance cost and design cycle time of modern SoC designs As application complexity strains the communication backbone of SoC designs academic and industrial R D efforts and dollars are increasingly focused on communication architecture design On Chip Communication Architectures is a comprehensive reference on concepts research and trends in on chip communication architecture design It will provide readers with a comprehensive survey not available elsewhere of all current standards for on chip communication architectures A definitive guide to on chip communication architectures explaining key concepts surveying research efforts and predicting future trends Detailed analysis of all popular standards for on chip communication architectures Comprehensive survey of all research on communication architectures covering a wide range of topics relevant to this area spanning the past several years and up to date with the most current research efforts Future trends that will have a significant impact on research and design of communication architectures over the next several years

Error Control for Network-on-Chip Links Bo Fu,Paul Ampadu,2011-10-09 This book provides readers with a comprehensive review of the state of the art in error control for Network on Chip NOC links Coverage includes detailed description of key issues in NOC error control faced by circuit and system designers as well as practical error control techniques to minimize the impact of these errors on system performance

Introduction to VLSI Design Flow Sneh Saurabh,2023-06-15 Chip designing is a complex task that requires an in depth understanding of VLSI design flow skills to employ sophisticated design tools and keeping pace with the bleeding edge semiconductor technologies This lucid textbook is focused on fulfilling these requirements for students as well as a refresher for professionals in the industry It helps the user develop a holistic view of the design flow through a well sequenced set of chapters on logic synthesis verification physical design and testing Illustrations and pictorial representations have been used liberally to simplify the explanation Additionally each chapter has a set of activities that can be performed using freely available tools and provide hands on experience with the design tools Review questions and problems are given at the end of each chapter to revise the concepts Recent trends and references are listed at the end of each chapter for further reading

Programmable Digital Signal Processors Yu Hen Hu, 2001-12-06 Presents the latest developments in the programming and design of programmable digital signal processors PDSPs with very long instruction word VLIW architecture algorithm formulation and implementation and modern applications for multimedia processing communications and industrial control

Viruses, Hardware and Software Trojans Anatoly Belous, Vitali Saladukha, 2020-06-27 This book provides readers with a valuable reference on cyber weapons and in particular viruses software and hardware Trojans The authors discuss in detail the most dangerous computer viruses software Trojans and spyware models of computer Trojans affecting computers methods of implementation and mechanisms of their interaction with an attacker a hacker an intruder or an intelligence agent Coverage includes Trojans in electronic equipment such as telecommunication systems computers mobile communication systems cars and even consumer electronics The evolutionary path of development of hardware Trojans from cabinets crates and boxes to the microcircuits IC is also discussed Readers will benefit from the detailed review of the major known types of hardware Trojans in chips principles of their design mechanisms of their functioning methods of their introduction means of camouflaging and detecting as well as methods of protection and counteraction

Low-Power High-Resolution Analog to Digital Converters Amir Zjajo, José Pineda de Gyvez, 2010-10-29 With the fast advancement of CMOS fabrication technology more and more signal processing functions are implemented in the digital domain for a lower cost lower power consumption higher yield and higher re configurability This has recently generated a great demand for low power low voltage A D converters that can be realized in a mainstream deep submicron CMOS technology However the discrepancies between lithography wavelengths and circuit feature sizes are increasing Lower power supply voltages significantly reduce noise margins and increase variations in process device and design parameters Consequently it is steadily more difficult to control the fabrication process precisely enough to maintain uniformity The inherent randomness of materials used in fabrication at nanoscopic scales means that performance will be increasingly variable not only from die to die but also within each individual die Parametric variability will be compounded by degradation in nanoscale integrated circuits resulting in instability of parameters over time eventually leading to the development of faults Process variation cannot be solved by improving manufacturing tolerances variability must be reduced by new device technology or managed by design in order for scaling to continue Similarly within die performance variation also imposes new challenges for test methods In an attempt to address these issues Low Power High Resolution Analog to Digital Converters specifically focus on i improving the power efficiency for the high speed and low spurious spectral A D conversion performance by exploring the potential of low voltage analog design and calibration techniques respectively and ii development of circuit techniques and algorithms to enhance testing and debugging potential to detect errors dynamically to isolate and confine faults and to recover errors continuously The feasibility of the described methods has been verified by measurements from the silicon prototypes fabricated in standard 180nm 90nm and 65nm CMOS technology

Design of High-speed Communication

Circuits Ramesh Harjani, 2006 MOS technology has rapidly become the de facto standard for mixed signal integrated circuit design due to the high levels of integration possible as device geometries shrink to nanometer scales The reduction in feature size means that the number of transistor and clock speeds have increased significantly In fact current day microprocessors contain hundreds of millions of transistors operating at multiple gigahertz Furthermore this reduction in feature size also has a significant impact on mixed signal circuits Due to the higher levels of integration the majority of ASICs possesses some analog components It has now become nearly mandatory to integrate both analog and digital circuits on the same substrate due to cost and power constraints This book presents some of the newer problems and opportunities offered by the small device geometries and the high levels of integration that is now possible The aim of this book is to summarize some of the most critical aspects of high speed analog RF communications circuits Attention is focused on the impact of scaling substrate noise data converters RF and wireless communication circuits and wireline communication circuits including high speed I O

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