

6. Specify the size of data, address, memory word and memory capacity of 8085 microprocessor.

| S.No | Components of 8085 | Size   |
|------|--------------------|--------|
| 1    | Data               | 8-bit  |
| 2    | Address            | 16-bit |
| 3    | Memory word        | 8-bit  |
| 4    | Memory capacity    | 64 KB  |

7. List the five interrupt pins available in 8085

There are 6 interrupt pins in the microprocessor used as Hardware Interrupts given below:

- TRAP
- RST7.5
- RST6.5
- RST5.5
- INTR
- INTA is not an interrupt.

INTA is used by the Microprocessor for sending the acknowledgement. TRAP has highest priority and RST7.5 has second highest priority and so on.

8. What is ALE ?

ALE is a control signal of 8085 MPU. 8085 is a 8-bit microprocessor which have 16-bit address bus among them lower 8-bit are multiplexed. So while MPU executes read or write operation it needs to demultiplexed. ALE control signal used to demultiplexed the lower order address and data bus.

9. To obtain a 320 ns clock, what should be the input clock frequency? What is the frequency of clock signal at CLK OUT ?

Given :  $T = 320 \text{ ns}$ . We know that  $f = 1/T$

$$f = 1/320 \times 10^{-9} = 10^9/320 \\ = 3.125 \text{ MHz}$$

The input clock frequency =  $2 \times 3.125 \text{ MHz} = 6.25 \text{ MHz}$

The frequency of clock signal at CLK OUT = 3.125 MHz

10. What is meant by level triggered interrupt? Which of the interrupts in 8085 are level triggered?

An **interrupt** is a signal to the processor emitted by hardware or software indicating an event that needs immediate attention.

A *level-triggered interrupt* is an interrupt signaled by maintaining the interrupt line at a high or low level. A device wishing to signal a Level-triggered interrupt drives the interrupt request line to its active level (high or low), and then holds it at that level until it is serviced.

TRAP interrupt is edge and level triggered. The RST 6.5 and RST 5.5 both are level triggered. INTR also a level sensitive interrupt.

11. Why data bus is bi-directional?

The microprocessor has to fetch (read) the data from memory or input device for processing and after processing, it has to store (write) the data to memory or output device. Hence the data bus is bi-directional.

12. Distinguish between I/O mapped I/O and memory mapped I/O .

# Questions And Answers On Microprocessors Newnes Microcomputers

**RD Boyd**



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