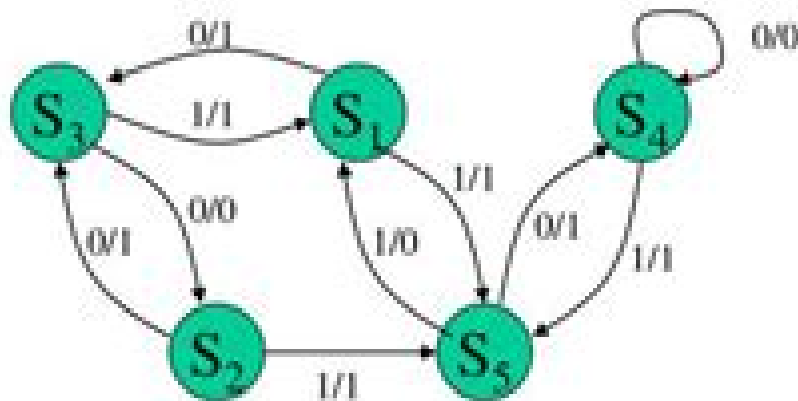


Synthesis of sequential circuits

- Steps in synthesis of a sequential circuit
 - Specify the FSM (state table or state diagram)
 - Minimize the states
 - State assignment/encoding
- Specification:



PS	x	NS	z
S_1	0	S_3	1
S_1	1	S_5	1
S_2	0	S_3	1
S_2	1	S_5	1
S_3	0	S_1	0
S_3	1	S_5	1
S_4	0	S_4	0
S_4	1	S_1	1
S_5	0	S_3	1
S_5	1	S_1	0

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B. Mikolajczak



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Sequential-circuit Synthesis: State Assignment Aspects Massachusetts Institute of Technology, *Sequential-circuit Synthesis* Donald R. Haring,1966 **Sequential-circuit Synthesis** Donald R. Haring,1966 **Spectral Logic and Its Applications for the Design of Digital Devices** Mark G. Karpovsky,Radomir S. Stankovic,Jaakko T. Astola,2008-04-15

Spectral techniques facilitate the design and testing of today s increasingly complex digital devices There is heightened interest in spectral techniques for the design of digital devices dictated by ever increasing demands on technology that often cannot be met by classical approaches Spectral methods provide a uniform and consistent theoretic environment for recent achievements in this area which appear divergent in many other approaches Spectral Logic and Its Applications for the Design of Digital Devices gives readers a foundation for further exploration of abstract harmonic analysis over finite groups in the analysis design and testing of digital devices After an introduction this book provides the essential mathematical background for discussing spectral methods It then delves into spectral logic and its applications covering Walsh Haar arithmetic transform Reed Muller transform for binary valued functions and Vilenkin Chrestenson transform generalized Haar and other related transforms for multiple valued functions Polynomial expressions and decision diagram representations for switching and multiple value functions Spectral analysis of Boolean functions Spectral synthesis and optimization of combinational and sequential devices Spectral methods in analysis and synthesis of reliable devices Spectral techniques for testing computer hardware This is the authoritative reference for computer science and engineering professionals and researchers with an interest in spectral methods of representing discrete functions and related applications in the design and testing of digital devices It is also an excellent text for graduate students in courses covering spectral logic and its applications *Network and Switching Theory* Giuseppe Biorci,2014-04-11 *Network and Switching Theory*

Algebraic and Structural Automata Theory B. Mikolajczak,1991-01-14 Automata Theory is part of computability theory which covers problems in computer systems software activity of nervous systems neural networks and processes of live organisms development The result of over ten years of research this book presents work in the following areas of Automata Theory automata morphisms time varying automata automata realizations and relationships between automata and semigroups Aimed at those working in discrete mathematics and computer science parts of the book are suitable for use in graduate courses in computer science electronics telecommunications and control engineering It is assumed that the reader is familiar with the basic concepts of algebra and graph theory **Design Methods for Digital Systems** J.

Chinal,2022-02-07 No detailed description available for Design Methods for Digital Systems *International Aerospace Abstracts* ,1967 **Introduction to the Methodology of Switching Circuits** George J. Klir,1972 **Logic Synthesis for Field-Programmable Gate Arrays** Rajeev Murgai,Robert K. Brayton,Alberto L. Sangiovanni-Vincentelli,2012-12-06 Short turnaround has become critical in the design of electronic systems Software programmable components such as

microprocessors and digital signal processors have been used extensively in such systems since they allow rapid design revisions. However, the inherent performance limitations of software programmable systems mean that they are inadequate for high performance designs. Designers thus turned to gate arrays as a solution. User programmable gate arrays, field programmable gate arrays (FPGAs) have recently emerged and are changing the way electronic systems are designed and implemented. The growing complexity of the logic circuits that can be packed onto an FPGA chip means that it has become important to have automatic synthesis tools that implement logic functions on these architectures. *Logic Synthesis for Field Programmable Gate Arrays* describes logic synthesis for both look up table (LUT) and multiplexor based architectures with a balanced presentation of existing techniques together with algorithms and the system developed by the authors. Audience: A useful reference for VLSI designers, developers of computer aided design tools and anyone involved in or with FPGAs.

VLSI Design Environments George Zobrist, 2000-04-17 *VLSI Design Environments* investigates design alternatives such as object oriented data modelling. The difficulty of automating chip architecture designs is caused by the complexity of the problem. The explosion of design decisions make a heuristic approach necessary. *PLAYOUT* aims at the solution of system problems based on hierarchy, top down planning, silicon compiler presentations, advances in encoding logic synthesis and a microarchitecture and logic optimization system. *PLAYOUT* supports the physical design from entering the structure of digital systems to the generation of the mask. The concept for autonomous tools with a clear interface to the network description and the simple interface to the graphics is presented. This enables the designer to have a great influence on the configuration of the placement of the schematic diagram. Substantial progress is being made in behavioural and logic synthesis, both of which depend upon specifications.

Switching and Finite Automata Theory Zvi Kohavi, Niraj K. Jha, 2010 Understand the structure, behavior and limitations of logic machines with this thoroughly updated third edition. Many new topics are included such as CMOS gates, logic synthesis, logic design for emerging nanotechnologies, digital system testing and asynchronous circuit design to bring students up to speed with modern developments. The intuitive examples and minimal formalism of the previous edition are retained, giving students a text that is logical and easy to follow yet rigorous. Kohavi and Jha begin with the basics and then cover combinational logic design and testing before moving on to more advanced topics in finite state machine design and testing. Theory is made easier to understand with 200 illustrative examples and students can test their understanding with over 350 end of chapter review questions. *NBS Special Publication*, 1968 Computer Literature Bibliography: 1964-1967 W. W. Youden, 1965 **Introduction to Switching Circuit Theory** Donald D. Givone, 1970

Behavioral Synthesis for Hardware Security Srinivas Katkoori, Sheikh Ariful Islam, 2022-02-08 This book presents state of the art research results from leading electronic design automation (EDA) researchers on automated approaches for generating cyber secure smart hardware. The authors first provide brief background on high level synthesis principles and motivate the need for secure design during behavioral synthesis. Then they provide readers with synthesis techniques for six

automated security solutions namely hardware obfuscation hardware Trojan detection IP watermarking state encoding side channel attack resistance and information flow tracking Provides a single source reference to behavioral synthesis for hardware security Describes automatic synthesis techniques for algorithmic obfuscation using code transformations Includes behavioral synthesis techniques for intellectual property protection *Choice* ,1967 *Library Journal* ,1965-10 **The Publishers Weekly** ,1966 Scientific and Technical Books in Print ,1972

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