

# Numerical Analysis of Compound Semiconductor RF Devices

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## Abstract

*An overview of heterostructure RF device simulation for industrial application based on III-V compound semiconductors has been given in [1]. Here, we present the most recent achievements in numerical simulation for industrial heterostructure devices, together with relevant industrial applications (GaAs, InP, and SiGe HBTs).*

## INTRODUCTION

To cope with explosive development costs and strong competition in the semiconductor industry today, Technology Computer-Aided Design (TCAD) methodologies are extensively used in development and production. Several questions during device fabrication, such as performance optimization and process control, can be addressed by simulation. The choice of a given simulation tool or a combination of tools depends to a large extent on the complexity of the particular task, on the desired accuracy of the problem solution, and on the available human, computer, and time resources.

Optimization of geometry, doping, materials, and material compositions targets high output power, high breakdown voltage, high speed (high  $f_T$  and  $f_{max}$ ), low leakage, low noise, and low power consumption. This is a challenging task that can be significantly supported by device simulation. While DC simulation is sufficient for optimization of breakdown voltages, turn-on voltages, or leakage currents, AC simulation is required for speed, noise, and power issues.

There are several challenges which are specific for modeling and simulation of heterostructure devices [2]. The characterization of the physical properties of III-V and SiGe compounds is required for wide ranges of material compositions, temperatures, doping concentrations, etc. The model parameters must be verified against several independent HEMT and HBT technologies to obtain a concise set used for all simulations.

For example, the database for novel materials, such as the GaN or the GaSb systems, which have entered the III-V world with impressive device results, is still relatively poor. Modeling of stress-induced changes of the physical properties of strained material layers and consideration of piezoelectrical effects is a subject of ongoing research [2]. Heterointerface modeling is a key issue for devices which in-

clude abrupt junctions. Thermionic emission, field emission, and tunneling effects critically determine the current transport, especially in double heterojunction bipolar transistors (DHBTs).

Advanced device simulation allows a precise physics-based extraction of small-signal parameters [2]. Measured bias-dependent S-parameters serve as a valuable source of information when compared at different bias points to simulated S-parameters from a device simulator. By simulating in the frequency domain, important small-signal figures of merit, such as the cut-off frequency  $f_T$  and the maximum oscillation frequency  $f_{max}$  can be efficiently extracted [3]. On the other hand, non-linear periodic steady-state analysis can be performed in the time domain to obtain large-signal figure-of-merit parameters, such as distortion, power, frequency, noise, etc. [4] as well in the context of coupled device and circuit simulation.

## HETEROSTRUCTURE DEVICE SIMULATORS

The continuously increasing computational power of computer systems allows the use of TCAD tools on a very large scale. Several commercial device simulators (such as [5]-[10]) company-developed simulators (such as [11]-[13]), and university-developed simulators (like [14]-[19]) have been successfully employed for device engineering applications. These simulators differ considerably in dimensionality (one, quasi-two, two, quasi-three, or three), in choice of carrier transport model (drift-diffusion, energy-transport, or Monte Carlo statistical solution of the Boltzmann transport equation), and in the capability of including electrothermal effects. The drift-diffusion transport model [20] is by now the most popular model used for device simulation. With down-scaling of the feature sizes, non-local effects become more pronounced and must be accounted for by applying an energy-transport model or a hydrodynamic transport model [21]. During the last two decades, Monte Carlo methods for solving the Boltzmann transport equation have been developed [22] and applied for device simulation [23, 24]. However, reduction of computational resources is still an issue, and therefore Monte Carlo device simulation is still not feasible for industrial application on daily basis. An approach to preserve accuracy at lower computational cost is to calibrate lower order transport parameters to Monte Carlo simulation data.

# Numerical Analysis For Semiconductor Devices

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