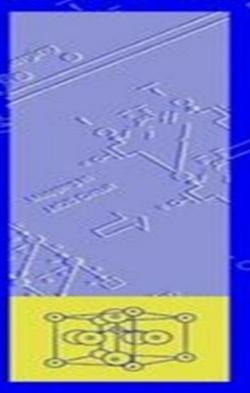
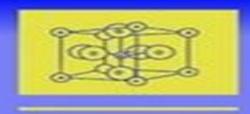
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Kuo, Jea-Hong Lou, 2004-09-24 Low voltage very large scale integration VLSI circuits represent the electronics of the future All electronic products are striving to reduce power consumption to create more economical efficient and compact devices Despite the inevitable trend towards low voltage few books address the technology needed Geared to the needs of engineers and designers in the field this comprehensive volume presents a remarkably detailed analysis of one of today s hottest and most compelling research techniques for VLSI systems An Instructor's Manual presenting detailed solutions to all the problems in the book is available from the Wiley editorial department Low-Power CMOS VLSI Circuit Design Kaushik Roy, Sharat Prasad, 2000-02-22 A comprehensive look at the rapidly growing field of low power VLSIdesign Low power VLSI circuit design is a dynamic research area driven by the growing reliance on battery powered portable computing andwireless communications products In addition it has become critical to the continued progress of high performance and reliable microelectronic systems. This self contained volume clearly introduces each topic incorporates dozens of illustrations and concludes chapters with summaries and references VLSI circuit and CAD engineers as well as researchers in universities and industry will find ample information on tools and techniques for design and optimization of low power electronic systems Topics include Fundamentals of power dissipation in microelectronic evices Estimation of power dissipation due to switching short circuit subthreshold leakage and diode leakage currents Design and test of low voltage CMOS circuits Power conscious logic and high level synthesis Low power static RAM architecture Energy recovery techniques Software power estimation and optimization Low-Voltage CMOS VLSI Circuits James B. Kuo, Jea-Hong Lou, 1999 Geared to the needs of engineers and designers in the field this unique volume presents a remarkably detailed analysis of one of the hottest and most compelling research topics in microelectronics today namely low voltage CMOS VLSI circuit techniques for VLSI systems It features complete guidelines to diversified low voltage and low power circuit techniques emphasizing the role of submicron and CMOS processing technology and device modeling in the circuit designs of low voltage CMOS VLSI CMOS VLSI Engineering James B. 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