



Low-Voltage, Low-Power Digital BiCMOS Circuits

Circuit Design, Comparative Study and Sensitivity Analysis

- BiCMOS design for today's fastest-growing applications
- Design concepts, in-depth analyses, and new methodologies
- Choosing the best BiCMOS circuit for your application
- Predicting and managing circuit performance sensitivities

SAMIR S. ROFAIL & KIAT-SENG YEO

Low Voltage Low Power Digital Bicmos Circuits Circuit Design Comparative Study And Sensitivity

Christopher T. Robertson



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Low-voltage, Low-power Digital BiCMOS Circuits Samir S. Rofail, Kiat Seng Yeo, 2000 PLEASE PROVIDE COURSE INFORMATION PLEASE PROVIDE **Signal Integrity Issues and Printed Circuit Board Design** Douglas Brooks, 2003 Complicated concepts explained succinctly and in laymen's terms to both experienced and novice PCB designers Numerous examples allow reader to visualize how high end software simulators see various types of SI problems and then their solutions Author is a frequent and recognized seminar leader in the industry **Essential Electronic Design Automation (EDA)** Mark Birnbaum, 2004 Describes the engineering needs addressed by the individual EDA tools and covers EDA from both the provider and user viewpoints Learn the importance of marketing and business trends in the EDA industry The EDA consortium is made up of major corporations including SUN HP and Intel Printed Circuit Board Designer's Reference Christopher T. Robertson, 2004 PCB design instruction and reference manual all in one book with in depth explanation of the processes and tools used in modern PCB design Standards formulas definitions and procedures plus software to tie it all together *High-speed Signal Propagation* Howard W. Johnson, Martin Graham, 2003 This advanced level reference presents a complete and unified theory of signal propagation for all metallic media from cables to pcb traces to chips It includes numerous examples pictures tables and wide ranging discussion of the high speed properties of transmission lines *Signal Integrity* Eric Bogatin, 2004 This thorough review of the fundamental principles associated with signal integrity provides engineering principles behind signal integrity effects and applies this understanding to solving problems From ASICs to SOCs Farzad Nekoogar, Faranak Nekoogar, 2003 From ASICs to SOCs A Practical Approach by Farzad Nekoogar and Faranak Nekoogar covers the techniques principles and everyday realities of designing ASICs and SOCs Material includes current issues in the field front end and back end designs integration of IPs on SOC designs and low power design techniques and methodologies Appropriate for practicing chip designers as well as graduate students in electrical engineering

CMOS/BiCMOS ULSI Kiat Seng Yeo, Samir S. Rofail, Wang-Ling Goh, 2002 For upper level and graduate level Electrical and Computer Engineering courses in Integrated Circuit Design as well as professional circuit designers engineers and researchers working in portable wireless communications hardware This book presents the fundamentals of Complementary Metal Oxide Semiconductor CMOS and Bipolar compatible Complementary Metal Oxide Semiconductor BiCMOS technology as well as the latest technological advances in the field It discusses the concepts and techniques of new integrated circuit design for building high performance and low power circuits and systems for current and future very large scale integration VLSI and giga scale integration GSI applications CMOS BiCMOS ULSI Low Voltage Low Power is an essential resource for every professional moving toward lower voltage lower power and higher performance VLSI circuits and subsystems design

High-Performance Digital VLSI Circuit Design Richard X. Gu, Khaled M. Sharaf, Mohamed I. Elmasry, 2012-12-06 High Performance Digital VLSI Circuit Design is the first book devoted entirely to the design of digital high performance VLSI

circuits CMOS BiCMOS and bipolar circuits are covered in depth including state of the art circuit structures Recent advances in both the computer and telecommunications industries demand high performance VLSI digital circuits Digital processing of signals demands high speed circuit techniques for the GHz range The design of such circuits represents a great challenge one that is amplified when the power supply is scaled down to 3.3 V Moreover the requirements of low power high performance circuits adds an extra dimension to the design of such circuits High Performance Digital VLSI Circuit Design is a self contained text introducing the subject of high performance VLSI circuit design and explaining the speed power tradeoffs The first few chapters of the book discuss the necessary background material in the area of device design and device modeling respectively High performance CMOS circuits are then covered especially the new all N logic dynamic circuits Propagation delay times of high speed bipolar CML and ECL are developed analytically to give a thorough understanding of various interacting process device and circuit parameters High current phenomena of bipolar devices are also addressed as these devices typically operate at maximum currents for limited device area Different new high performance BiCMOS circuits are presented and compared to their conventional counterparts These new circuits find direct applications in the areas of high speed adders frequency dividers sense amplifiers level shifters input output clock buffers and PLLs The book concludes with a few system application examples of digital high performance VLSI circuits Audience A vital reference for practicing IC designers Can be used as a text for graduate and senior undergraduate students in the area

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