POWER OPTIMIZATION AND SYNTHESIS AT BEHAVIORAL AND SYSTEM LEVELS USING FORMAL METHODS

Jui-Ming Chang
Massoud Pedram

Power Optimization And Synthesis At Behavioral And System Levels Using Formal Methods

Ying-Ying Zheng

Power Optimization And Synthesis At Behavioral And System Levels Using Formal Methods:
    Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods Jui-Ming Chang,Massoud Pedram,2012-12-06 Integrated circuit densities and operating speeds continue to rise at an exponential rate Chips however cannot get larger and faster without a sharp decrease in power consumption beyond the current levels Minimization of power consumption in VLSI chips has thus become an important design objective In fact with the explosive growth in demand for portable electronics and the usual push toward more complex functionality and higher performance power consumption has in many cases become the limiting factor in satisfying the market demand A new generation of power conscious CAD tools are coming onto the market to help designers estimate optimize and verify power consumption levels at most stages of the IC design process These tools are especially prevalent at the register transfer level and below There is a great need for similar tools and capabilities at the behavioral and system levels of the design process Many researchers and CAD tool developers are working on high level power modeling and estimation as well as power constrained high level synthesis and optimization Techniques and tools alone are however insufficient to optimize VLSI circuit power dissipation a consistent and convergent design methodology is also required Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods was written to address some of the key problems in power analysis and optimization early in the design process In particular this book focuses on power macro modeling based on regression analysis and power minimization through behavioral transformations scheduling resource assignment and hardware software partitioning and mapping What differentiates this book from other published work on the subject is the mathematical basis and formalism behind the algorithms and the optimality of these algorithms subject to the stated assumptions From the Foreword This book makes animportant contribution to the field of system design technologies by presenting a set of algorithms with guaranteed optimality properties that can be readily applied to system level design This contribution is timely because it fills the need of new methods for a new design tool generation which supports the design of electronic systems with even more demanding requirements Giovanni De Micheli Professor Stanford University   Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods Jui-Ming Chang,Massoud Pedram,1999-06-30 Integrated circuit densities and operating speeds continue to rise at an exponential rate Chips however cannot get larger and faster without a sharp decrease in power consumption beyond the current levels Minimization of power consumption in VLSI chips has thus become an important design objective In fact with the explosive growth in demand for portable electronics and the usual push toward more complex functionality and higher performance power consumption has in many cases become the limiting factor in satisfying the market demand A new generation of power conscious CAD tools are coming onto the market to help designers estimate optimize and verify power consumption levels at most stages of the IC design process These tools are especially prevalent at the register transfer level and below There is a great need for similar tools and capabilities at the behavioral and

system levels of the design process Many researchers and CAD tool developers are working on high level power modeling and estimation as well as power constrained high level synthesis and optimization Techniques and tools alone are however insufficient to optimize VLSI circuit power dissipation a consistent and convergent design methodology is also required Power Optimization and Synthesis at Behavioral and System Levels Using Formal Methods was written to address some of the key problems in power analysis and optimization early in the design process In particular this book focuses on power macro modeling based on regression analysis and power minimization through behavioral transformations scheduling resource assignment and hardware software partitioning and mapping What differentiates this book from other published work on the subject is the mathematical basis and formalism behind the algorithms and the optimality of these algorithms subject to the stated assumptions From the Foreword This book makes an important contribution to the field of system design technologies by presenting a set of algorithms with guaranteed optimality properties that can be readily applied to system level design This contribution is timely because it fills the need of new methods for a new design tool generation which supports the design of electronic systems with even more demanding requirements Giovanni De Micheli Professor Stanford University **Formal Methods for Behavioral and System Level Power Optimization and Synthesis** Jui-ming Chang,1998 *Low Power Design with High-Level Power Estimation and Power-Aware Synthesis* Sumit Ahuja,Avinash Lakshminarayana,Sandeep Kumar Shukla,2011-10-22 This book presents novel research techniques algorithms methodologies and experimental results for high level power estimation and power aware high level synthesis Readers will learn to apply such techniques to enable design flows resulting in shorter time to market and successful low power ASIC FPGA design *Low-Power High-Level Synthesis for Nanoscale CMOS Circuits* Saraju P. Mohanty,Nagarajan Ranganathan,Elias Kougianos,Priyardarsan Patra,2008-05-31 Low Power High Level Synthesis for Nanoscale CMOS Circuits addresses the need for analysis characterization estimation and optimization of the various forms of power dissipation in the presence of process variations of nano CMOS technologies The authors show very large scale integration VLSI researchers and engineers how to minimize the different types of power consumption of digital circuits The material deals primarily with high level architectural or behavioral energy dissipation because the behavioral level is not as highly abstracted as the system level nor is it as complex as the gate transistor level At the behavioral level there is a balanced degree of freedom to explore power reduction mechanisms the power reduction opportunities are greater and it can cost effectively help in investigating lower power design alternatives prior to actual circuit layout or silicon implementation The book is a self contained low power high level synthesis text for Nanoscale VLSI design engineers and researchers Each chapter has simple relevant examples for a better grasp of the principles presented Several algorithms are given to provide a better understanding of the underlying concepts The initial chapters deal with the basics of high level synthesis power dissipation mechanisms and power estimation In subsequent parts of the text a detailed discussion of methodologies for the reduction of

different types of power is presented including Power Reduction Fundamentals Energy or Average Power Reduction Peak Power Reduction Transient Power Reduction Leakage Power Reduction Low Power High Level Synthesis for Nanoscale CMOS Circuits provides a valuable resource for the design of low power CMOS circuits *Low Power Hardware Synthesis from Concurrent Action-Oriented Specifications* Gaurav Singh,Sandeep Kumar Shukla,2010-07-23 Human lives are getting increasingly entangled with technology especially comp ing and electronics At each step we take especially in a developing world we are dependent on various gadgets such as cell phones handheld PDAs netbooks me cal prosthetic devices and medical measurement devices e g blood pressure m itors glucometers Two important design constraints for such consumer electronics are their form factor and battery life This translates to the requirements of reduction in the die area and reduced power consumption for the semiconductor chips that go inside these gadgets Performance is also important as increasingly sophisticated applications run on these devices and many of them require fast response time The form factor of such electronics goods depends not only on the overall area of the chips inside them but also on the packaging which depends on thermal ch acteristics Thermal characteristics in turn depend on peak power signature of the chips As a result while the overall energy usage reduction increases battery life peak power reduction in uences the form factor One more important aspect of these electronic equipments is that every 6 months or so a newer feature needs to be added to keep ahead of the market competition and hence new designs have to be completed with these new features better form factor battery life and performance every few months This extreme pressure on the time to market is another force that drives the innovations in design automation of semiconductor chips **Design and Control of Power Converters 2019** Manuel Arias,2021-07-02 In this book 20 papers focused on different fields of power electronics are gathered Approximately half of the papers are focused on different control issues and techniques ranging from the computer aided design of digital compensators to more specific approaches such as fuzzy or sliding control techniques The rest of the papers are focused on the design of novel topologies The fields in which these controls and topologies are applied are varied MMCs photovoltaic systems supercapacitors and traction systems LEDs wireless power transfer etc *DCIS2002* Salvador Bracho del Pino,Mar Martínez,Teresa Riesgo,Miguel Ángel Allende Recio,2002 Este libro contiene las presentaciones de la XVII Conferencia de Dise o de Circuitos y Sistemas Integrados celebrado en el Palacio de la Magdalena Santander en noviembre de 2002 Esta Conferencia ha alcanzado un alto nivel de calidad como consecuencia de su tradici n y madurez que lo convierte en uno de los acontecimientos m s importantes para los circuitos de microelectr nica y la comunidad de dise o de sistemas en el sur de Europa Desde su origen tiene una gran contribuci n de Universidades espa olas aunque hoy los autores participan desde catorce pa ses *Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation* Jorge Juan Chico,Enrico Macii,2003-09-03 This book constitutes the refereed proceedings of the 13th International Workshop on Power and Timing Modeling Optimization and Simulation PATMOS 2003 held in Torino Italy in September 2003 The 43

revised full papers and 18 revised poster papers presented together with three keynote contributions were carefully reviewed and selected from 85 submissions The papers are organized in topical sections on gate level modeling and characterization interconnect modeling and optimization asynchronous techniques RTL power modeling and memory optimization high level modeling power efficient technologies and designs communication modeling and design and low power issues in processors and multimedia **Springer Handbook of Automation** Shimon Y. Nof,2009-07-16 Automation is undergoing a major transformation in scope and dimension and plays an increasingly important role in the global economy and in our daily lives Engineers combine automated devices with mathematical and organizational tools to create complex systems for a rapidly expanding range of applications and human activities This handbook incorporates these new developments and presents a widespread and well structured conglomeration of new emerging application areas of automation Besides manufacturing as a primary application of automation the handbook contains new application areas such as medical systems and health transportation security and maintenance service construction and retail as well as production or logistics This Springer Handbook is not only an ideal resource for automation experts but also for people new to this expanding field such as engineers medical doctors computer scientists designers It is edited by an internationally renowned and experienced expert *Integrated Circuit and System Design* Enrico Macii,Vassilis Paliouras,Odysseas Koufopavlou,2004-09-07 This book constitutes the refereed proceedings of the 14th International Workshop on Power and Timing Optimization and Simulation PATMOS 2004 held in Santorini Greece in September 2004 The 85 revised papers presented together with abstracts of 6 invited presentations were carefully reviewed and selected from 152 papers submitted The papers are organized in topical sections on buses and communication circuits and devices low power issues architectures asynchronous circuits systems design interconnect and physical design security and safety low power processing digital design and modeling and simulation **Formal Methods in VLSI System Design** Adnan Aziz,1996 *Modeling, Verification and Exploration of Task-Level Concurrency in Real-Time Embedded Systems* Filip Thoen,Francky Catthoor,2012-12-06 system is a complex object containing a significant percentage of elec A tronics that interacts with the Real World physical environments humans etc through sensing and actuating devices A system is heterogeneous i e is characterized by the co existence of a large number of components of disparate type and function for example programmable components such as micro processors and Digital Signal Processors DSPs analog components such as AID and D A converters sensors transmitters and receivers Any approach to system design today must include software concerns to be viable In fact it is now common knowledge that more than 70% of the development cost for complex systems such as automotive electronics and communication systems are due to software development In addition this percentage is increasing constantly It has been my take for years that the so called hardware software co design problem is formulated at a too low level to yield significant results in shorten ing design time to the point needed for next generation electronic devices

and systems The level of abstraction has to be raised to the Architecture Function co design problem where Function refers to the operations that the system is supposed to carry out and Architecture is the set of supporting components for that functionality The supporting components as we said above are heteroge neous and contain almost always programmable components   The VLSI Handbook Wai-Kai Chen,2019-07-17 Over the years the fundamentals of VLSI technology have evolved to include a wide range of topics and a broad range of practices To encompass such a vast amount of knowledge The VLSI Handbook focuses on the key concepts models and equations that enable the electrical engineer to analyze design and predict the behavior of very large scale integrated circuits It provides the most up to date information on IC technology you can find Using frequent examples the Handbook stresses the fundamental theory behind professional applications Focusing not only on the traditional design methods it contains all relevant sources of information and tools to assist you in performing your job This includes software databases standards seminars conferences and more The VLSI Handbook answers all your needs in one comprehensive volume at a level that will enlighten and refresh the knowledge of experienced engineers and educate the novice This one source reference keeps you current on new techniques and procedures and serves as a review for standard practice It will be your first choice when looking for a solution   **Formal Methods for Open Object-Based Distributed Systems** Marcello M. Bonsangue,Einar Broch Johnsen,2007-06-27 This book constitutes the refereed proceedings of the 9th IFIP WG 6 1 International Conference on Formal Methods for Open Object Based Distributed Systems FMOODS 2007 held in Paphos Cyprus June 2007 The 17 revised full papers presented together with two invited papers cover model checking rewriting logic components and services algebraic calculi specification verification and refinement and quality of service   **Neural Networks for Instrumentation, Measurement and Related Industrial Applications** Sergey Ablameyko,2003 This work aims to disseminate theoretical and practical knowledge about neural networks in measurement instrumentation and the related industrial applications It also creates a consciousness about the effectiveness of these techniques as well as the measurement problems in industrial environments   Formal Methods – The Next 30 Years Maurice H. ter Beek,Annabelle McIver,José N. Oliveira,2019-09-23 This book constitutes the refereed proceedings of the 23rd Symposium on Formal Methods FM 2019 held in Porto Portugal in the form of the Third World Congress on Formal Methods in October 2019 The 44 full papers presented together with 3 invited presentations were carefully reviewed and selected from 129 submissions The papers are organized in topical sections named Invited Presentations Verification Synthesis Techniques Concurrency Model Checking Circus Model Checking Analysis Techniques Specification Languages Reasoning Techniques Modelling Languages Learning Based Techniques and Applications Refactoring and Reprogramming I Day Presentations   Leveraging Applications of Formal Methods, Verification and Validation: Discussion, Dissemination, Applications Tiziana Margaria,Bernhard Steffen,2016-10-04 The two volume set LNCS 9952 and LNCS 9953 constitutes the refereed proceedings of the 7th International Symposium on Leveraging Applications of Formal Methods Verification and

Validation ISoLA 2016 held in Imperial Corfu Greece in October 2016 The papers presented in this volume were carefully reviewed and selected for inclusion in the proceedings Featuring a track introduction to each section the papers are organized in topical sections named statistical model checking evaluation and reproducibility of program analysis and verification ModSyn PP modular synthesis of programs and processes semantic heterogeneity in the formal development of complex systems static and runtime verification competitors or friends rigorous engineering of collective adaptive systems correctness by construction and post hoc verification friends or foes privacy and security issues in information systems towards a unified view of modeling and programming formal methods and safety certification challenges in the railways domain RVE runtime verification and enforcement the industrial application perspective variability modeling for scalable software evolution detecting and understanding software doping learning systems machine learning in software products and learning based analysis of software systems testing the internet of things doctoral symposium industrial track RERS challenge and STRESS **Dissertation Abstracts International** ,2009 <u>Semantic Hyper/Multimedia Adaptation</u> Ioannis E. Anagnostopoulos,Mária Bieliková,Phivos Mylonas,Nicolas Tsapatsoulis,2012-07-28 Nowadays more and more users are witnessing the impact of Hypermedia Multimedia as well as the penetration of social applications in their life Parallel to the evolution of the Internet and Web several Hypermedia Multimedia schemes and technologies bring semantic based intelligent personalized and adaptive services to the end users More and more techniques are applied in media systems in order to be user group centric adapting to different content and context features of a single or a community user In respect to all the above researchers need to explore and study the plethora of challenges that emergent personalisation and adaptation technologies bring to the new era This edited volume aims to increase the awareness of researchers in this area All contributions provide an in depth investigation on research and deployment issues regarding already introduced schemes and applications in Semantic Hyper Multimedia and Social Media Adaptation Moreover the authors provide survey based articles so as potential readers can use it for catching up the recent trends and applications in respect to the relevant literature Finally the authors discuss and present their approach in the respective field or problem addressed

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