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Christian G. Meyer

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Role Microscopy In Semiconductor Failure Analysis B. P. Richards, P. K. Footner, 1992-06-15 Microscopy is central to the vast majority of semiconductor failure analyses and is therefore of great importance to engineers concerned with design validation process optimization component qualification testing and pre or post use diagnostics A wide range of microscopical techniques is available and each has a unique and complementary role to play in determining the causes of semiconductor failure The applications of microscopy to semiconductor failure analysis are described in this concise handbook which provides a valuable practical guide for all those working in the field The basic principles and operation of each type of microscopy are explained and each is illustrated with case histories and micrographs of many failure mechanisms. The need for new microscopies for the study of future generation devices is discussed and several possible candidates for this purpose are assessed The Role of Microscopy in Semiconductor Failure Analysis B. P. Richards, P. K. Footner, 1992 Microelectronics Failure Analysis, 2004-01-01 For newcomers cast into the waters to sink or swim as well as seasoned professionals who want authoritative guidance desk side this hefty volume updates the previous 1999 edition It contains the work of expert contributors who rallied to the job in response to a committee s call for help the committee was assigned to the update by the Electron Microelectronic Failure Analysis Richard J. Ross, Christian Boit, Donald Staab, 1999 Forty seven papers on electronics failure analysis provide an overview for newcomers to the field and a reference tool for the experienced analyst Topics include electron ion bean based techniques deprocessing and sample preparation and physical chemical defect characterization For the fourth ed *ULSI Semiconductor Technology* Atlas Chih-Hang Tung, George T. T. Sheng, Chih-Yuan Lu, 2003-10-06 More than 1 100 TEM images illustrate the science of ULSI The natural outgrowth of VLSI Very Large Scale Integration Ultra Large Scale Integration ULSI refers to semiconductor chips with more than 10 million devices per chip Written by three renowned pioneers in their field ULSI Semiconductor Technology Atlas uses examples and TEM Transmission Electron Microscopy micrographs to explain and illustrate ULSI process technologies and their associated problems The first book available on the subject to be illustrated using TEM images ULSI Semiconductor Technology Atlas is logically divided into four parts Part I includes basic introductions to the ULSI process device construction analysis and TEM sample preparation Part II focuses on key ULSI modules ion implantation and defects dielectrics and isolation structures silicides salicides and metallization Part III examines integrated devices including complete planar DRAM stacked cell DRAM and trench cell DRAM as well as SRAM as examples for process integration and development Part IV emphasizes special applications including TEM in advanced failure analysis TEM in advanced packaging development and UBM Under Bump Metallization studies and high resolution TEM in microelectronics. This innovative guide also provides engineers and managers in the microelectronics industry as well as graduate students with More than 1 100 TEM images to illustrate the science of ULSI A historical introduction to the

technology as well as coverage of the evolution of basic ULSI process problems and issues Discussion of TEM in other advanced microelectronics devices and materials such as flash memories SOI SiGe devices MEMS and CD ROMs

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