Selected Reprints on Data Flow and Reduction Architectures

Thakkar, S.S.

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Peter M. Kuhn

Selected Reprints On Dataflow And Reduction Architectures:

   Selected Reprints on Dataflow and Reduction Architectures S. S. Thakkar,1987   The Organization of Reduction, Data Flow, and Control Flow Systems Werner Kluge,1992 In light of research over the last decade on new ways of representing and performing computations this book provides a timely reexamination of computer organization and computer architecture It systematically investigates the basic organizational concepts of reduction data flow and control flow or state transition and their relationship to the underlying programming paradigms For each of these concepts Kluge looks at how princip1es of language organization translate into architectures and how architectural features translate into concrete system implementations comparing them in order to identify their similarities and differences The focus is primarily on a functional programming paradigm based on a full fledged operational calculus and on its realization by various reduction systems Kluge first presents a brief outline of the overall configuration of a computing system and of an operating system kernel introduce elements of the theory of Petrinets as modeling tools for nonsequential systems and processes and use a simple form of higher order Petri nets to identify by means of examples the operational and control disciplines that govern the organization of reduction data flow and control flow computations He then introduces the notions of abstract algorithms and of reductions and includes an overview of the theory of the calculus The next five chapters describe the various computing engines that realize the reduction semantics of a full fledged calculus The remaining chapters provide self contained investigations of the G machine SKI combinator reduction and the data flow approach for implementing the functional programming paradigm This is followed by a detailed description of a typical control flow or von Neumann machine architecture a VAX11 system Properties of these machines are summarized in the concluding chapter which classifies them according to the semantic models they support   **VLSI for Artificial Intelligence and Neural Networks** Jose G. Delgado-Frias,W.R. Moore,2012-12-06 This book is an edited selection of the papers presented at the International Workshop on VLSI for Artifidal Intelligence and Neural Networks which was held at the University of Oxford in September 1990 Our thanks go to all the contributors and especially to the programme committee for all their hard work Thanks are also due to the ACM SIGARCH the IEEE Computer Society and the lEE for publicizing the event and to the University of Oxford and SUNY Binghamton for their active support We are particularly grateful to Anna Morris Maureen Doherty and Laura Duffy for coping with the administrative problems Jose Delgado Frias Will Moore April 1991 vii PROLOGUE Artificial intelligence and neural network algorithms computing have increased in complexity as well as in the number of applications This in tum has posed a tremendous need for a larger computational power than can be provided by conventional scalar processors which are oriented towards numeric and data manipulations Due to the artificial intelligence requirements symbolic manipulation knowledge representation non deterministic computations and dynamic resource allocation and neural network computing approach non programming and learning a different set of constraints and demands are imposed

on the computer architectures for these applications **Proceedings of the 1993 International Conference on Parallel Processing** Salim Hariri,P. Bruce Berra,1993-08-16 This three volume work presents a compendium of current and seminal papers on parallel distributed processing offered at the 22nd International Conference on Parallel Processing held August 16 20 1993 in Chicago Illinois Topics include processor architectures mapping algorithms to parallel systems performance evaluations fault diagnosis recovery and tolerance cube networks portable software synchronization compilers hypercube computing and image processing and graphics Computer professionals in parallel processing distributed systems and software engineering will find this book essential to complete their computer reference library **Foundations of Parallel Programming** D. B. Skillicorn,1994-12 The major reason for the lack of use of parallel computing is the mismatch between the complexity and variety of parallel hardware and the software development tools to program it The cost of the developing software needs to be amortised over decades but the platforms on which it executes change every few years requiring complete rewrites The evident cost effectiveness of parallel computation has not been realized because of this mismatch This book presents an integrated approach to parallel software development that addresses software issues and performance issues together It presents a methodology for software construction that produces software that is architecture independent and intellectually abstract The software can execute efficiently on a range of existing and potential hardware configurations The approach is based on the construction of categorical data types a generalisation of abstract data types and of objects Categorical data types abstract both from the representation of a data type and also from the detailed control flow necessary to perform operations on it They thus impose a strong separation between the semantics on which programs can depend and the implementation which is therefore free to hide the parallel machine properties that are used **Multidimensional Programming** Edward A. Ashcroft,1995 This book describes a powerful language for multidimensional declarative programming called Lucid Lucid has evolved considerably in the past ten years The main catalyst for this metamorphosis was the discovery that Lucid is based on intensional logic one commonly used in studying natural languages Intensionality and more specifically indexicality has enabled Lucid to implicitly express multidimensional objects that change a fundamental capability with several consequences which are explored in this book The author covers a broad range of topics from foundations to applications and from implementations to implications The role of intensional logic in Lucid as well as its consequences for programming in general is discussed The syntax and mathematical semantics of the language are given and its ability to be used as a formal system for transformation and verification is presented The use of Lucid in both multidimensional applications programming and software systems construction such as a parallel programming system and a visual programming system is described A novel model of multidimensional computation education is described along with its serendipitous practical benefits for harnessing parallelism and tolerating faults As the only volume that reflects the advances over the past decade this work will be of great interest to researchers and advanced students involved with declarative

language systems and programming **Conceptual Structures: Standards and Practices** William M. Tepfenhart,Walling Cyre,2007-07-23 With all of the news about the Internet and the Y2K problem it is easy to forget that other areas of computer science still exist Reading the newspaper or watching the television conveys a very warped view of what is happening in computer science This conference illustrates how a maturing subdiscipline of computer science can continue to grow and integrate within it both old and new approaches despite or perhaps due to a lack of public awareness The conceptual graph community has basically existed since the 1984 publication of John Sowa s book Conceptual Structures Information Processing In Mind and Machine In this book John Sowa laid the foundations for a knowledge representation model called conceptual graphs based on semantic networks and the existential graphs of C S Peirce Conceptual graphs constitutes a very powerful and expressive knowledge representation scheme inheriting the benefits of logic and the mathematics of graphs The expressiveness and formal underpinnings of conceptual graph theory have attracted a large international community of researchers and scholars The International Conferences on Conceptual Structures and this is the seventh in the series is the primary forum for these researchers to report their progress and activities As in the past the doors were open to admit alternate representation models and approaches Advances in Computing and Information - ICCI '91 Frank Dehne,F. Fiala,1991-05-07 This volume contains papers presented at the Third International Conference on Computing and Information ICCI 91 held at Carleton University in Ottawa Canada May 27 29 1991 The conference was organized by the School of Computer Science at Carleton University and was sponsored by the Natural Sciences and Engineering Research Council of Canada NSERC and Carleton University ICCI 91 was an international forum for the presentation of original results in research development and applications in computing and information processing The conference was aimed at both practitioners and theoreticians and was organized into five streams Algorithms and complexity Databases and information systems Parallel processing and systems Distributed computing and systems Expert systems artificial intelligence This volume contains three invited papers by E C R Hehner R L Probert and S J Smith and 71 selected papers **TAPSOFT '89. Proceedings of the International Joint Conference on Theory and Practice of Software Development Barcelona, Spain, March 13-17, 1989** Josep Diaz,Fernando Orejas,1989-02-27 TAPSOFT 89 is the Third International Joint Conference on Theory and Practice of Software Development held in Barcelona Spain March 13 17 1989 The conference consissted of three parts Advanced Seminar on Foundations of Innovative Software Development Colloquium on Trees in Algebra and Programming CAAP 89 Colloquium on Current Issues in Programming Languages CCIPL The TAPSOFT 89 Conference Proceedings are published in two volumes The first volume includes the papers from CAAP plus the more theoretical ones of the invited papers The second volume comprises the papers from CCIPL and the invited papers more relevant to current issues in programming languages Parallel Architectures and Compilation Techniques Michel Cosnard,Guang R. Gao,Gabriel M. Silberman,1994 Fine and medium grain parallelism continues to hold its own as a vital vibrant research topic

Within the area new developments in superscalar and VLIW architectures and their associated compilation techniques have provided exciting new avenues to extract performance from a slowing technology curve Comprising 28 full length papers and a number of short poster papers this publication offers a high quality exploration of the current state of the art in the field It will be of particular interest to those involved in control structures and microprogramming processor architectures computer system implementation programming techniques and software engineering **Types and Programming Languages** Benjamin C. Pierce,2002-01-04 A comprehensive introduction to type systems and programming languages A type system is a syntactic method for automatically checking the absence of certain erroneous behaviors by classifying program phrases according to the kinds of values they compute The study of type systems and of programming languages from a type theoretic perspective has important applications in software engineering language design high performance compilers and security This text provides a comprehensive introduction both to type systems in computer science and to the basic theory of programming languages The approach is pragmatic and operational each new concept is motivated by programming examples and the more theoretical sections are driven by the needs of implementations Each chapter is accompanied by numerous exercises and solutions as well as a running implementation available via the Web Dependencies between chapters are explicitly identified allowing readers to choose a variety of paths through the material The core topics include the untyped lambda calculus simple type systems type reconstruction universal and existential polymorphism subtyping bounded quantification recursive types kinds and type operators Extended case studies develop a variety of approaches to modeling the features of object oriented languages **Reduced Instruction Set Computers** William Stallings,1990 Euro-Par'98 Parallel Processing David Pritchard,1998-08-19 Proceedings Parallel Computing Computer Architecture '97 Ronald Pose,1997-04-01 The Australasian Computer Architecture Conferences were established in recognition of the fundamental reliance of all computer systems on the integrity capability and performance of the underlying computer architecture ACAC 97 held in February 1997 in Sydney Australia was the second in this series and attracted authors from Singapore Hong Kong Korea Sweden New Zealand Spain China the United States of America and Australia This volume contains a selection of 21 research papers the best and most interesting of those presented at the conference highlighting technologies to increase processor performance asynchronous processor designs interconnection networks and routing and parallel and distributed multiprocessor systems **Introduction to Parallel Algorithms** C. Xavier,S. S. Iyengar,1998-08-05 Parallel algorithms Made Easy The complexity of today s applications coupled with the widespread use of parallel computing has made the design and analysis of parallel algorithms topics of growing interest This volume fills a need in the field for an introductory treatment of parallel algorithms appropriate even at the undergraduate level where no other textbooks on the subject exist It features a systematic approach to the latest design techniques providing analysis and implementation details for each parallel algorithm described in the book Introduction to Parallel Algorithms covers foundations of parallel computing parallel

algorithms for trees and graphs parallel algorithms for sorting searching and merging and numerical algorithms This remarkable book Presents basic concepts in clear and simple terms Incorporates numerous examples to enhance students understanding Shows how to develop parallel algorithms for all classical problems in computer science mathematics and engineering Employs extensive illustrations of new design techniques Discusses parallel algorithms in the context of PRAM model Includes end of chapter exercises and detailed references on parallel computing This book enables universities to offer parallel algorithm courses at the senior undergraduate level in computer science and engineering It is also an invaluable text reference for graduate students scientists and engineers in computer science mathematics and engineering *Algorithms, Complexity Analysis and VLSI Architectures for MPEG-4 Motion Estimation* Peter M. Kuhn,2013-06-29 MPEG 4 is the multimedia standard for combining interactivity natural and synthetic digital video audio and computer graphics Typical applications are internet video conferencing mobile videophones multimedia cooperative work teleteaching and games With MPEG 4 the next step from block based video ISO IEC MPEG 1 MPEG 2 CCITT H 261 ITU T H 263 to arbitrarily shaped visual objects is taken This significant step demands a new methodology for system analysis and design to meet the considerably higher flexibility of MPEG 4 Motion estimation is a central part of MPEG 1 2 4 and H 261 H 263 video compression standards and has attracted much attention in research and industry for the following reasons it is computationally the most demanding algorithm of a video encoder about 60 80% of the total computation time it has a high impact on the visual quality of a video encoder and it is not standardized thus being open to competition Algorithms Complexity Analysis and VLSI Architectures for MPEG 4 Motion Estimation covers in detail every single step in the design of a MPEG 1 2 4 or H 261 H 263 compliant video encoder Fast motion estimation algorithms Complexity analysis tools Detailed complexity analysis of a software implementation of MPEG 4 video Complexity and visual quality analysis of fast motion estimation algorithms within MPEG 4 Design space on motion estimation VLSI architectures Detailed VLSI design examples of 1 a high throughput and 2 a low power MPEG 4 motion estimator Algorithms Complexity Analysis and VLSI Architectures for MPEG 4 Motion Estimation is an important introduction to numerous algorithmic architectural and system design aspects of the multimedia standard MPEG 4 As such all researchers students and practitioners working in image processing video coding or system and VLSI design will find this book of interest **Energy Efficiency and Robustness of Advanced Machine Learning Architectures** Alberto Marchisio,Muhammad Shafique,2024-11-14 Machine Learning ML algorithms have shown a high level of accuracy and applications are widely used in many systems and platforms However developing efficient ML based systems requires addressing three problems energy efficiency robustness and techniques that typically focus on optimizing for a single objective have a limited set of goals This book tackles these challenges by exploiting the unique features of advanced ML models and investigates cross layer concepts and techniques to engage both hardware and software level methods to build robust and energy efficient architectures for these advanced ML networks More specifically

this book improves the energy efficiency of complex models like CapsNets through a specialized flow of hardware level designs and software level optimizations exploiting the application driven knowledge of these systems and the error tolerance through approximations and quantization This book also improves the robustness of ML models in particular for SNNs executed on neuromorphic hardware due to their inherent cost effective features This book integrates multiple optimization objectives into specialized frameworks for jointly optimizing the robustness and energy efficiency of these systems This is an important resource for students and researchers of computer and electrical engineering who are interested in developing energy efficient and robust ML *Computer Architecture Technology Trends* Architecture Technology Corpor,2013-10-22 Please note this is a Short Discount publication This year s edition of Computer Architecture Technology Trends analyses the trends which are taking place in the architecture of computing systems today Due to the sheer number of different applications to which computers are being applied there seems no end to the different adoptions which proliferate There are however some underlying trends which appear Decision makers should be aware of these trends when specifying architectures particularly for future applications This report is fully revised and updated and provides insight into the fundamentals of computer architecture what it is and how it is applied to fit a particular problem definition Also discussed is where the future leads given current trends in computer architecture Proceedings Institute of Electrical and Electronics Engineers,1993 Papers of the meeting held in Gran Canaria Spain 27 29 January 1993 on architecture logic programming massively parallel processing algorithms communications methodologies tools environments simulation fault tolerance No index Acidic paper Annotation copyright Book News Inc Portland **Paperbound Books in Print** ,1991

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