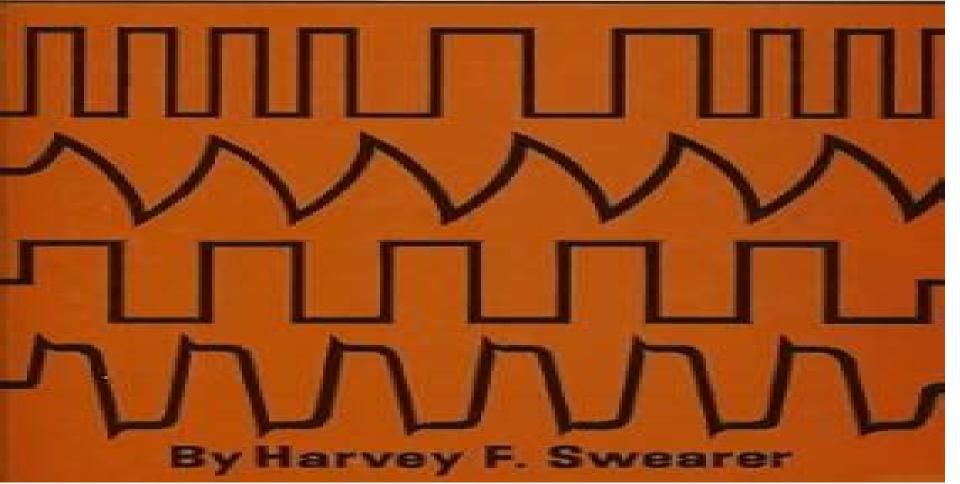
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PULSE & SWITCHING SWITCHING CIRCUITS



Pulse And Switching Circuits

Joseph Mayo Pettit

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self study the book is useful even for AMIE and grad IETE students Written in a student friendly style the book provides an excellent introduction to digital concepts and basic design techniques of digital circuits It discusses Boolean algebra concepts and their application to digital circuitry and elaborates on both combinational and sequential circuits It provides numerous fully worked out laboratory tested examples to give students a solid grounding in the related design concepts It includes a number of short questions with answers review questions fill in the blanks with answers objective type questions with answers and exercise problems at the end of each chapter TARGET AUDIENCE B Sc Electronic Science B E B Tech Electrical Electronics Computer Science and Engineering Information Technology etc MCA Polytechnic M Sc Physics M Sc **Electronic Science** <u>Technical Manual United States</u>. War Department,1951 Basic Theory and Application of Transistors United States. Department of the Army, 1959 **NBS Special Publication** ,1965 Pulse and Switching Circuits [by] Donald J. Ketchum [and] E. Charles Alvarez Donald J. Ketchum, 1964 SWITCHING THEORY AND LOGIC DESIGN, Third Edition KUMAR, A. ANAND, 2016-07-18 This comprehensive text on switching theory and logic design is designed for the undergraduate students of electronics and communication engineering electrical and electronics engineering electronics and computers engineering electronics and instrumentation engineering telecommunication engineering computer science and engineering and information technology It will also be useful to M Sc electronics M Sc computers AMIE IETE and diploma students Written in a student friendly style this book now in its Third Edition provides an in depth knowledge of switching theory and the design techniques of digital circuits Striking a balance between theory and practice it covers topics ranging from number systems binary codes logic gates and Boolean algebra to minimization using K maps and tabular method design of combinational logic circuits synchronous and asynchronous sequential circuits and algorithmic state machines The book discusses threshold gates and programmable logic devices PLDs In addition it elaborates on flip flops and shift registers Each chapter includes several fully worked out examples so that the students get a thorough grounding in related design concepts Short questions with answers review questions fill in the blanks multiple choice questions and problems are provided at the end of each chapter These help the students test their level of understanding of the subject and prepare for examinations confidently NEW TO THIS EDITION VERILOG programs at the National Bureau of Standards Miscellaneous Publication .1965 end of each chapter

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