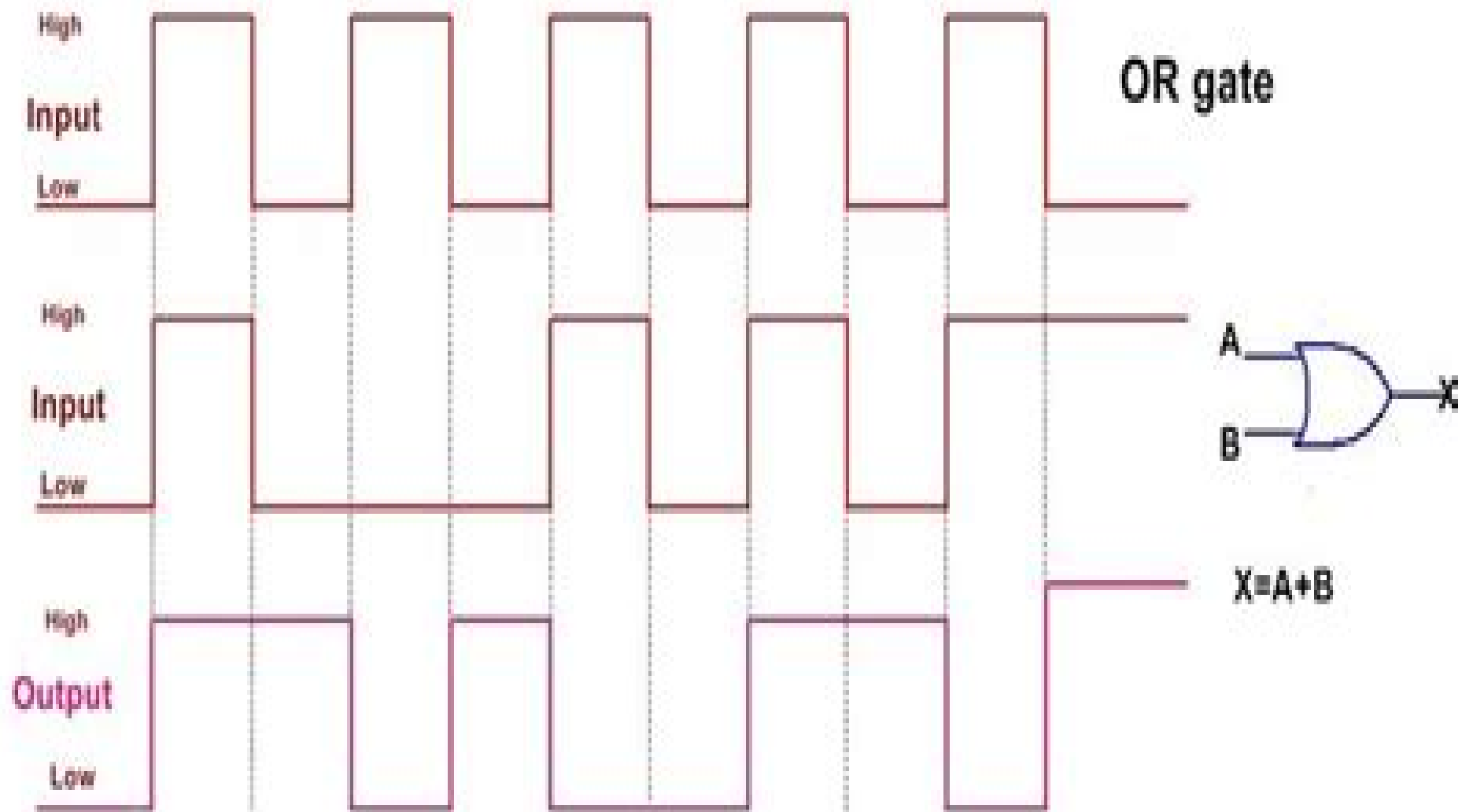




Pulse And Logic Circuits

Richard C. Dorf



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Kannan, Mrs. M. Saraswathi, Mr. C. Rameshkumar, PREFACE OF THE BOOK This book is extensively designed for the third semester EEE EIE students as per Anna university syllabus R 2013. The following chapters constitute the following units:
Chapter 1 & 9 covers Unit 1
Chapter 2 and 3 covers Unit 2
Chapter 4 and 5 covers Unit 3
Chapter 6 and 7 covers Unit 4
Chapter 8 VHDL Unit 5
CHAPTER 1 Introduces the Number System binary arithmetic and codes
CHAPTER 2 Deals with Boolean algebra simplification using Boolean theorems K map method Quine McCluskey method logic gates implementation of switching function using basic Logical Gates and Universal Gates
CHAPTER 3 Describes the combinational circuits like Adder Subtractor Multiplier Divider magnitude comparator encoder decoder code converters Multiplexer and Demultiplexer
CHAPTER 4 Describes with Latches Flip Flops Registers and Counters
CHAPTER 5 Concentrates on the Analysis as well as design of synchronous sequential circuits Design of synchronous counters sequence generator and Sequence detector
CHAPTER 6 Concentrates the Design as well as Analysis of Fundamental Mode circuits Pulse mode Circuits Hazard Free Circuits ASM Chart and Design of Asynchronous counters
CHAPTER 7 Discussion on memory devices which includes ROM RAM PLA PAL Sequential logic devices and ASIC
CHAPTER 8 The chapter concentrates on the design fundamental building blocks Data types operates subprograms packages compilation process used for VHDL. It discusses on Finite state machine as an important tool for designing logic level state machines. The chapter also discusses register transfer level designing and test benches usage in stimulation of the state logic machines
CHAPTER 9 Concentrate on the comparison operation and characteristics of RTL DTL TTL ECL and MOS families. We have taken enough care to present the definitions and statements of basic laws and theorems problems with simple steps to make the students familiar with the fundamentals of Digital Design.

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