RECONFIGURABLE COMPUTING

ACCELERATING COMPUTATION WITH FIELD-PROGRAMMABLE GATE ARRAYS

By

Maya Gokhale and Paul S. Graham

Reconfigurable Computing Accelerating Computation With Field Programmable Gate Arrays

Roberto Giorgi,Mirjana Stojilović,Dirk Stroobandt,Piedad Brox Jiménez,Ángel Barriga Barros

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Reconfigurable Computing Maya B. Gokhale,Paul S. Graham,2006-07-04 A one of a kind survey of the field of Reconfigurable Computing Gives a comprehensive introduction to a discipline that offers a 10X 100X acceleration of algorithms over microprocessors Discusses the impact of reconfigurable hardware on a wide range of applications signal and image processing network security bioinformatics and supercomputing Includes the history of the field as well as recent advances Includes an extensive bibliography of primary sources   **Reconfigurable Computing** Maya B. Gokhale,Paul S. Graham,2008-11-01 A one of a kind survey of the field of Reconfigurable Computing Gives a comprehensive introduction to a discipline that offers a 10X 100X acceleration of algorithms over microprocessors Discusses the impact of reconfigurable hardware on a wide range of applications signal and image processing network security bioinformatics and supercomputing Includes the history of the field as well as recent advances Includes an extensive bibliography of primary sources

Reconfigurable Computing Joao Cardoso,Michael Hübner,2011-08-17 As the complexity of modern embedded systems increases it becomes less practical to design monolithic processing platforms As a result reconfigurable computing is being adopted widely for more flexible design Reconfigurable Computers offer the spatial parallelism and fine grained customizability of application specific circuits with the postfabrication programmability of software To make the most of this unique combination of performance and flexibility designers need to be aware of both hardware and software issues FPGA users must think not only about the gates needed to perform a computation but also about the software flow that supports the design process The goal of this book is to help designers become comfortable with these issues and thus be able to exploit the vast opportunities possible with reconfigurable logic   **Reconfigurable Computing: Architectures, Tools, and Applications** Roger Woods,Katherine Compton,Christos Bourganis,Pedro C. Diniz,2008-08-29 Coverage in this proceedings volume includes DNA and string processing applications reconfigurable computing hardware and systems image processing run time behavior instruction set extension as well as random number generation and financial computation

Reconfigurable Computing: Architectures, Tools and Applications Pedro C. Diniz,2007-03-19 This book constitutes the refereed proceedings of the Third International Workshop on Applied Reconfigurable Computing ARC 2007 held in Mangaratiba Brazil in March 2007 The 27 full papers and 10 short papers presented together with a late comer contribution from ARC 2006 are organized in topical sections on architectures mapping techniques and tools arithmetic and applications

Reconfigurable Computing: Architectures, Tools and Applications Andreas Koch,Ram Krishnamurthy,John McAllister,Roger Woods,Tarek El-Ghazawi,2011-03-15 This book constitutes the refereed proceedings of the 7th International Symposium on Reconfigurable Computing Architectures Tools and Applications ARC 2011 held in Belfast UK in March 2011 The 40 revised papers presented consisting of 24 full papers 14 poster papers and the abstracts of 2 plenary talks were carefully reviewed and selected from 88 submissions The topics covered are reconfigurable accelerators design

tools reconfigurable processors applications device architecture methodology and simulation and system architecture

Handbook of Parallel Computing Sanguthevar Rajasekaran,John Reif,2007-12-20 The ability of parallel computing to process large data sets and handle time consuming operations has resulted in unprecedented advances in biological and scientific computing modeling and simulations Exploring these recent developments the Handbook of Parallel Computing Models Algorithms and Applications provides comprehensive coverage on a <u>Reconfigurable Computing Systems Engineering</u> Lev Kirischian,2017-12-19 Reconfigurable Computing Systems Engineering Virtualization of Computing Architecture describes the organization of reconfigurable computing system RCS architecture and discusses the pros and cons of different RCS architecture implementations Providing a solid understanding of RCS technology and where it s most effective this book Details the architecture organization of RCS platforms for application specific workloads Covers the process of the architectural synthesis of hardware components for system on chip SoC for the RCS Explores the virtualization of RCS architecture from the system and on chip levels Presents methodologies for RCS architecture run time integration according to mode of operation and rapid adaptation to changes of multi parametric constraints Includes illustrative examples case studies homework problems and references to important literature A solutions manual is available with qualifying course adoption Reconfigurable Computing Systems Engineering Virtualization of Computing Architecture offers a complete road map to the synthesis of RCS architecture exposing hardware design engineers system architects and students specializing in designing FPGA based embedded systems to novel concepts in RCS architecture organization and virtualization **Reconfigurable Computing** Scott Hauck,André DeHon,2010-07-26 Reconfigurable Computing marks a revolutionary and hot topic that bridges the gap between the separate worlds of hardware and software design the key feature of reconfigurable computing is its groundbreaking ability to perform computations in hardware to increase performance while retaining the flexibility of a software solution Reconfigurable computers serve as affordable fast and accurate tools for developing designs ranging from single chip architectures to multi chip and embedded systems Scott Hauck and Andre DeHon have assembled a group of the key experts in the fields of both hardware and software computing to provide an introduction to the entire range of issues relating to reconfigurable computing FPGAs field programmable gate arrays act as the computing vehicles to implement this powerful technology Readers will be guided into adopting a completely new way of handling existing design concerns and be able to make use of the vast opportunities possible with reconfigurable logic in this rapidly evolving field Designed for both hardware and software programmers Views of reconfigurable programming beyond standard programming languages Broad set of case studies demonstrating how to use FPGAs in novel and efficient ways **Applied Reconfigurable Computing. Architectures, Tools, and Applications** Roberto Giorgi,Mirjana Stojilović,Dirk Stroobandt,Piedad Brox Jiménez,Ángel Barriga Barros,2025-04-03 This book constitutes the proceedings of the 21st International Symposium on Applied Reconfigurable Computing Architectures Tools

and Applications ARC 2025 held in Seville Spain during April 9 11 2025 The 12 full papers presented in this book together with 1 short paper from the technical program were carefully reviewed and selected from 40 submissions ARC 2025 covers a wide range of topics including hardware acceleration security and fault tolerance energy efficient architectures and emerging applications in artificial intelligence and high performance computing The symposium fostered collaboration and pushed the boundaries of state of the art research *Dynamic Reconfigurable Network-on-Chip Design: Innovations for Computational Processing and Communication* Shen, Jih-Sheng,Hsiung, Pao-Ann,2010-06-30 Reconfigurable computing brings immense flexibility to on chip processing while network on chip has improved flexibility in on chip communication Integrating these two areas of research reaps the benefits of both and represents the promising future of multiprocessor systems on chip This book is the one of the first compilations written to demonstrate this future for network on chip design Through dynamic and creative research into questions ranging from integrating reconfigurable computing techniques to task assigning scheduling and arrival to designing an operating system to take advantage of the computing and communication flexibilities brought about by run time reconfiguration and network on chip it represents a complete source of the techniques and applications for reconfigurable network on chip necessary for understanding of future of this field *Embedded Systems* Krzysztof Iniewski,2012-10-26 Covers the significant embedded computing technologies highlighting their applications in wireless communication and computing power An embedded system is a computer system designed for specific control functions within a larger system often with real time computing constraints It is embedded as part of a complete device often including hardware and mechanical parts Presented in three parts Embedded Systems Hardware Design and Implementation provides readers with an immersive introduction to this rapidly growing segment of the computer industry Acknowledging the fact that embedded systems control many of today s most common devices such as smart phones PC tablets as well as hardware embedded in cars TVs and even refrigerators and heating systems the book starts with a basic introduction to embedded computing systems It hones in on system on a chip SoC multiprocessor system on chip MPSoC and network on chip NoC It then covers on chip integration of software and custom hardware accelerators as well as fabric flexibility custom architectures and the multiple I O standards that facilitate PCB integration Next it focuses on the technologies associated with embedded computing systems going over the basics of field programmable gate array FPGA digital signal processing DSP and application specific integrated circuit ASIC technology architectural support for on chip integration of custom accelerators with processors and O S support for these systems Finally it offers full details on architecture testability and computer aided design CAD support for embedded systems soft processors heterogeneous resources and on chip storage before concluding with coverage of software support in particular O S Linux Embedded Systems Hardware Design and Implementation is an ideal book for design engineers looking to optimize and reduce the size and cost of embedded system products and increase their reliability and performance **Reconfigurable System Design**

and Verification Pao-Ann Hsiung,Marco D. Santambrogio,Chun-Hsian Huang,2018-10-08 Reconfigurable systems have pervaded nearly all fields of computation and will continue to do so for the foreseeable future Reconfigurable System Design and Verification provides a compendium of design and verification techniques for reconfigurable systems allowing you to quickly search for a technique and determine if it is appropriate to the task at hand It bridges the gap between the need for reconfigurable computing education and the burgeoning development of numerous different techniques in the design and verification of reconfigurable systems in various application domains The text explains topics in such a way that they can be immediately grasped and put into practice It starts with an overview of reconfigurable computing architectures and platforms and demonstrates how to develop reconfigurable systems This sets up the discussion of the hardware software and system techniques that form the core of the text The authors classify design and verification techniques into primary and secondary categories allowing the appropriate ones to be easily located and compared The techniques discussed range from system modeling and system level design to co simulation and formal verification Case studies illustrating real world applications detailed explanations of complex algorithms and self explaining illustrations add depth to the presentation Comprehensively covering all techniques related to the hardware software design and verification of reconfigurable systems this book provides a single source for information that otherwise would have been dispersed among the literature making it very difficult to search compare and select the technique most suitable The authors do it all for you making it easy to find the techniques that fit your system requirements without having to surf the net or digital libraries to find the candidate techniques and compare them yourself   *Parallel Computing Technologies* Victor Malyshkin,2015-07-24 This book constitutes the proceedings of the 13th International Conference on Parallel Computing Technologies PaCT 2015 held in Petrozavodsk Russia during August September 2015 The 37 full papers and 14 short papers presented were carefully reviewed and selected from 87 submissions The papers are organized in topical sections on parallel models algorithms and programming methods unconventional computing cellular automata distributed computing special processors programming techniques applications   **Parallel and Distributed Computing** Alberto Ros,2010-01-01 The 14 chapters presented in this book cover a wide variety of representative works ranging from hardware design to application development Particularly the topics that are addressed are programmable and reconfigurable devices and systems dependability of GPUs General Purpose Units network topologies cache coherence protocols resource allocation scheduling algorithms peertopeer networks largescale network simulation and parallel routines and algorithms In this way the articles included in this book constitute an excellent reference for engineers and researchers who have particular interests in each of these topics in parallel and distributed computing   **Compilation Techniques for Reconfigurable Architectures** João M.P. Cardoso,Pedro C. Diniz,2011-04-02 The extreme exibility of recon gurable architectures and their performance pot tial have made them a vehicle of choice in a wide range of computing domains from rapid circuit prototyping to high performance computing The

increasing availab ity of transistors on a die has allowed the emergence of recon gurable architectures with a large number of computing resources and interconnection topologies To ploit the potential of these recon gurable architectures programmers are forced to map their applications typically written in high level imperative programming l guages such as C or MATLAB to hardware oriented languages such as VHDL or Verilog In this process they must assume the role of hardware designers and software programmers and navigate a maze of program transformations mapping and synthesis steps to produce ef cient recon gurable computing implementations The richness and sophistication of any of these application mapping steps make the mapping of computations to these architectures an increasingly daunting process It is thus widely believed that automatic compilation from high level programming languages is the key to the success of recon gurable computing This book describes a wide range of code transformations and mapping te niques for programs described in high level programming languages most tably imperative languages to recon gurable architectures **Computational Mechanics with Neural Networks** Genki Yagawa,Atsuya Oishi,2021-02-26 This book shows how neural networks are applied to computational mechanics Part I presents the fundamentals of neural networks and other machine learning method in computational mechanics Part II highlights the applications of neural networks to a variety of problems of computational mechanics The final chapter gives perspectives to the applications of the deep learning to computational mechanics

Reconfigurable Computing Scott Hauck,André DeHon,2008 The main characteristic of Reconfigurable Computing is the presence of hardware that can be reconfigured to implement specific functionality more suitable for specially tailored hardware than on a simple uniprocessor Reconfigurable computing systems join microprocessors and programmable hardware in order to take advantage of the combined strengths of hardware and software and have been used in applications ranging from embedded systems to high performance computing Many of the fundamental theories have been identified and used by the Hardware Software Co Design research field Although the same background ideas are shared in both areas they have different goals and use different approaches This book is intended as an introduction to the entire range of issues important to reconfigurable computing using FPGAs as the context or computing vehicles to implement this powerful technology It will take a reader with a background in the basics of digital design and software programming and provide them with the knowledge needed to be an effective designer or researcher in this rapidly evolving field Treatment of FPGAs as computing vehicles rather than glue logic or ASIC substitutes Views of FPGA programming beyond Verilog VHDL Broad set of case studies demonstrating how to use FPGAs in novel and efficient ways **Proceedings of the 5th International Workshop on Reconfigurable Communication-centric Systems on Chip 2010 - ReCoSoC'10** Michael Hübner,2010

Applied Reconfigurable Computing. Architectures, Tools, and Applications Nikolaos Voros,Michael Huebner,Georgios Keramidas,Diana Goehringer,Christos Antonopoulos,Pedro C. Diniz,2018-04-25 This book constitutes the proceedings of the 14th International Conference on Applied Reconfigurable Computing ARC 2018 held in Santorini Greece in May 2018 The 29

full papers and 22 short presented in this volume were carefully reviewed and selected from 78 submissions In addition the volume contains 9 contributions from research projects The papers were organized in topical sections named machine learning and neural networks FPGA based design and CGRA optimizations applications and surveys fault tolerance security and communication architectures reconfigurable and adaptive architectures design methods and fast prototyping FPGA based design and applications and special session research projects

Reviewing **Reconfigurable Computing Accelerating Computation With Field Programmable Gate Arrays**: Unlocking the Spellbinding Force of Linguistics

In a fast-paced world fueled by information and interconnectivity, the spellbinding force of linguistics has acquired newfound prominence. Its capacity to evoke emotions, stimulate contemplation, and stimulate metamorphosis is actually astonishing. Within the pages of "**Reconfigurable Computing Accelerating Computation With Field Programmable Gate Arrays**," an enthralling opus penned by a highly acclaimed wordsmith, readers attempt an immersive expedition to unravel the intricate significance of language and its indelible imprint on our lives. Throughout this assessment, we shall delve into the book is central motifs, appraise its distinctive narrative style, and gauge its overarching influence on the minds of its readers.

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