Reuse in Electronic Design

From Information Modelling to Intellectual Properties

Peter Conradi

Reuse In Electronic Design From Information Modelling To Intellectual Properties

Gang Qu,Miodrag Potkonjak

Reuse In Electronic Design From Information Modelling To Intellectual Properties:

Reuse in Electronic Design Peter Conradi,1999-06-02 Reuse in Electronic Design From Information Modelling to Intellectual Properties Peter Conradi Berlin Germany Reuse of existing IC intellectual property blocks is currently the hot topic in electronic design automation By cutting development time and improving designer productivity reuse offers a faster time to market and consequently increased profitability Conradi encompasses the fundamentals of physical system modelling design methodologies and basic design architectures plus reuse strategies and tasks Features include Graph oriented visualisations enabling the reader to understand the requirements of future interdisciplinary design tools Guidance on information modelling languages system classification and decomposition of systems under development Section on reuse strategies and tasks examining the practical aspects of the technique for both analogue and digital design Data management and retrieval methods including the algebra of the selection process Comprehensive appendices covering fine grained design representations VLSI design standards and data model extraction of a VHDL multiplier design This timely exposition of design capture and manipulation techniques will be a boon for postgraduate electronics students and researchers CAD engineers and electronic designers will profit from the industry examples and the practical applications of the reuse cycle

Understanding and Profiting from Intellectual Property in International Business Deli Yang,2021-04-07 This book covers cross border strategies to understand and profit from intellectual property It starts with a basic overview of IP before focusing specifically on international business contexts The book then explores factors that affect IP related business activities in different countries Next follows a discussion of the importance of managing IP valuation people and products which leads into an examination of strategies for obtaining value from IP related activities including licensing This edition updates the contents and adds new contemporary cases such as internet based crimes and trademarked sport brands Readers will gain an understanding of the significance of IP to corporate success in the increasingly globalized world With updated knowledge on deriving value from IP this book will provide insights for practitioners to deal with cross border issues of IP and for scholars across disciplines to advance studies of cross border issues and conflicts in IP **Understanding and Profiting from Intellectual Property** D. Yang,2012-12-04 A new look at the strategic and managerial issues surrounding intellectual property IP and international commercialization in the international market An updated version which provides practitioners and analysts with guidelines and an action framework on how to benefit from IP **Neural and Fuzzy Logic Control of Drives and Power Systems** Marcian Cirstea,2002-10-08 Introduces cutting edge control systems to a wide readership of engineers and students The first book on neuro fuzzy control systems to take a practical applications based approach backed up with worked examples and case studies Learn to use VHDL in real world applications Introducing cutting edge control systems through real world applications Neural networks and fuzzy logic based systems offer a modern control solution to AC machines used in variable speed drives enabling industry to save costs and increase efficiency by

replacing expensive and high maintenance DC motor systems The use of fast micros has revolutionised the field with sensorless vector control and direct torque control This book reflects recent research findings and acts as a useful guide to the new generation of control systems for a wide readership of advanced undergraduate and graduate students as well as practising engineers The authors guide readers quickly and concisely through the complex topics of neural networks fuzzy logic mathematical modelling of electrical machines power systems control and VHDL design Unlike the academic monographs that have previously been published on each of these subjects this book combines them and is based round case studies of systems analysis control strategies design simulation and implementation The result is a guide to applied control systems design that will appeal equally to students and professional design engineers The book can also be used as a unique VHDL design aid based on real world power engineering applications **Intellectual Property Protection in VLSI Designs** Gang Qu,Miodrag Potkonjak,2007-05-08 Intellectual Property Protection in VLSI Designs Theory and Practice provides an overview of the security problems in modern VLSI design with a detailed treatment of our newly developed constraint based protection paradigm for the protection of VLSI design IPs from FPGA design to standard cell placement from high level synthesis solutions to gate level netlist place and rout and from advanced CAD tools to physical design algorithms The problem of VLSI design IP protection is much more challenging than the protection of multimedia contents or software and our protection paradigm is also conceptually different from the state of the art approaches in those domains Intellectual Property Protection in VLSI Designs Theory and Practice contains the mathematical foundations for the developed IP protection paradigm detailed pseudo code and descriptions of its many techniques numerous examples and experimental validation on well known benchmarks and clear explanations and comparisons of the many protection methods *System-on-Chip Methodologies & Design Languages* Peter J. Ashenden,Jean Mermet,Ralf Seepold,2013-03-14 System on Chip Methodologies the Forum on Design Languages FDL held in Europe and the Asia Pacific Chip Design Language APChDL Conference The papers cover a range of topics including design methods specification and modeling languages tool issues formal verification simulation and synthesis The results presented in these papers will help researchers and practicing engineers keep abreast of developments in this rapidly evolving field System Level Design Model with Reuse of System IP Patrizia Cavalloro,Christophe Gendarme,Klaus Kronlöf,Jean Mermet,J. van Sas,Kari Tiensyrjä,Nikolaos Voros,2007-05-08 This book addresses system design providing a framework for assessing and developing system design practices that observe and utilise reuse of system design know how The know how accumulated in the companies represents an intellectual asset or property IP Reuse Techniques for VLSI Design Ralf Seepold,Arno Kunzmann,2012-12-06 Reuse Techniques for VLSI Design is a reflection on the current state of the art in design reuse for microelectronic systems To that end it is the first book to garner the input of leading experts from both research and application areas These experts document herein not only their more mature approaches but also their latest research results Firstly it sets out the background and support from

international organisations that enforce System on a Chip SoC design by reuse oriented methodologies This overview is followed by a number of technical presentations covering different requirements of the reuse domain These are presented from different points of view i e IP provider IP user designer isolated reuse intra company or inter company reuse More general systems or case studies e g metrics are followed by comprehensive reuse systems e g reuse management systems partly including business models Since design reuse must not be restricted to digital components mixed signal and analog reuse approaches are also presented In parallel to the digital domain this area covers research in reuse database design Design verification and legal aspects are two important topics that are closely related to the realization of design reuse These hot topics are covered by presentations that finalize the survey of outstanding research development and application of design reuse for SoC design Reuse Techniques for VLSI Design is an invaluable reference for researchers and engineers involved in VLSI ASIC design *Intellectual Property for Electronic Systems* Brian Bailey,Kathy Werner,2007 Featuring articles by top experts from such companies as Rambus IBM Hewlett Packard and FreeScale this collection addresses the issues that concern those in the ICT field looking to keep systems safe and secure without sacrificing quality or ease of use This book cogently addresses verification standards handoff and legal issues to create a comprehensive look at one of the most important yet sometimes under appreciated topics in the industry Winning the SoC Revolution Grant Martin,Henry Chang,2012-12-06 In 1998 99 at the dawn of the SoC Revolution we wrote Surviving the SOC Revolution A Guide to Platform Based Design In that book we focused on presenting guidelines and best practices to aid engineers beginning to design complex System on Chip devices SoCs Now in 2003 facing the mid point of that revolution we believe that it is time to focus on winning In this book Winning the SoC Revolution Experiences in Real Design we gather the best practical experiences in how to design SoCs from the most advanced design groups while setting the issues and techniques in the context of SoC design methodologies As an edited volume this book has contributions from the leading design houses who are winning in SoCs Altera ARM IBM Philips TI UC Berkeley and Xilinx These chapters present the many facets of SoC design the platform based approach how to best utilize IP Verification FPGA fabrics as an alternative to ASICs and next generation process technology issues We also include observations from Ron Wilson of CMP Media on best practices for SoC design team collaboration We hope that by utilizing this book you too will win the SoC Revolution *The Electronic Design Automation Handbook* Dirk Jansen,2010-02-23 When I attended college we studied vacuum tubes in our junior year At that time an average radio had ve vacuum tubes and better ones even seven Then transistors appeared in 1960s A good radio was judged to be one with more thententransistors Latergoodradioshad15 20transistors and after that everyone stopped counting transistors Today modern processors runing personal computers have over 10milliontransistorsandmoremillionswillbeaddedevery year The difference between 20 and 20M is in complexity methodology and business models Designs with 20 tr sistors are easily generated by design engineers without any tools whilst designs

with 20M transistors can not be done by humans in reasonable time without the help of Prof Dr Gajski demonstrates the Y chart automation This difference in complexity introduced a paradigm shift which required sophisticated methods and tools and introduced design automation into design practice By the decomposition of the design process into many tasks and abstraction levels the methodology of designing chips or systems has also evolved Similarly the business model has changed from vertical integration in which one company did all the tasks from product speci cation to manufacturing to globally distributed client server production in which most of the design and manufacturing tasks are outsourced

Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar,Georges G. E. Gielen,2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck   *Digital Fingerprinting* Cliff Wang,Ryan M. Gerdes,Yong Guan,Sneha Kumar Kasera,2016-10-25 This is the first book on digital fingerprinting that comprehensively covers the major areas of study in a range of information security areas including authentication schemes intrusion detection forensic analysis and more Available techniques for assurance are limited and authentication schemes are potentially vulnerable to the theft of digital tokens or secrets Intrusion detection can be thwarted by spoofing or impersonating devices and forensic analysis is incapable of demonstrably tying a particular device to specific digital evidence This book presents an innovative and effective approach that addresses these concerns This book introduces the origins and scientific underpinnings of digital fingerprinting It also proposes a unified framework for digital fingerprinting evaluates methodologies and includes examples and case studies The last chapter of this book covers the future directions of digital fingerprinting This book is designed for practitioners and researchers working in the security field and military Advanced level students focused on computer science and engineering will find this book beneficial as secondary

textbook or reference **Digital Design and Fabrication** Vojin G. Oklobdzija,2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user s point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design *Software-Hardware Integration in Automotive Product Development* John Blyler,2013-11-07 Software Hardware Integration in Automotive Product Development brings together a must read set of technical papers on one the most talked about subjects among industry experts The carefully selected content of this book demonstrates how leading companies universities and organizations have developed methodologies tools and technologies to integrate verify and validate hardware and software systems The automotive industry is no different with the future of its product development lying in the timely integration of these chiefly electronic and mechanical systems The integration activities cross both product type and engineering discipline boundaries to include chip embedded board and network vehicle level systems Integration verification and validation of each of these three domains are examined in depth attesting to the difficulties of this phase of the automotive hardware and software system life cycle The current state of the art is to integrate verify validate and test automotive hardware and software with a complement of physical hardware and virtual software prototyping tools The growth of sophisticated software tools sometimes combined with hardware in the loop devices has allowed the automotive industry to meet shrinking time to market decreasing costs and increasing safety demands It is also why most of the papers in this book focus on virtual systems prototypes and models to emulate and simulate both hardware and software Further such tools and techniques are the way that hardware and software systems can be co verified and tested in a concurrent fashion The goal of this compilation of expert articles is to reveal the similarities and differences between the integration verification and validation IVV of hardware and software at the chip board and network levels This comparative study will reveal the common IVV thread among the different but ultimately related implementations of hardware and software systems In so doing it supports the larger systems engineering approach for the vertically integrated automobile namely that of model driven development EDA for IC System Design, Verification, and Testing Louis

Scheffer,Luciano Lavagno,Grant Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set **FPGA-based Implementation of Signal Processing Systems** Roger Woods,John McAllister,Gaye Lightbody,Ying Yi,2017-05-01 An important working resource for engineers and researchers involved in the design development and implementation of signal processing systems The last decade has seen a rapid expansion of the use of field programmable gate arrays FPGAs for a wide range of applications beyond traditional digital signal processing DSP systems Written by a team of experts working at the leading edge of FPGA research and development this second edition of FPGA based Implementation of Signal Processing Systems has been extensively updated and revised to reflect the latest iterations of FPGA theory applications and technology Written from a system level perspective it features expert discussions of contemporary methods and tools used in the design optimization and implementation of DSP systems using programmable FPGA hardware And it provides a wealth of practical insights along with illustrative case studies and timely real world examples of critical concern to engineers working in the design and development of DSP systems for radio telecommunications audio visual and security applications as well as bioinformatics Big Data applications and more Inside you will find up to date coverage of FPGA solutions for Big Data Applications especially as they apply to huge data sets The use of ARM processors in FPGAs and the transfer of FPGAs towards heterogeneous computing platforms The evolution of High Level Synthesis tools including new sections on Xilinx s HLS Vivado tool flow and Altera s OpenCL approach Developments in Graphical Processing Units GPUs which are rapidly replacing more traditional DSP systems FPGA based Implementation of Signal Processing Systems 2nd Edition is an indispensable guide for engineers and researchers involved in the design and development of both traditional and cutting edge data and signal processing systems Senior level electrical and computer engineering graduates studying signal processing or digital signal processing also will find this volume of great interest *Technologies for the Information Society* Jean-Yves Roger,Brian Stanford-Smith,Paul T. Kidd,1998 GATEWAYS TO DEMOCRACY continues with its framework of gateways to help readers conceptualize participation and civic engagement even democracy itself with reference to how individuals access the political system This approach helps readers better see the relevance of government in their lives GATEWAYS uniquely incorporates policy into a section at the end of each chapter helping readers better understand the connection between public opinion policy making and how public policy applies to their lives The second edition complete

with 2012 election updates emphasizes critical thinking by clearly outlining learning outcomes and enhancing learning with self assessment Checkpoints and a clear chapter study plan Chapters in this ESSENTIALS version are condensed to accommodate a shorter format but preserve the integrity of the text s hallmarks   **DCIS2002** Salvador Bracho del Pino,Mar Martínez,Teresa Riesgo,Miguel Ángel Allende Recio,2002 Este libro contiene las presentaciones de la XVII Conferencia de Dise o de Circuitos y Sistemas Integrados celebrado en el Palacio de la Magdalena Santander en noviembre de 2002 Esta Conferencia ha alcanzado un alto nivel de calidad como consecuencia de su tradici n y madurez que lo convierte en uno de los acontecimientos m s importantes para los circuitos de microelectr nica y la comunidad de dise o de sistemas en el sur de Europa Desde su origen tiene una gran contribuci n de Universidades espa olas aunque hoy los autores participan desde catorce pa ses   Multiprocessor Systems-on-Chips Ahmed Jerraya,Wayne Wolf,2005 Modern system on chip SoC design shows a clear trend toward integration of multiple processor cores on a single chip Designing a multiprocessor system on chip MPSOC requires an understanding of the various design styles and techniques used in the multiprocessor Understanding the application area of the MPSOC is also critical to making proper tradeoffs and design decisions Multiprocessor Systems on Chips covers both design techniques and applications for MPSOCs Design topics include multiprocessor architectures processors operating systems compilers methodologies and synthesis algorithms and application areas covered include telecommunications and multimedia The majority of the chapters were collected from presentations made at the International Workshop on Application Specific Multi Processor SoC held over the past two years The workshop assembled internationally recognized speakers on the range of topics relevant to MPSOCs After having refined their material at the workshop the speakers are now writing chapters and the editors are fashioning them into a unified book by making connections between chapters and developing common terminology Examines several different architectures and the constraints imposed on them Discusses scheduling real time operating systems and compilers Analyzes design trade off and decisions in telecommunications and multimedia applications

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