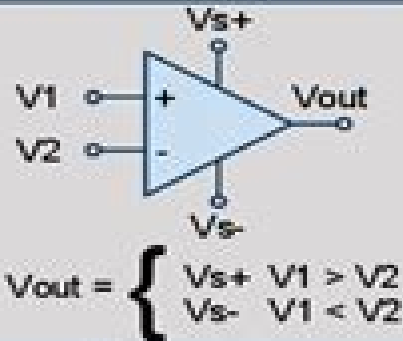
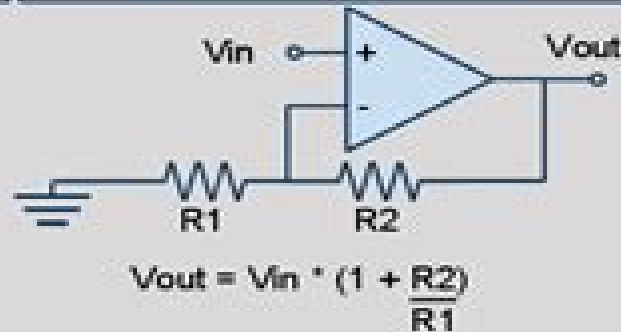


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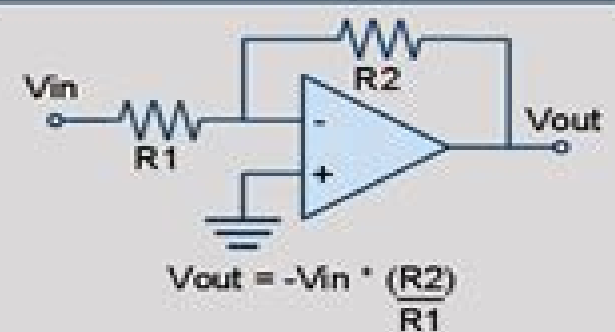
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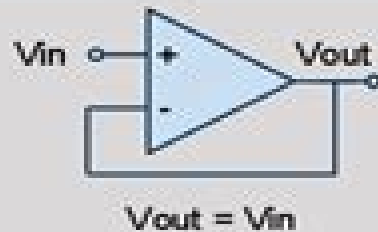
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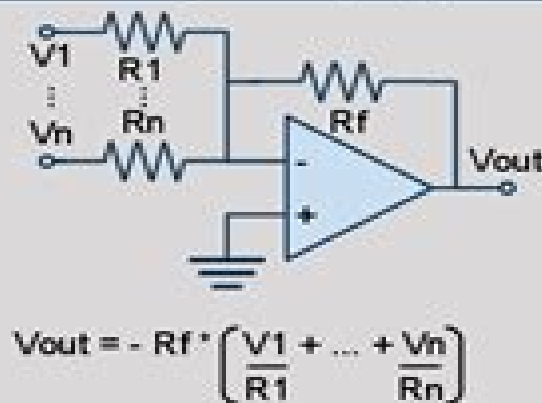
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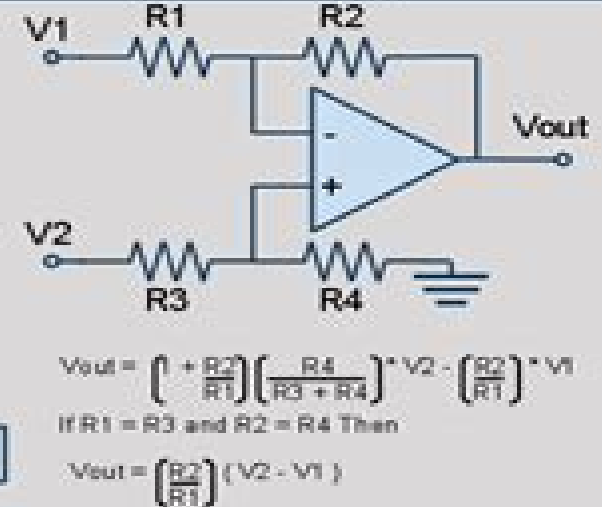
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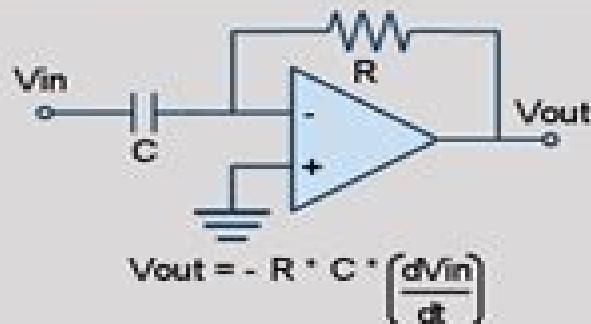
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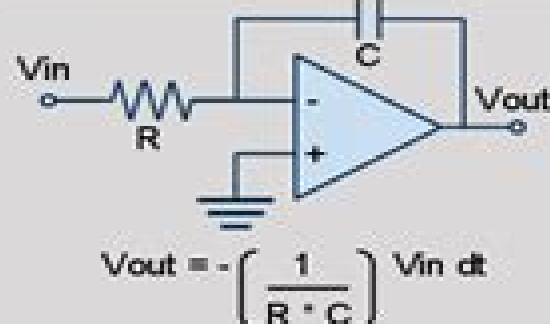
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amp theory and applications Covers circuit board layout techniques for manufacturing op amp circuits *Theory and Design of Linear Active Networks* Sundaram Natarajan,1987 **Design of CMOS Operational Amplifiers** Rasoul Dehghani,2013

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Introduction to operational amplifiers Fundamentals of circuit design using op amps Feedback stability Amplifiers Comparators Converters Demodulators and discriminators Detectors Differential amplifiers Low pass filters High pass filters Bandpass filters Bandstop filters Frequency control Integrators and differentiators Limiters and rectifiers Logarithmic circuits Modulators Oscillators Parameter enhancement and simulation Power circuits Regulators Sampling circuits Time and phase circuits Waveform generators Appendix Operational amplifier parameters Operational amplifier maximum ratings Circuit fabrication techniques Notation used in handbook Decibel calculations RC circuit characteristics **Using Artificial Neural Networks for Analog Integrated Circuit Design Automation** João P. S. Rosa,Daniel J. D. Guerra,Nuno C. G. Horta,Ricardo M. F. Martins,Nuno C. C. Lourenço,2019-12-11

This book addresses the automatic sizing and layout of analog integrated circuits ICs using deep learning DL and artificial neural networks ANN It explores an innovative approach to automatic circuit sizing where ANNs learn patterns from previously optimized design solutions In opposition to classical optimization based sizing strategies where computational intelligence techniques are used to iterate over the map from devices sizes to circuits performances provided by design equations or circuit simulations ANNs are shown to be capable of solving analog IC sizing as a direct map from specifications to the devices sizes Two separate ANN architectures are proposed a Regression only model and a Classification and Regression model The goal of the Regression only model is to learn design patterns from the studied circuits using circuit s performances as input features and devices sizes as target outputs This model can size a circuit given its specifications for a single topology The Classification and Regression model has the same capabilities of the previous model but it can also select the most appropriate circuit topology and its respective sizing given the target specification The proposed methodology was implemented and tested on two analog circuit topologies Circuits, Devices and Systems Ralph J. Smith,Richard C. Dorf,1992-04-16

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